SPEC® CINT2006 Result
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Acer Incorporated

Acer AW2000ht-AW170ht F1 (Intel Xeon X5672, 3.2 GHz)

$SPECint^®\_rate2006 =$ 306

$SPECint\_rate\_base2006 =$ 290
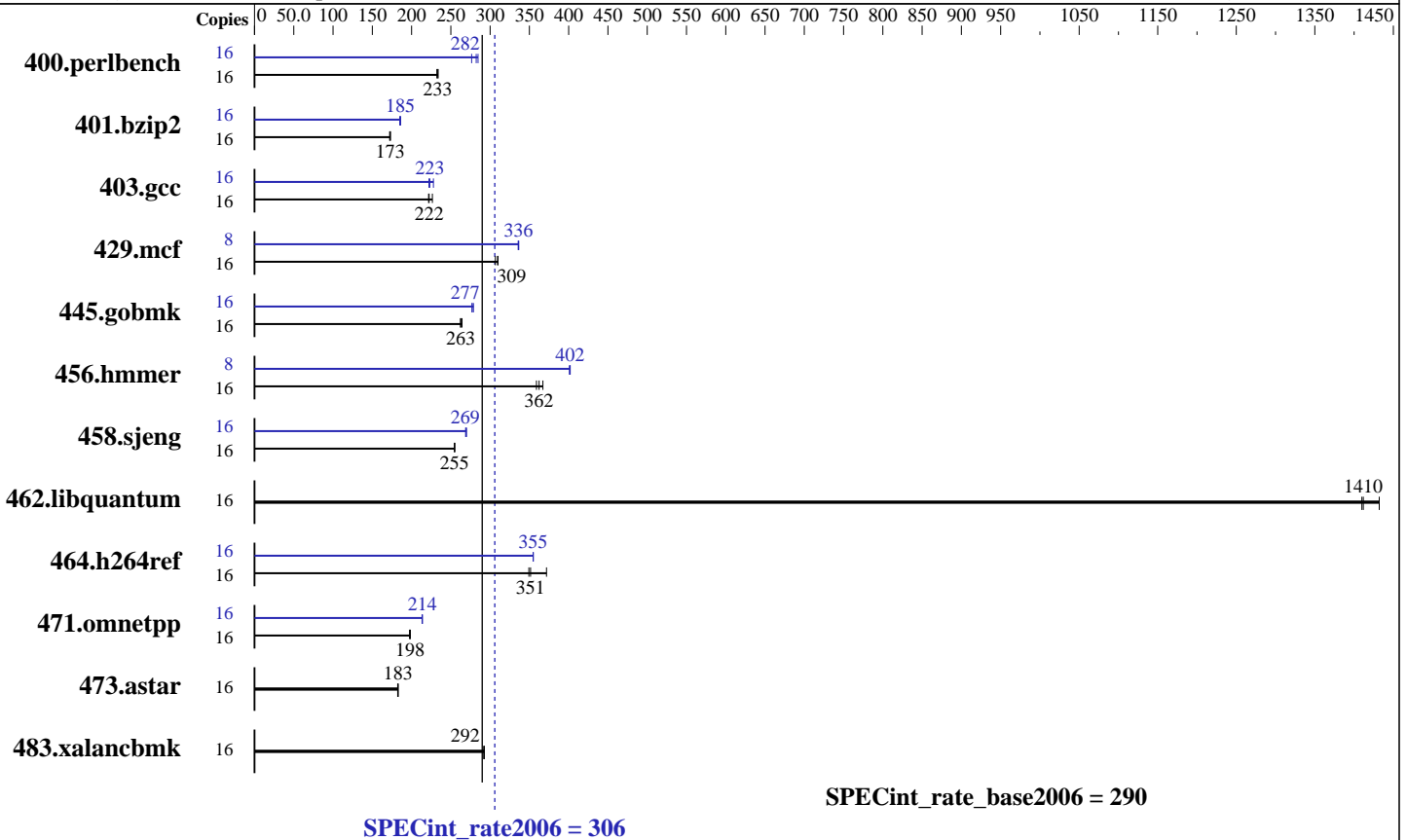
CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Jun-2011
Hardware Availability: Feb-2011
Software Availability: Jan-2011

Copies scale: 0 50.0 100 150 200 250 300 350 400 450 500 550 600 650 700 750 800 850 900 950 1050 1150 1250 1350 1450

- 400.perlbench: 16 / 282; 16 / 233
- 401.bzip2: 16 / 185; 16 / 173
- 403.gcc: 16 / 223; 16 / 222
- 429.mcf: 8 / 336; 16 / 309
- 445.gobmk: 16 / 277; 16 / 263
- 456.hmmer: 8 / 402; 16 / 362
- 458.sjeng: 16 / 269; 16 / 255
- 462.libquantum: 16 / 1410
- 464.h264ref: 16 / 355; 16 / 351
- 471.omnetpp: 16 / 214; 16 / 198
- 473.astar: 16 / 183
- 483.xalancbmk: 16 / 292

SPECint_rate_base2006 = 290

SPECint_rate2006 = 306

Hardware

CPU Name: Intel Xeon X5672
CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz: 3200
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1, 2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB 2Rx8 PC3-10600R-9, ECC)
Disk Subsystem: 1 x 1000 GB SATA 7200RPM
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default
Compiler: Intel C++ Compiler XE for applications running on IA-32
Version 12.0.1.116 Build 20101116
Auto Parallel: No
File System: ReiserFS
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V9.01

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Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	668	234	**671**	**233**	673	232	16	565	276	**554**	**282**	549	285
401.bzip2	16	897	172	892	173	**893**	**173**	16	**832**	**185**	835	185	830	186
403.gcc	16	569	226	**579**	**222**	581	222	16	565	228	**577**	**223**	580	222
429.mcf	16	476	307	471	310	**472**	**309**	8	217	336	217	336	**217**	**336**
445.gobmk	16	635	264	**639**	**263**	640	262	16	606	277	**606**	**277**	602	279
456.hmmer	16	407	367	**412**	**362**	416	359	8	186	401	**186**	**402**	186	402
458.sjeng	16	759	255	**760**	**255**	761	254	16	**719**	**269**	720	269	717	270
462.libquantum	16	**235**	**1410**	235	1410	232	1430	16	**235**	**1410**	235	1410	232	1430
464.h264ref	16	952	372	1014	349	**1007**	**351**	16	998	355	998	355	**998**	**355**
471.omnetpp	16	504	198	507	197	**505**	**198**	16	468	214	469	213	**468**	**214**
473.astar	16	**615**	**183**	616	182	614	183	16	**615**	**183**	616	182	614	183
483.xalancbmk	16	**378**	**292**	377	293	378	292	16	**378**	**292**	377	293	378	292

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
Large pages were disabled for this run
```

Platform Notes

```
BIOS Settings:
Fan speed = full speed (Default = Energy Saving)
Data Reuse = Disabled (Default = Enabled)
```

General Notes

```
Binaries compiled on RHEL5.5
 The Acer AW2000h-AW170h F1, Gateway GW2000h-GW170h F1, Acer AW2000ht-AW170ht F1  and Gateway GW2000ht-GW170ht F1
 are electronically equivalent.  This result was measured on Gateway GW2000ht-GW170ht F1.
```

Base Compiler Invocation

C benchmarks:
```
     icc  -m32
```

Continued on next page

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Base Compiler Invocation (Continued)

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
    -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/smartheap -lsmartheap
    -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

    400.perlbench: icc -m64

    401.bzip2: icc -m64

    456.hmmer: icc -m64

    458.sjeng: icc -m64

C++ benchmarks:
    icpc -m32

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Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
    401.bzip2: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
    458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

    401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-prefetch -auto-ilp32 -ansi-alias
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

     403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
               -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

     429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -auto-ilp32

   445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
               -ansi-alias -auto-ilp32

   456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

    458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll4 -auto-ilp32
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

   464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll2 -ansi-alias
```

C++ benchmarks:

```
   471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
```

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Peak Optimization Flags (Continued)

471.omnetpp (continued):
```
-L/smartheap -lsmartheap
```

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html
http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml
http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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