



SPEC® CFP2006 Result

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Acer Incorporated

SPECfp®_rate2006 = 108

Acer AT110 F2 (Xeon E3-1220)

SPECfp_rate_base2006 = 106

CPU2006 license: 97

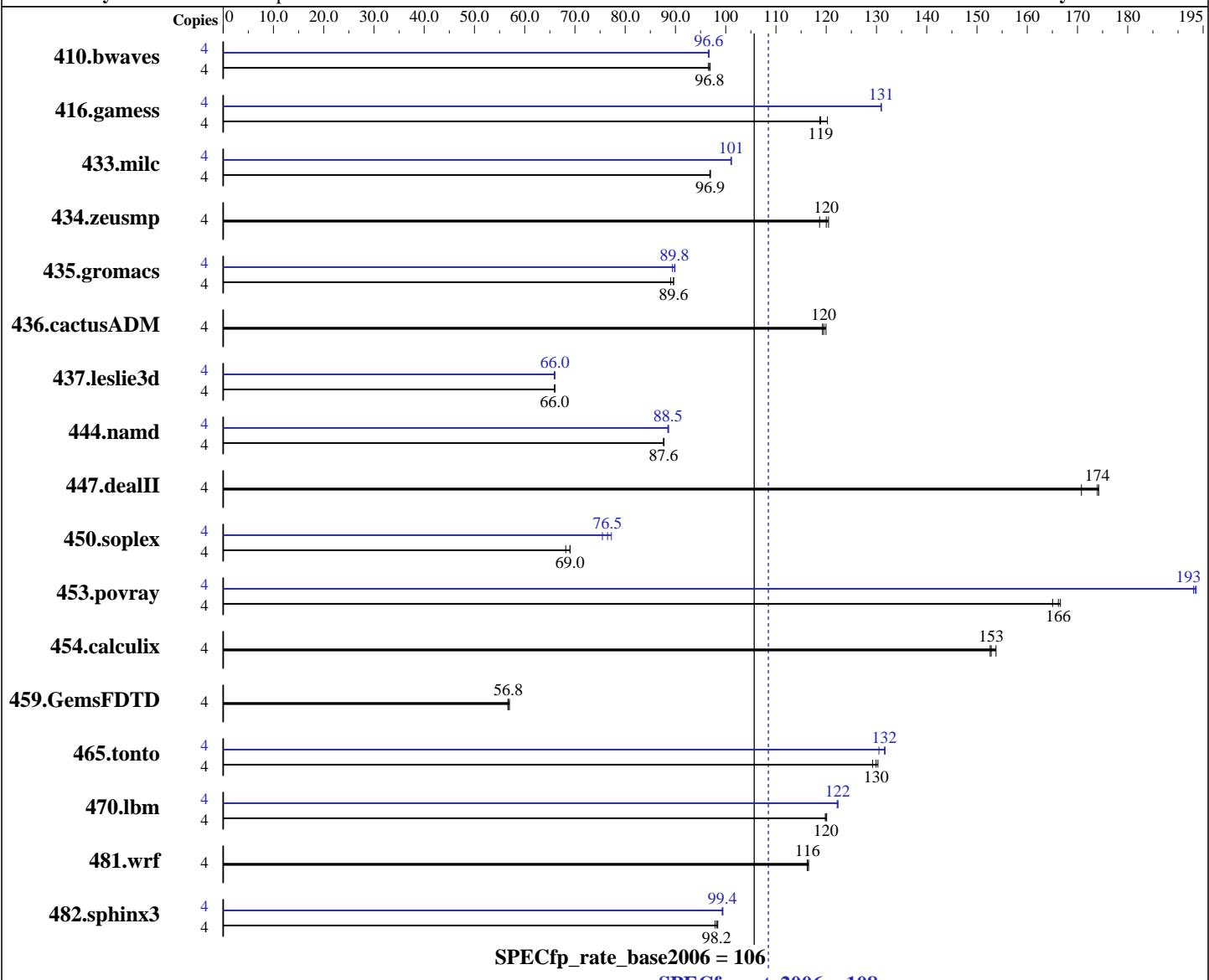
Test date: Apr-2011

Test sponsor: Acer Incorporated

Hardware Availability: Apr-2011

Tested by: Acer Incorporated

Software Availability: Jan-2011



Hardware

CPU Name: Intel Xeon E3-1220
CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz
CPU MHz: 3100
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) SP1 , Kernel 2.6.32.12-0.7-default
Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116
Auto Parallel: No
File System: ext3
System State: Run level 3 (multi-user)
Base Pointers: 32-bit

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L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9,ECC)
 Disk Subsystem: 1 x 640 GB SATA, 7200 RPM
 Other Hardware: None

Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	561	96.9	562	96.8	563	96.5	4	562	96.7	563	96.6	563	96.5
416.gamess	4	651	120	659	119	660	119	4	598	131	598	131	598	131
433.milc	4	379	96.9	379	96.9	379	96.8	4	363	101	363	101	363	101
434.zeusmp	4	307	119	302	120	303	120	4	307	119	302	120	303	120
435.gromacs	4	319	89.6	319	89.6	321	89.1	4	319	89.4	318	89.9	318	89.8
436.cactusADM	4	400	120	398	120	401	119	4	400	120	398	120	401	119
437.leslie3d	4	570	66.0	570	65.9	570	66.0	4	570	66.0	570	66.0	571	65.9
444.namd	4	366	87.7	366	87.6	366	87.6	4	362	88.5	362	88.5	362	88.6
447.dealII	4	263	174	263	174	268	171	4	263	174	263	174	268	171
450.soplex	4	483	69.0	489	68.2	483	69.0	4	442	75.5	436	76.5	432	77.3
453.povray	4	129	165	128	167	128	166	4	110	193	110	194	110	193
454.calculix	4	215	154	216	153	216	153	4	215	154	216	153	216	153
459.GemsFDTD	4	748	56.8	749	56.7	745	57.0	4	748	56.8	749	56.7	745	57.0
465.tonto	4	303	130	302	130	305	129	4	299	132	299	132	302	130
470.lbm	4	458	120	458	120	459	120	4	449	122	450	122	449	122
481.wrf	4	384	116	384	116	384	116	4	384	116	384	116	384	116
482.sphinx3	4	794	98.2	797	97.9	792	98.4	4	785	99.3	784	99.5	785	99.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

numactl was used to bind copies to the cores
 The config file option 'submit' was used

Operating System Notes

Large pages were disabled for this run

General Notes

Binaries compiled on RHEL5.5
 This result was measured on the Gateway GT110 F2.
 The Acer AT110 F2 is electronically equivalent.



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Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -ansi-alias



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-ansi-alias -opt-prefetch -auto-ilp32

Continued on next page



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Peak Optimization Flags (Continued)

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll12

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
 -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
 -B /usr/share/libhugetlbfss/ -Wl,-hugetlbfss-link=BDT

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -ansi-alias
 -B /usr/share/libhugetlbfss/ -Wl,-melf_x86_64 -Wl,-hugetlbfss-link=BDT

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
 -inline-level=0 -scalar-rep -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div
 -B /usr/share/libhugetlbfss/ -Wl,-melf_x86_64 -Wl,-hugetlbfss-link=BDT

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -auto
 -inline-calloc -opt-malloc-options=3
 -B /usr/share/libhugetlbfss/ -Wl,-melf_x86_64 -Wl,-hugetlbfss-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
 -static -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

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Peak Optimization Flags (Continued)

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html>

<http://www.spec.org/cpu2006/flags/Intel-icl2.0-linux64-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml>

<http://www.spec.org/cpu2006/flags/Intel-icl2.0-linux64-revB.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

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