



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Acer Incorporated

SPECint®\_rate2006 = 167

## Gateway GT110 F2 (Xeon E3-1280)

SPECint\_rate\_base2006 = 160

CPU2006 license: 97

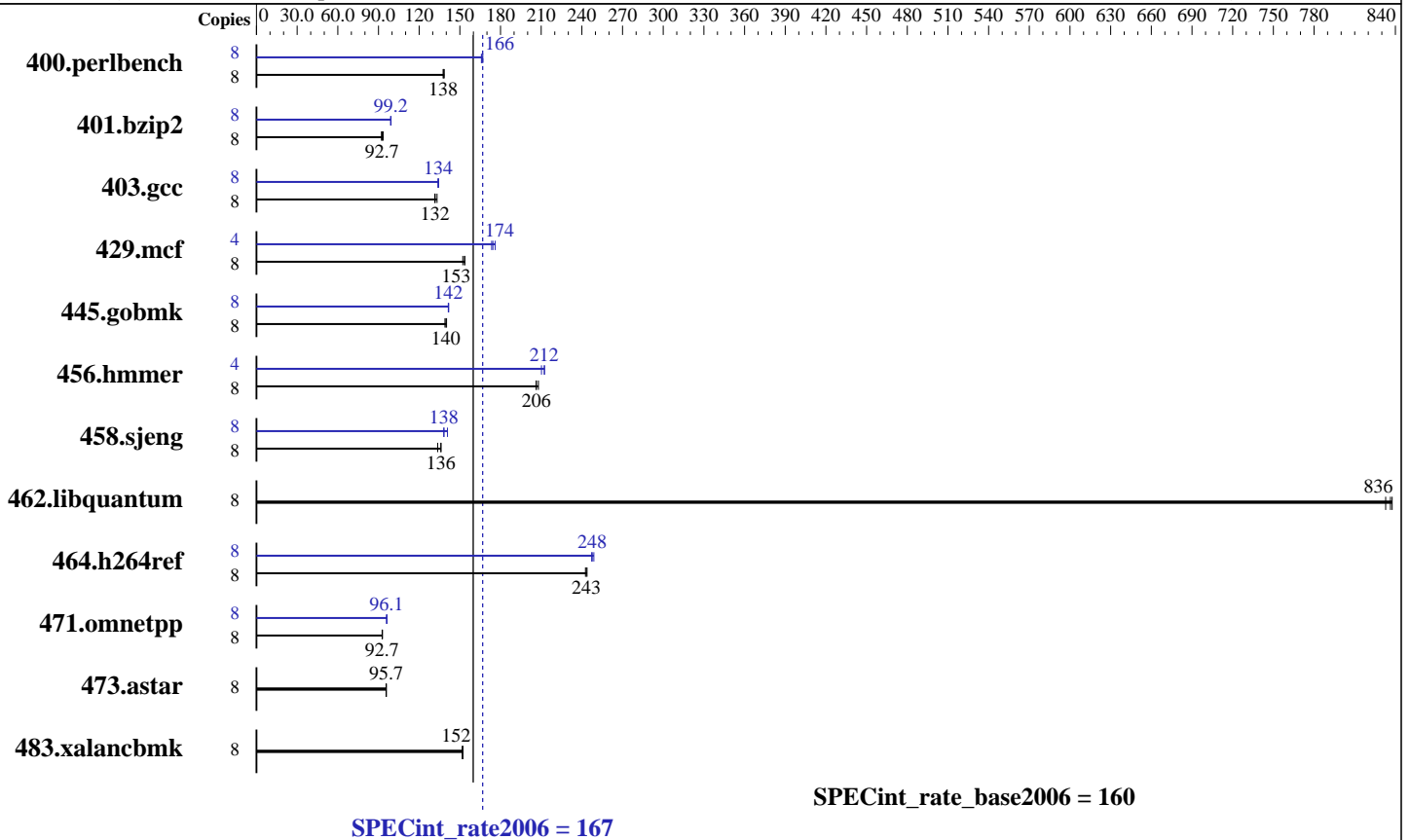
Test date: Apr-2011

Test sponsor: Acer Incorporated

Hardware Availability: Apr-2011

Tested by: Acer Incorporated

Software Availability: Jan-2011



### Hardware

CPU Name: Intel Xeon E3-1280  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.90 GHz  
 CPU MHz: 3500  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9,ECC)  
 Disk Subsystem: 1 x 640 GB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) SP1 ,  
 Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Compiler XE for applications running  
 on IA-32  
 Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint\_rate2006 = 167

Gateway GT110 F2 (Xeon E3-1280)

SPECint\_rate\_base2006 = 160

CPU2006 license: 97

Test date: Apr-2011

Test sponsor: Acer Incorporated

Hardware Availability: Apr-2011

Tested by: Acer Incorporated

Software Availability: Jan-2011

## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	564	139	567	138	<b>567</b>	<b>138</b>	8	468	167	471	166	<b>470</b>	<b>166</b>
401.bzip2	8	826	93.5	838	92.2	<b>833</b>	<b>92.7</b>	8	<b>778</b>	<b>99.2</b>	778	99.3	781	98.9
403.gcc	8	484	133	<b>489</b>	<b>132</b>	490	132	8	479	134	481	134	<b>480</b>	<b>134</b>
429.mcf	8	<b>476</b>	<b>153</b>	475	154	479	152	4	210	173	<b>209</b>	<b>174</b>	207	176
445.gobmk	8	<b>601</b>	<b>140</b>	604	139	599	140	8	593	141	<b>593</b>	<b>142</b>	592	142
456.hammer	8	359	208	<b>362</b>	<b>206</b>	362	206	4	178	210	<b>176</b>	<b>212</b>	176	212
458.sjeng	8	724	134	711	136	<b>712</b>	<b>136</b>	8	701	138	687	141	<b>700</b>	<b>138</b>
462.libquantum	8	<b>198</b>	<b>836</b>	198	838	199	833	8	<b>198</b>	<b>836</b>	198	838	199	833
464.h264ref	8	726	244	730	243	<b>728</b>	<b>243</b>	8	711	249	716	247	<b>715</b>	<b>248</b>
471.omnetpp	8	538	93.0	539	92.7	<b>539</b>	<b>92.7</b>	8	<b>520</b>	<b>96.1</b>	520	96.2	521	96.0
473.astar	8	587	95.7	588	95.6	<b>587</b>	<b>95.7</b>	8	587	95.7	588	95.6	<b>587</b>	<b>95.7</b>
483.xalancbmk	8	363	152	<b>363</b>	<b>152</b>	364	152	8	363	152	<b>363</b>	<b>152</b>	364	152

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

numactl was used to bind copies to the cores  
The config file option 'submit' was used.

## Operating System Notes

Large pages were disabled for this run.

## General Notes

Binaries compiled on RHEL5.5  
This result was measured on the Gateway GT110 F2.  
The Acer AT110 F2 is electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint\_rate2006 = 167

Gateway GT110 F2 (Xeon E3-1280)

SPECint\_rate\_base2006 = 160

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Apr-2011

Hardware Availability: Apr-2011

Software Availability: Jan-2011

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint\_rate2006 = 167

Gateway GT110 F2 (Xeon E3-1280)

SPECint\_rate\_base2006 = 160

CPU2006 license: 97

Test date: Apr-2011

Test sponsor: Acer Incorporated

Hardware Availability: Apr-2011

Tested by: Acer Incorporated

Software Availability: Jan-2011

## Peak Portability Flags (Continued)

456.hmmr: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2)  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xAVX -ipo -O3 -no-prec-div  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias  
-auto-ilp32

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

456.hmmr: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias  
-opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/smartheap -lsmartheap

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint\_rate2006 = 167

Gateway GT110 F2 (Xeon E3-1280)

SPECint\_rate\_base2006 = 160

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Apr-2011

Hardware Availability: Apr-2011

Software Availability: Jan-2011

## Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html>

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 21:02:56 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 10 May 2011.