



# SPEC® CFP2006 Result

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**IBM Corporation**

**SPECfp®\_rate2006 = 261**

**IBM System x3650 M3 (Intel Xeon X5675)**

**SPECfp\_rate\_base2006 = 254**

**CPU2006 license:** 11

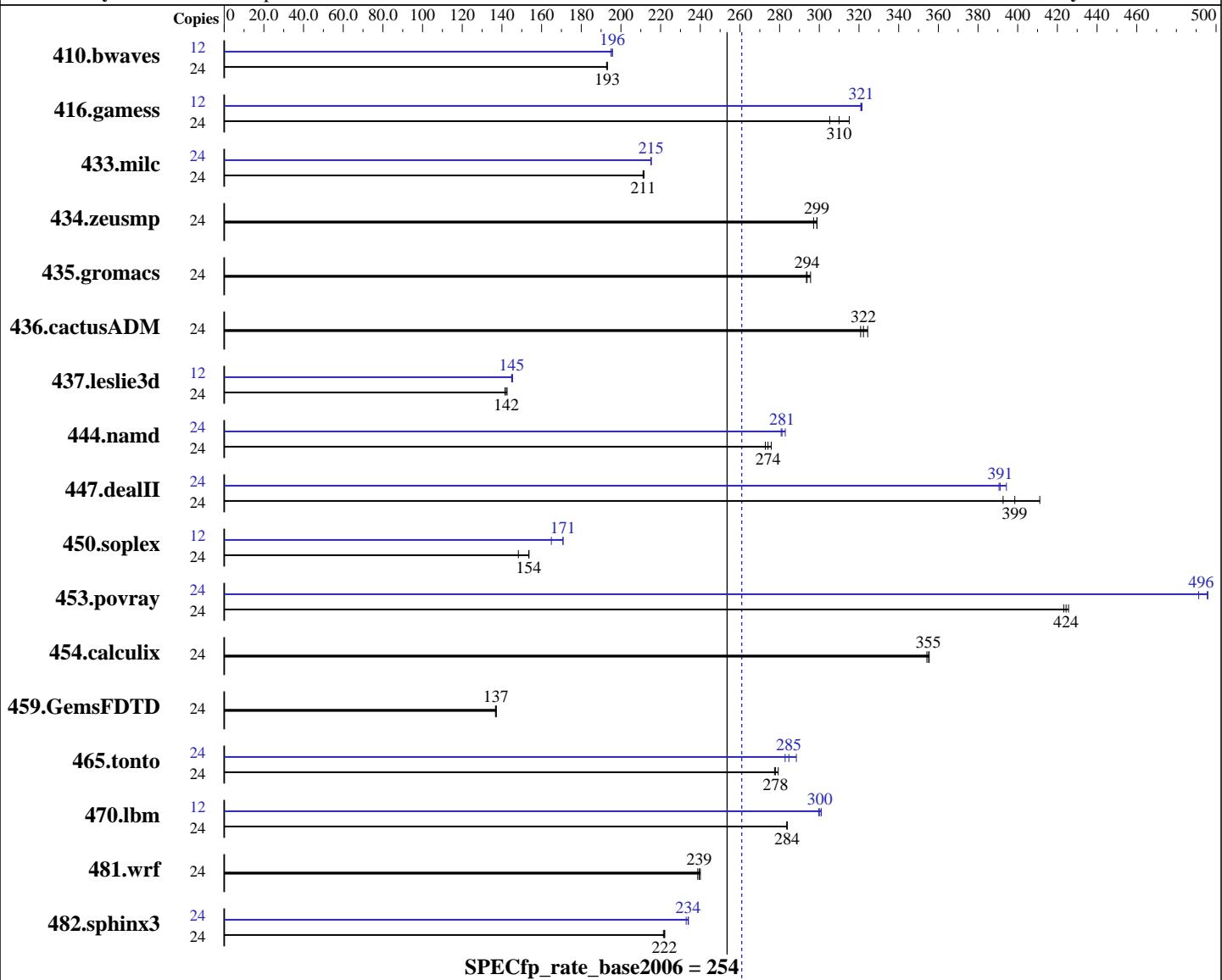
**Test date:** Mar-2011

**Test sponsor:** IBM Corporation

**Hardware Availability:** Feb-2011

**Tested by:** IBM Corporation

**Software Availability:** Jan-2011



## Hardware

CPU Name: Intel Xeon X5675  
CPU Characteristics: Intel Turbo Boost Technology up to 3.46 GHz  
CPU MHz: 3067  
FPU: Integrated  
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
CPU(s) orderable: 1,2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116  
Auto Parallel: No  
File System: ext3  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit

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L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 96 GB (12 x 8 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 146 GB SAS, 15000 RPM  
 Other Hardware: None

Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	<b>1689</b>	<b>193</b>	1689	193	1692	193	12	<b>833</b>	<b>196</b>	833	196	837	195
416.gamess	24	1491	315	<b>1516</b>	<b>310</b>	1540	305	12	<b>731</b>	<b>322</b>	732	321	<b>732</b>	<b>321</b>
433.milc	24	1041	212	<b>1043</b>	<b>211</b>	1043	211	24	1023	<b>215</b>	<b>1024</b>	<b>215</b>	1024	215
434.zeusmp	24	735	297	731	299	<b>731</b>	<b>299</b>	24	735	297	731	299	<b>731</b>	<b>299</b>
435.gromacs	24	580	296	<b>583</b>	<b>294</b>	584	294	24	580	296	<b>583</b>	<b>294</b>	584	294
436.cactusADM	24	894	321	<b>890</b>	<b>322</b>	884	324	24	894	321	<b>890</b>	<b>322</b>	884	324
437.leslie3d	24	<b>1586</b>	<b>142</b>	1582	143	1594	142	12	<b>778</b>	<b>145</b>	<b>778</b>	<b>145</b>	776	145
444.namd	24	698	276	<b>702</b>	<b>274</b>	706	273	24	681	283	<b>685</b>	<b>281</b>	686	281
447.dealII	24	<b>689</b>	<b>399</b>	699	393	668	411	24	696	394	703	391	<b>702</b>	<b>391</b>
450.soplex	24	1350	148	1303	154	<b>1304</b>	<b>154</b>	12	<b>607</b>	<b>165</b>	586	171	<b>586</b>	<b>171</b>
453.povray	24	300	426	<b>301</b>	<b>424</b>	302	423	24	260	491	<b>258</b>	<b>496</b>	257	496
454.calculix	24	559	354	<b>558</b>	<b>355</b>	557	355	24	559	354	<b>558</b>	<b>355</b>	557	355
459.GemsFDTD	24	1862	137	1856	137	<b>1859</b>	<b>137</b>	24	1862	137	1856	137	<b>1859</b>	<b>137</b>
465.tonto	24	<b>850</b>	<b>278</b>	846	279	851	277	24	835	283	819	288	<b>830</b>	<b>285</b>
470.lbm	24	1162	284	<b>1162</b>	<b>284</b>	1163	284	12	<b>548</b>	<b>301</b>	<b>549</b>	<b>300</b>	550	300
481.wrf	24	1117	240	<b>1120</b>	<b>239</b>	1123	239	24	1117	240	<b>1120</b>	<b>239</b>	1123	239
482.sphinx3	24	2113	221	2106	222	<b>2108</b>	<b>222</b>	24	2009	233	<b>2000</b>	<b>234</b>	1999	234

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
 numactl was used to bind copies to the cores

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 10800 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```



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## Platform Notes

Turbo Mode enabled in BIOS

Turbo Boost set to Traditional in BIOS

Power C-State enabled in BIOS

Data Reuse disabled in BIOS

Demand Scrub disabled in BIOS

## General Notes

Binaries compiled on RHEL5.5

## Base Compiler Invocation

C benchmarks:

  icc -m64

C++ benchmarks:

  icpc -m64

Fortran benchmarks:

  ifort -m64

Benchmarks using both Fortran and C:

  icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
  433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
  444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
  465.tonto: -DSPEC\_CPU\_LP64  
  470.lbm: -DSPEC\_CPU\_LP64  
  481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

470.lbm: -DSPEC\_CPU\_LP64

481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-B /usr/share/libhugetlbfss/ -Wl,-hugetlbfss-link=BDT

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias  
-B /usr/share/libhugetlbfss/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfss-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div  
-B /usr/share/libhugetlbfss/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfss-link=BDT

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## Peak Optimization Flags (Continued)

459.GemsFDTD: basepeak = yes

```
465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -auto
            -inline-calloc -opt-malloc-options=3
            -B /usr/share/libhugetlbfss/ -Wl,-melf_x86_64 -Wl,-hugetlbfss-link=BDT
```

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>  
<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.20110420.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>  
<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.20110420.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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