



SPEC[®] CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint[®]_rate2006 = 396

PRIMERGY BX620 S6, Intel Xeon X5675, 3.07 GHz

SPECint_rate_base2006 = 371

CPU2006 license: 19

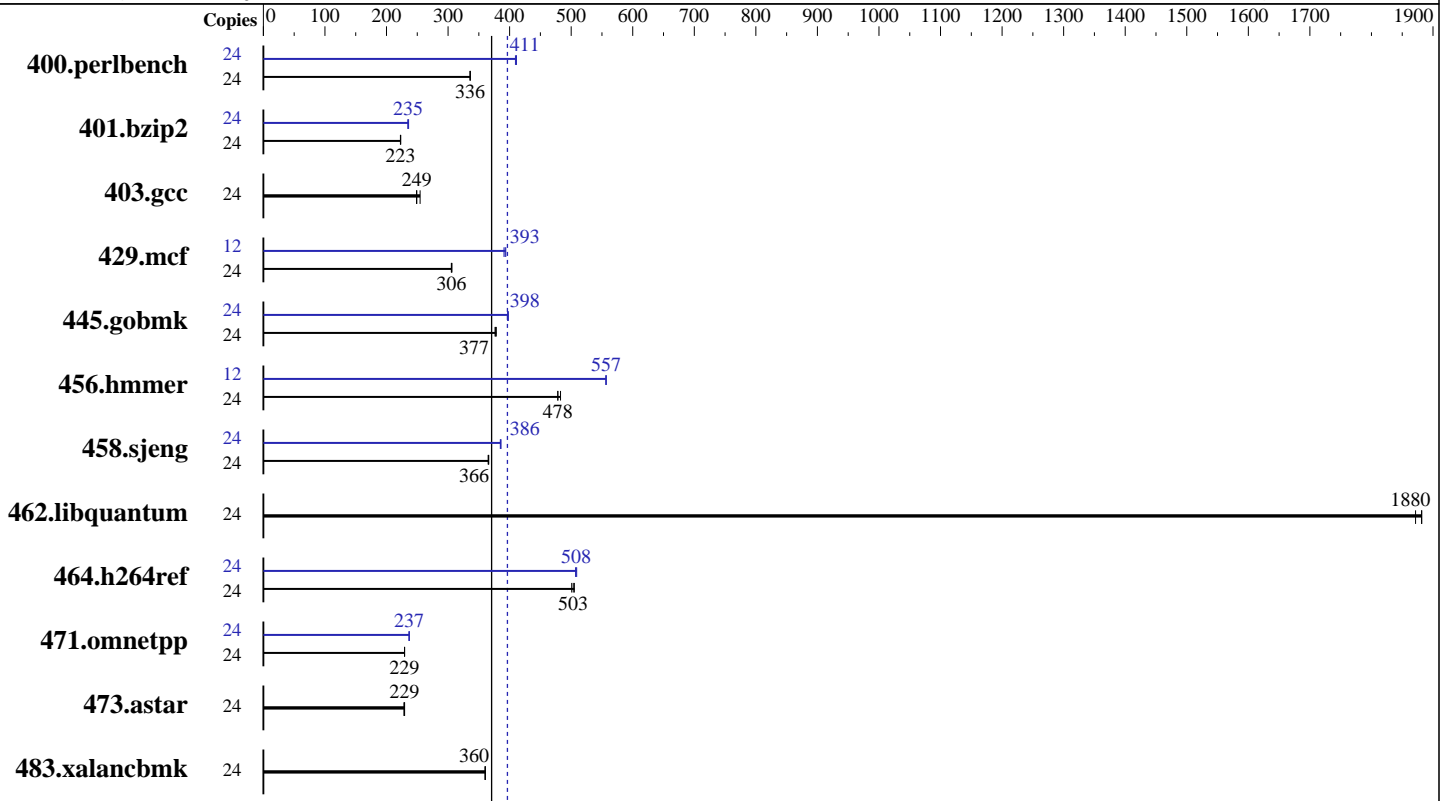
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jan-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011



SPECint_rate2006 = 396

SPECint_rate_base2006 = 371

Hardware

CPU Name: Intel Xeon X5675
 CPU Characteristics: Intel Turbo Boost Technology up to 3.47 GHz
 CPU MHz: 3067
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 96 GB (12 x 8 GB 2Rx4 PC3-10600R-9, ECC)
 Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM
 Other Hardware: --

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ Compiler XE for applications running on IA-32, Version 12.0.1.116 Build 20101116
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01, Binaries compiled on RHEL5.5 with binutils-2.17.50.0.6-14.el5



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint_rate2006 = 396

PRIMERGY BX620 S6, Intel Xeon X5675, 3.07 GHz

SPECint_rate_base2006 = 371

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jan-2011
Hardware Availability: Feb-2011
Software Availability: Jan-2011

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	698	336	698	336	698	336	24	570	411	571	411	573	409
401.bzip2	24	1040	223	1038	223	1040	223	24	986	235	982	236	986	235
403.gcc	24	759	255	777	249	775	249	24	759	255	777	249	775	249
429.mcf	24	717	305	716	306	715	306	12	280	391	278	394	279	393
445.gobmk	24	667	377	669	376	665	378	24	635	397	633	398	633	398
456.hammer	24	464	483	468	478	468	478	12	201	556	201	557	201	557
458.sjeng	24	794	366	795	365	793	366	24	753	386	753	386	755	385
462.libquantum	24	264	1880	266	1870	264	1880	24	264	1880	266	1870	264	1880
464.h264ref	24	1051	505	1060	501	1055	503	24	1044	509	1047	507	1045	508
471.omnetpp	24	654	229	654	229	654	229	24	633	237	633	237	634	237
473.astar	24	735	229	734	229	738	228	24	735	229	734	229	738	228
483.xalancbmk	24	460	360	459	361	460	360	24	460	360	459	361	460	360

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
Large pages were not enabled for this run

Platform Notes

BIOS configuration:
Data Reuse Optimization = Disable
Performance/Power Setting = Traditional

General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc -m32

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint_rate2006 = 396

PRIMERGY BX620 S6, Intel Xeon X5675, 3.07 GHz

SPECint_rate_base2006 = 371

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jan-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint_rate2006 = 396

PRIMERGY BX620 S6, Intel Xeon X5675, 3.07 GHz

SPECint_rate_base2006 = 371

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jan-2011
Hardware Availability: Feb-2011
Software Availability: Jan-2011

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: basepeak = yes

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/smartheap -lsmartheap

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint_rate2006 = 396

PRIMERGY BX620 S6, Intel Xeon X5675, 3.07 GHz

SPECint_rate_base2006 = 371

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jan-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011

Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 17:04:07 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 21 February 2011.