



# SPEC® CINT2006 Result

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## Fujitsu

SPECint®\_rate2006 = 344

PRIMERGY BX620 S6, Intel Xeon X5650, 2.67 GHz

SPECint\_rate\_base2006 = 321

CPU2006 license: 19

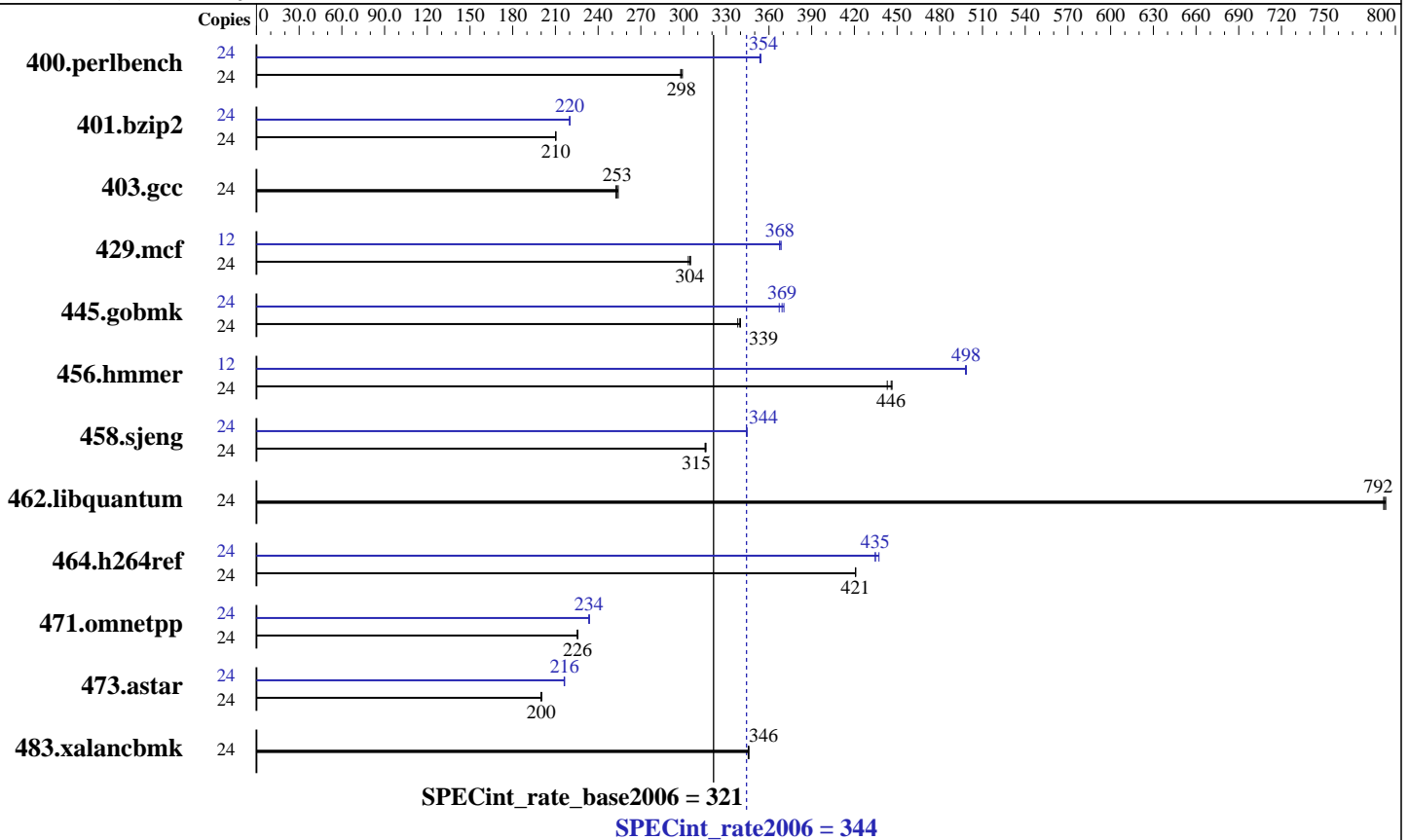
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Aug-2010

Hardware Availability: Aug-2010

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon X5650  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.06 GHz  
 CPU MHz: 2667  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)  
 Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) with SP1, Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	784	299	<b><u>786</u></b>	<b><u>298</u></b>	787	298	24	<b><u>662</u></b>	<b><u>354</u></b>	663	354	662	354
401.bzip2	24	1102	210	<b><u>1102</u></b>	<b><u>210</u></b>	1101	210	24	1052	220	1053	220	<b><u>1053</u></b>	<b><u>220</u></b>
403.gcc	24	760	254	<b><u>763</u></b>	<b><u>253</u></b>	765	253	24	760	254	<b><u>763</u></b>	<b><u>253</u></b>	765	253
429.mcf	24	718	305	<b><u>719</u></b>	<b><u>304</u></b>	722	303	12	298	368	<b><u>298</u></b>	<b><u>368</u></b>	297	369
445.gobmk	24	740	340	745	338	<b><u>742</u></b>	<b><u>339</u></b>	24	<b><u>682</u></b>	<b><u>369</u></b>	686	367	679	371
456.hammer	24	502	446	<b><u>502</u></b>	<b><u>446</u></b>	505	443	12	225	499	<b><u>225</u></b>	<b><u>498</u></b>	225	498
458.sjeng	24	922	315	<b><u>921</u></b>	<b><u>315</u></b>	920	316	24	843	345	843	344	<b><u>843</u></b>	<b><u>344</u></b>
462.libquantum	24	628	792	<b><u>628</u></b>	<b><u>792</u></b>	627	793	24	628	792	<b><u>628</u></b>	<b><u>792</u></b>	627	793
464.h264ref	24	1262	421	<b><u>1262</u></b>	<b><u>421</u></b>	1262	421	24	1215	437	<b><u>1222</u></b>	<b><u>435</u></b>	1222	435
471.omnetpp	24	<b><u>665</u></b>	<b><u>226</u></b>	664	226	666	225	24	641	234	<b><u>642</u></b>	<b><u>234</u></b>	642	234
473.astar	24	843	200	841	200	<b><u>843</u></b>	<b><u>200</u></b>	24	<b><u>779</u></b>	<b><u>216</u></b>	779	216	779	216
483.xalancbmk	24	479	345	<b><u>479</u></b>	<b><u>346</u></b>	479	346	24	479	345	<b><u>479</u></b>	<b><u>346</u></b>	479	346

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

BIOS configuration:  
Data Reuse Optimization = Disable  
Performance/Power Setting = Traditional

## General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>  
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

## Base Compiler Invocation

C benchmarks:  
icc -m32

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.icl1.1/libicl1.1-32bit -lsmarheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks (except as noted below):  
icpc -m32

473.astar: icpc -m64



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias  
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32  
403.gcc: basepeak = yes  
429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias  
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32  
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32  
462.libquantum: basepeak = yes  
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

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## Peak Optimization Flags (Continued)

```
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
           -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64
```

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

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