



# SPEC® CINT2006 Result

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## Fujitsu

### SPECint®\_rate2006 = 101

PRIMERGY BX924 S2, Intel Xeon L5630, 2.13 GHz

### SPECint\_rate\_base2006 = 94.7

CPU2006 license: 19

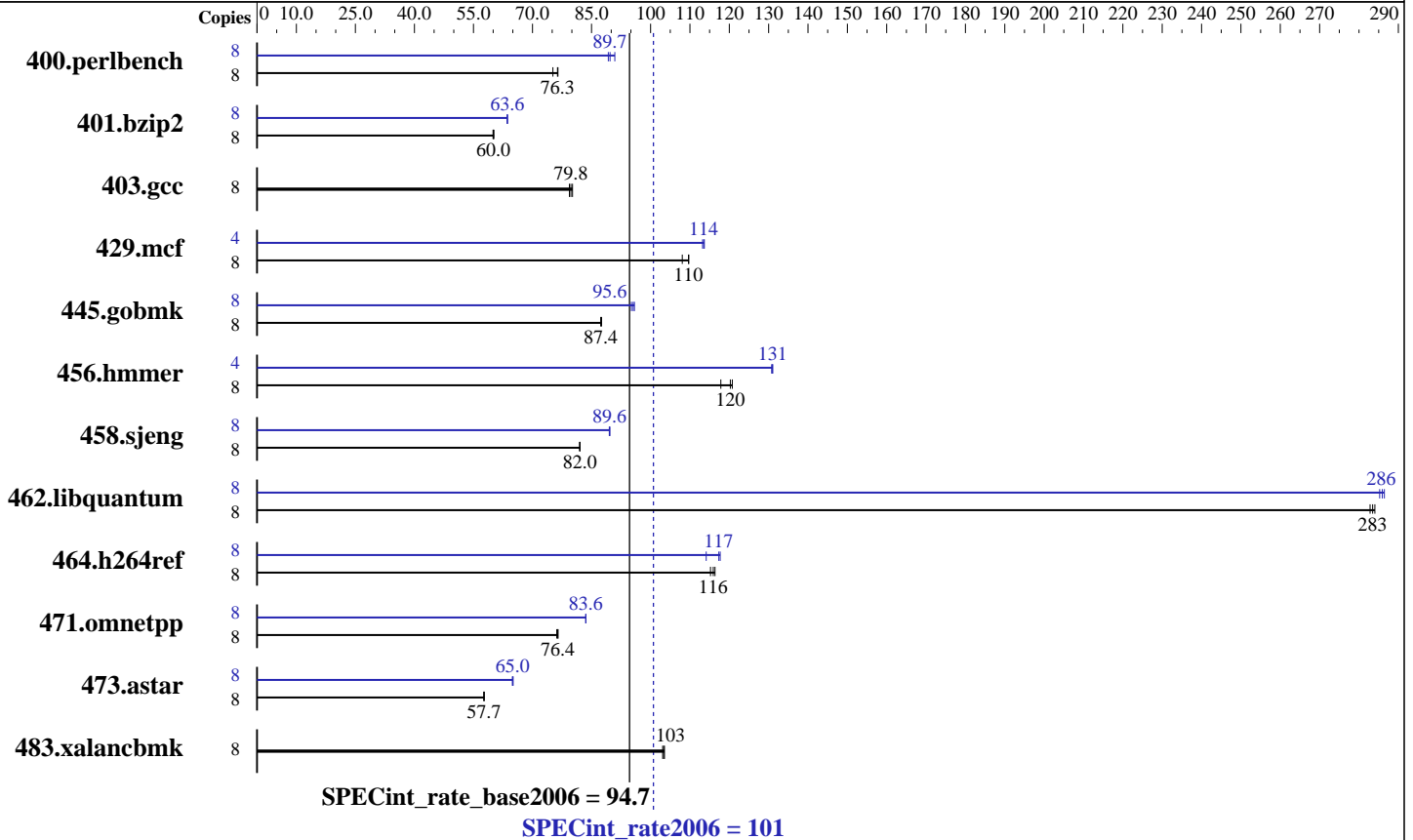
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Aug-2010

Hardware Availability: Jun-2010

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon L5630  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.4 GHz  
 CPU MHz: 2133  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)  
 Disk Subsystem: 1 x SATA, 160 GB, 5400 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	1022	76.5	<b><u>1024</u></b>	<b><u>76.3</u></b>	1040	75.2	8	876	89.3	860	90.9	<b><u>871</u></b>	<b><u>89.7</u></b>
401.bzip2	8	1287	60.0	1282	60.2	<b><u>1286</u></b>	<b><u>60.0</u></b>	8	1214	63.6	1213	63.6	<b><u>1214</u></b>	<b><u>63.6</u></b>
403.gcc	8	<b><u>807</u></b>	<b><u>79.8</u></b>	811	79.4	803	80.2	8	<b><u>807</u></b>	<b><u>79.8</u></b>	811	79.4	803	80.2
429.mcf	8	<b><u>665</u></b>	<b><u>110</u></b>	675	108	665	110	4	<b><u>321</u></b>	<b><u>114</u></b>	321	114	322	113
445.gobmk	8	<b><u>960</u></b>	<b><u>87.4</u></b>	960	87.4	958	87.6	8	<b><u>878</u></b>	<b><u>95.6</u></b>	875	95.9	882	95.1
456.hammer	8	633	118	<b><u>621</u></b>	<b><u>120</u></b>	618	121	4	285	131	285	131	<b><u>285</u></b>	<b><u>131</u></b>
458.sjeng	8	<b><u>1180</u></b>	<b><u>82.0</u></b>	1181	82.0	1180	82.1	8	1080	89.7	1082	89.5	<b><u>1081</u></b>	<b><u>89.6</u></b>
462.libquantum	8	586	283	<b><u>585</u></b>	<b><u>283</u></b>	584	284	8	<b><u>580</u></b>	<b><u>286</u></b>	581	285	579	286
464.h264ref	8	1536	115	1521	116	<b><u>1527</u></b>	<b><u>116</u></b>	8	<b><u>1509</u></b>	<b><u>117</u></b>	1551	114	1504	118
471.omnetpp	8	654	76.5	656	76.2	<b><u>654</u></b>	<b><u>76.4</u></b>	8	<b><u>598</u></b>	<b><u>83.6</u></b>	599	83.5	598	83.6
473.astar	8	973	57.7	<b><u>974</u></b>	<b><u>57.7</u></b>	974	57.6	8	<b><u>865</u></b>	<b><u>65.0</u></b>	865	64.9	863	65.1
483.xalancbmk	8	533	104	<b><u>534</u></b>	<b><u>103</u></b>	536	103	8	533	104	<b><u>534</u></b>	<b><u>103</u></b>	536	103

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

The system automatically configures the memory to run at 1067 MHz.  
BIOS configuration:  
Data Reuse Optimization = Disable  
Performance/Power Setting = Traditional

## General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>  
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

## Base Compiler Invocation

C benchmarks:  
icc -m32

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmarheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):  
icpc -m32

473.astar: icpc -m64



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## Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

```

## Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
            -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
            -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
            -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
            -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
                -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
              -prof-use(pass 2) -unroll2 -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
              -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

```

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## Peak Optimization Flags (Continued)

```

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
          -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
          -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
          -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

```

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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