



# SPEC® CINT2006 Result

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## ACTION S.A.

## SPECint®\_rate2006 = 144

### ACTINA SOLAR 220 S4 (Intel Xeon L5609)

## SPECint\_rate\_base2006 = 134

CPU2006 license: 9008

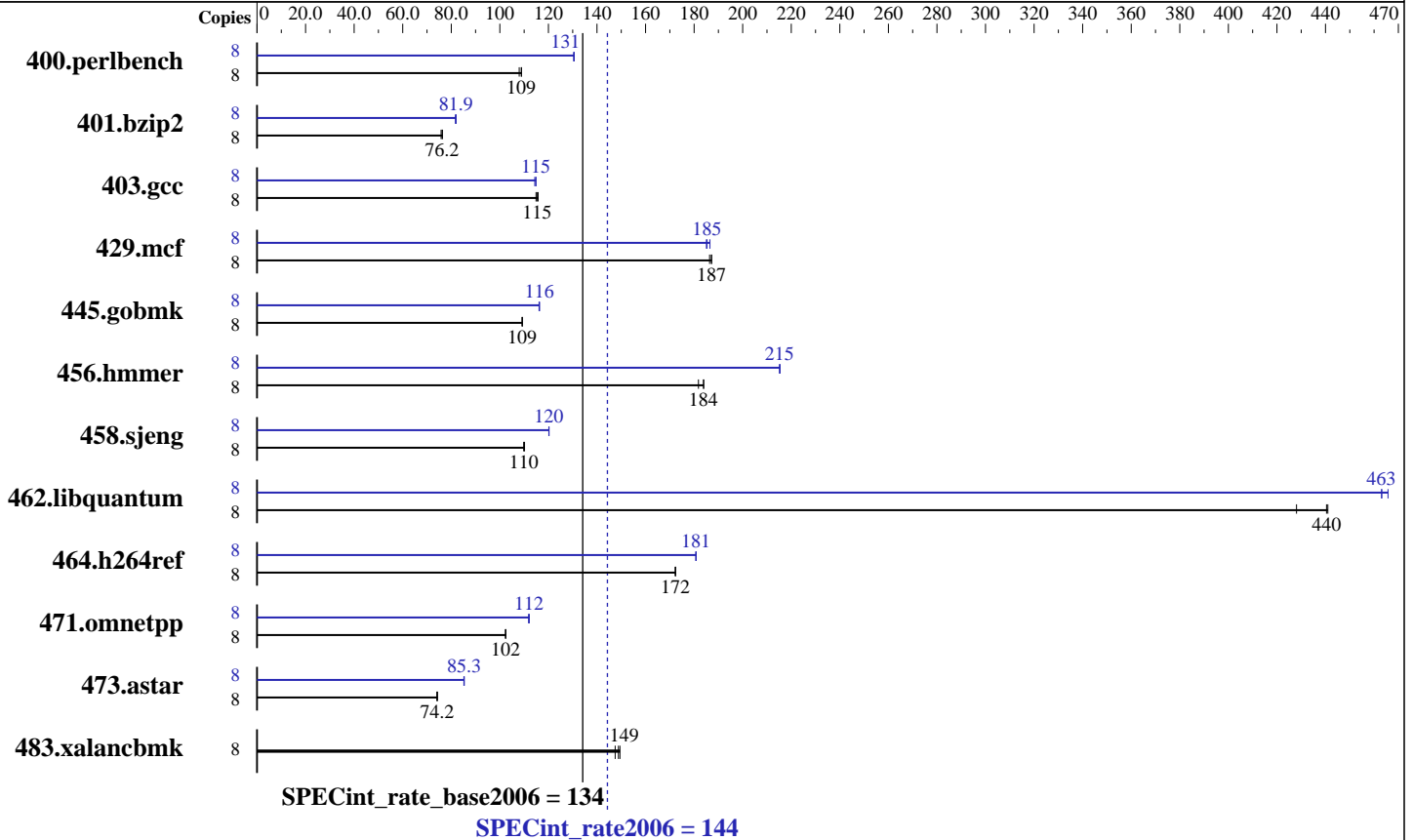
Test sponsor: ACTION S.A.

Tested by: ACTION S.A.

Test date: Jun-2010

Hardware Availability: Apr-2010

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon L5609  
 CPU Characteristics:  
 CPU MHz: 1887  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6 x 4 GB, DDR3-1066 RDIMM, ECC, CL7, Dual Rank)  
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	724	108	718	109	<b>718</b>	<b>109</b>	8	598	131	<b>599</b>	<b>131</b>	599	130
401.bzip2	8	<b>1012</b>	<b>76.2</b>	1018	75.8	1011	76.3	8	<b>942</b>	<b>81.9</b>	942	82.0	945	81.7
403.gcc	8	561	115	<b>558</b>	<b>115</b>	556	116	8	561	115	<b>561</b>	<b>115</b>	563	114
429.mcf	8	391	186	<b>390</b>	<b>187</b>	390	187	8	391	187	<b>394</b>	<b>185</b>	394	185
445.gobmk	8	768	109	769	109	<b>768</b>	<b>109</b>	8	<b>722</b>	<b>116</b>	722	116	721	116
456.hammer	8	411	182	<b>406</b>	<b>184</b>	406	184	8	347	215	347	215	<b>347</b>	<b>215</b>
458.sjeng	8	879	110	<b>881</b>	<b>110</b>	881	110	8	805	120	805	120	<b>805</b>	<b>120</b>
462.libquantum	8	<b>376</b>	<b>440</b>	376	441	387	428	8	<b>358</b>	<b>463</b>	356	466	358	463
464.h264ref	8	1027	172	1028	172	<b>1027</b>	<b>172</b>	8	<b>979</b>	<b>181</b>	980	181	979	181
471.omnetpp	8	488	102	<b>488</b>	<b>102</b>	488	102	8	447	112	446	112	<b>447</b>	<b>112</b>
473.astar	8	758	74.1	756	74.3	<b>757</b>	<b>74.2</b>	8	<b>658</b>	<b>85.3</b>	659	85.2	658	85.3
483.xalancbmk	8	369	149	<b>371</b>	<b>149</b>	374	147	8	369	149	<b>371</b>	<b>149</b>	374	147

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was set for stacksize unlimited

## General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

## Base Compiler Invocation

C benchmarks:  
icc -m32  
  
C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

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## Base Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32  
 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
 -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmarheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs  
 -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmarheap64

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## Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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