



# SPEC<sup>®</sup> CFP2006 Result

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## IBM Corporation

### SPECfp<sup>®</sup>\_rate2006 = 519

### IBM System x3850 X5 (Intel Xeon X7550)

### SPECfp\_rate\_base2006 = 507

CPU2006 license: 11

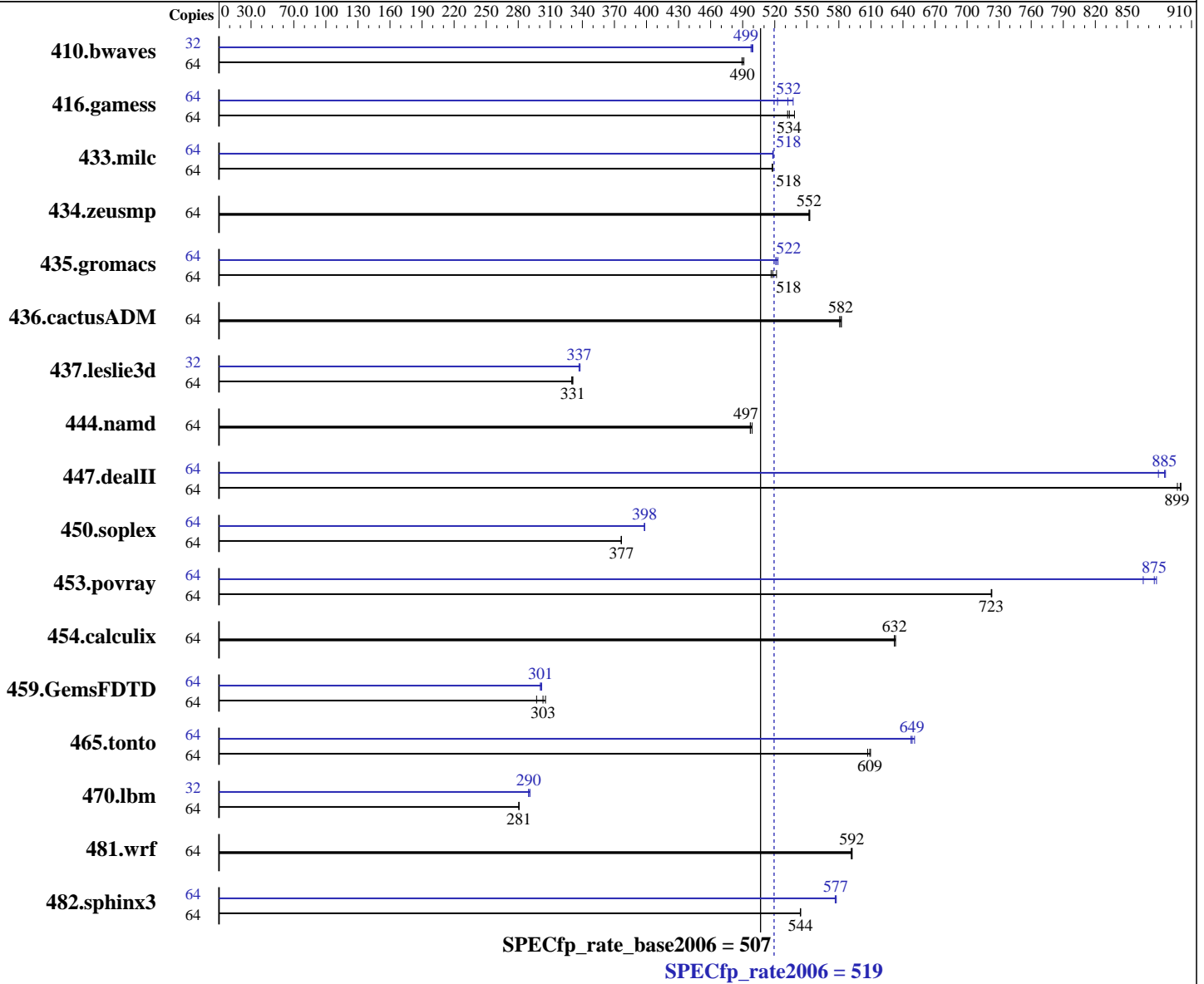
Test date: Apr-2010

Test sponsor: IBM Corporation

Hardware Availability: Mar-2010

Tested by: IBM Corporation

Software Availability: Jan-2010



#### Hardware

CPU Name: Intel Xeon X7550  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.40 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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#### Software

Operating System: SuSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064, l\_cprof\_p\_11.1.064  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)

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L3 Cache: 18 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (64 x 4 GB PC3-8500R, Quad Rank)  
Disk Subsystem: 3 x 50 GB SATA, SSD  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: Binutils 2.18.50.0.7.20080502

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	64	1771	491	<b><u>1776</u></b>	<b><u>490</u></b>	1777	489	32	<b><u>872</u></b>	<b><u>499</u></b>	874	498	871	500
416.gamess	64	2355	532	2327	538	<b><u>2348</u></b>	<b><u>534</u></b>	64	2333	537	2398	523	<b><u>2354</u></b>	<b><u>532</u></b>
433.milc	64	1134	518	1135	518	<b><u>1135</u></b>	<b><u>518</u></b>	64	<b><u>1133</u></b>	<b><u>518</u></b>	1133	519	1134	518
434.zeusmp	64	<b><u>1054</u></b>	<b><u>552</u></b>	1055	552	1053	553	64	<b><u>1054</u></b>	<b><u>552</u></b>	1055	552	1053	553
435.gromacs	64	<b><u>882</u></b>	<b><u>518</u></b>	885	517	876	522	64	874	523	877	521	<b><u>876</u></b>	<b><u>522</u></b>
436.cactusADM	64	1313	582	1317	581	<b><u>1315</u></b>	<b><u>582</u></b>	64	1313	582	1317	581	<b><u>1315</u></b>	<b><u>582</u></b>
437.leslie3d	64	<b><u>1818</u></b>	<b><u>331</u></b>	1817	331	1823	330	32	893	337	890	338	<b><u>893</u></b>	<b><u>337</u></b>
444.namd	64	1033	497	1029	499	<b><u>1032</u></b>	<b><u>497</u></b>	64	1033	497	1029	499	<b><u>1032</u></b>	<b><u>497</u></b>
447.dealII	64	816	897	<b><u>814</u></b>	<b><u>899</u></b>	813	900	64	833	879	827	886	<b><u>828</u></b>	<b><u>885</u></b>
450.soplex	64	1417	377	<b><u>1418</u></b>	<b><u>377</u></b>	1418	376	64	1341	398	<b><u>1341</u></b>	<b><u>398</u></b>	1340	398
453.povray	64	<b><u>471</u></b>	<b><u>723</u></b>	471	723	471	723	64	394	865	<b><u>389</u></b>	<b><u>875</u></b>	388	877
454.calculix	64	835	632	834	633	<b><u>835</u></b>	<b><u>632</u></b>	64	835	632	834	633	<b><u>835</u></b>	<b><u>632</u></b>
459.GemsFDTD	64	2285	297	<b><u>2240</u></b>	<b><u>303</u></b>	2222	306	64	<b><u>2256</u></b>	<b><u>301</u></b>	2258	301	2249	302
465.tonto	64	<b><u>1034</u></b>	<b><u>609</u></b>	1033	610	1038	607	64	972	648	<b><u>971</u></b>	<b><u>649</u></b>	968	651
470.lbm	64	3132	281	<b><u>3133</u></b>	<b><u>281</u></b>	3134	281	32	<b><u>1517</u></b>	<b><u>290</u></b>	1517	290	1511	291
481.wrf	64	<b><u>1207</u></b>	<b><u>592</u></b>	1207	592	1208	592	64	<b><u>1207</u></b>	<b><u>592</u></b>	1207	592	1208	592
482.sphinx3	64	<b><u>2292</u></b>	<b><u>544</u></b>	2292	544	2293	544	64	<b><u>2162</u></b>	<b><u>577</u></b>	2163	577	2160	578

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Platform Notes

Turbo Boost set to Traditional  
Demand Scrub disabled

## General Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run



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## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.lelie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
 -fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
 -opt-malloc-options=3 -ansi-alias -auto-ilp32

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## Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

### C++ benchmarks:

444.namd: basepeak = yes

447.dealIII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -auto -inline-calloc -opt-malloc-options=3

### Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.03.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.03.xml>

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