



SPEC® CINT2006 Result

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Intel Corporation

Supermicro SuperServer 7046A-T (Intel Xeon X5677, 3.46 GHz)

SPECint®2006 = 39.8

SPECint_base2006 = 37.8

CPU2006 license: 13

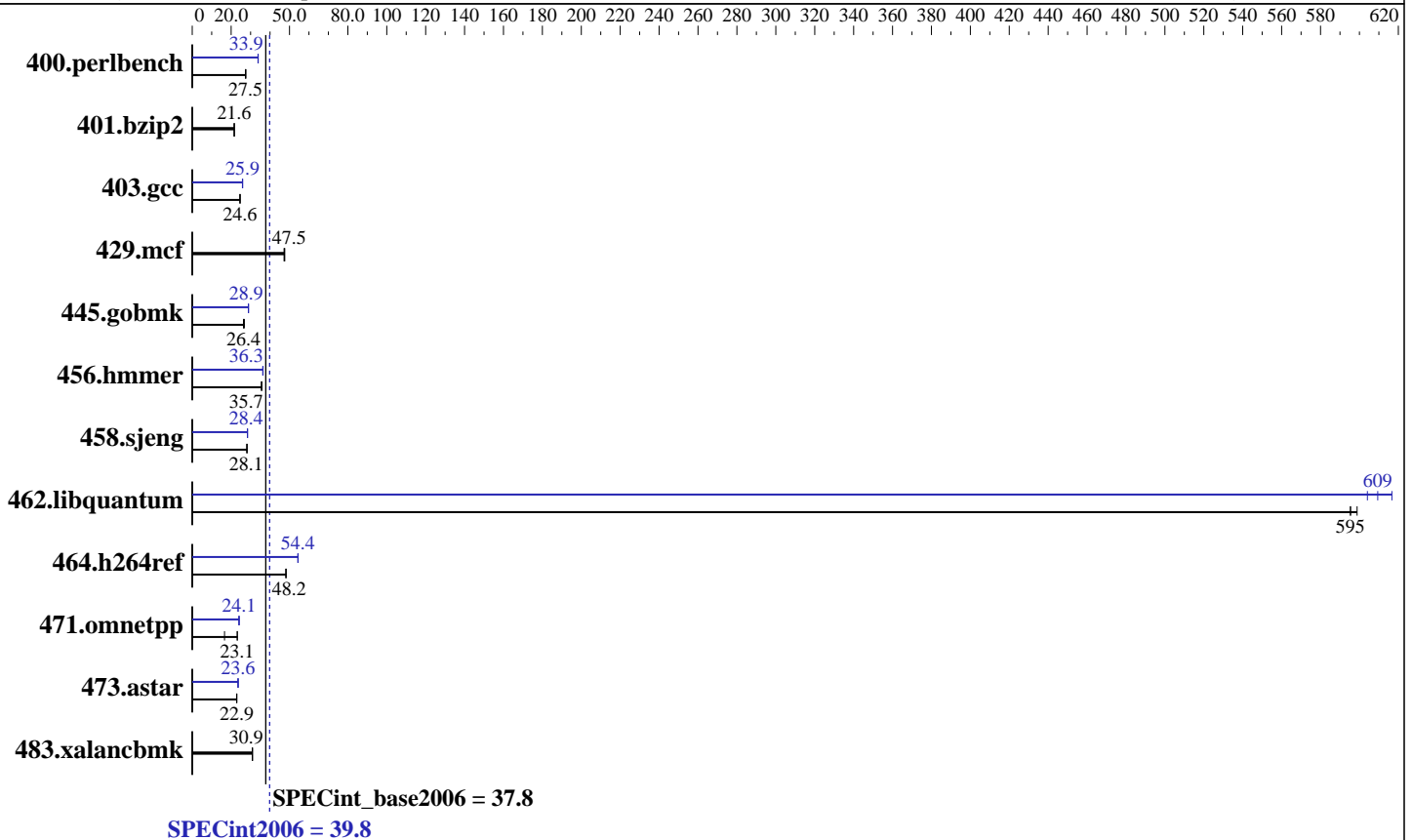
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Mar-2010

Hardware Availability: Mar-2010

Software Availability: Nov-2009



Hardware

CPU Name: Intel Xeon X5677
 CPU Characteristics: Intel Turbo Boost Technology up to 3.73 GHz
 CPU MHz: 3467
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6x 4GB DDR3-1333, ECC, CL9)
 Disk Subsystem: 1 x 300 GB SATA, 10000 RPM
 Other Hardware: None

Software

Operating System: Windows 7 Enterprise (64-bit)
 Compiler: Intel C++ Compiler Professional 11.1 for Intel 64 Build 20090903 Package ID: w_cproc_p_11.1.045 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: SmartHeap Library Version 8.1 from <http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	356	27.4	<u>356</u>	<u>27.5</u>	355	27.5	288	33.9	<u>289</u>	<u>33.9</u>	289	33.8
401.bzip2	447	21.6	<u>447</u>	<u>21.6</u>	447	21.6	447	21.6	<u>447</u>	<u>21.6</u>	447	21.6
403.gcc	328	24.6	<u>328</u>	<u>24.6</u>	328	24.5	<u>311</u>	<u>25.9</u>	312	25.8	311	25.9
429.mcf	<u>192</u>	<u>47.5</u>	192	47.5	193	47.2	<u>192</u>	<u>47.5</u>	192	47.5	193	47.2
445.gobmk	<u>398</u>	<u>26.4</u>	392	26.8	398	26.4	363	28.9	363	28.9	<u>363</u>	<u>28.9</u>
456.hmmer	262	35.6	262	35.7	<u>262</u>	<u>35.7</u>	257	36.3	257	36.4	<u>257</u>	<u>36.3</u>
458.sjeng	430	28.1	430	28.2	<u>430</u>	<u>28.1</u>	427	28.4	427	28.4	<u>427</u>	<u>28.4</u>
462.libquantum	34.6	599	<u>34.8</u>	<u>595</u>	34.8	595	33.6	617	<u>34.0</u>	<u>609</u>	34.3	604
464.h264ref	459	48.2	459	48.2	<u>459</u>	<u>48.2</u>	<u>407</u>	<u>54.4</u>	407	54.3	407	54.4
471.omnetpp	376	16.6	270	23.2	<u>270</u>	<u>23.1</u>	<u>260</u>	<u>24.1</u>	259	24.1	260	24.0
473.astar	<u>307</u>	<u>22.9</u>	307	22.9	308	22.8	<u>298</u>	<u>23.6</u>	298	23.5	298	23.6
483.xalancbmk	223	31.0	<u>223</u>	<u>30.9</u>	223	30.9	223	31.0	<u>223</u>	<u>30.9</u>	223	30.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems is used with tower case,
 PC Power and Cooling dual 865W power supplys
 OMP_NUM_THREADS set to number of processors cores
 KMP_AFFINITY set to granularity=fine,scatter
 System was configured with nVidia Quadro FX 370 discrete graphics card

Base Compiler Invocation

C benchmarks:
 icl -Qvc9 -Qstd=c99

C++ benchmarks:
 icl -Qvc9

Base Portability Flags

400.perlbench: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64_X64
 -DSPEC_CPU_NO_NEED_VA_COPY

401.bzip2: -DSPEC_CPU_P64

403.gcc: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64

429.mcf: -DSPEC_CPU_P64

445.gobmk: -DSPEC_CPU_P64

456.hmmer: -DSPEC_CPU_P64

Continued on next page



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Base Portability Flags (Continued)

```

458.sjeng: -DSPEC_CPU_P64
462.libquantum: -DSPEC_CPU_P64
464.h264ref: -DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTTYPES
471.omnetpp: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
473.astar: -DSPEC_CPU_P64
483.xalancbmk: -DSPEC_CPU_P64 -Qoption,cpp,--no_wchar_t_keyword

```

Base Optimization Flags

```

C benchmarks:
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel
-Qauto-ilp32 /F512000000

C++ benchmarks:
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
-Qauto-ilp32 /F512000000 shlw64M.lib -link /FORCE:MULTIPLE

```

Base Other Flags

```

C benchmarks:
403.gcc: -Dalloca=_alloca

```

Peak Compiler Invocation

```

C benchmarks:
icl -Qvc9 -Qstd=c99

C++ benchmarks:
icl -Qvc9

```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
-Qauto-ilp32 /F512000000 shlw64M.lib
-link /FORCE:MULTIPLE

401.bzip2: basepeak = yes

403.gcc: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qauto-ilp32 /F512000000

429.mcf: basepeak = yes

445.gobmk: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O2 -Qprec-div- -Qansi-alias -Qauto-ilp32
/F512000000

456.hmmer: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qauto-ilp32 /F512000000

458.sjeng: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 -Qauto-ilp32 /F512000000

462.libquantum: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch
-Qparallel -Qpar-schedule-static:32768 -Qansi-alias
-Qauto-ilp32 /F512000000

464.h264ref: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qauto-ilp32 /F512000000

C++ benchmarks:

471.omnetpp: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=block -Qauto-ilp32 /F512000000
shlw64M.lib -link /FORCE:MULTIPLE

473.astar: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=routine -Qauto-ilp32 /F512000000
shlw64M.lib -link /FORCE:MULTIPLE

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

Continued on next page



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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.xml>

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For other inquiries, please contact webmaster@spec.org.

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