SPEC® CFP2006 Result

Fujitsu

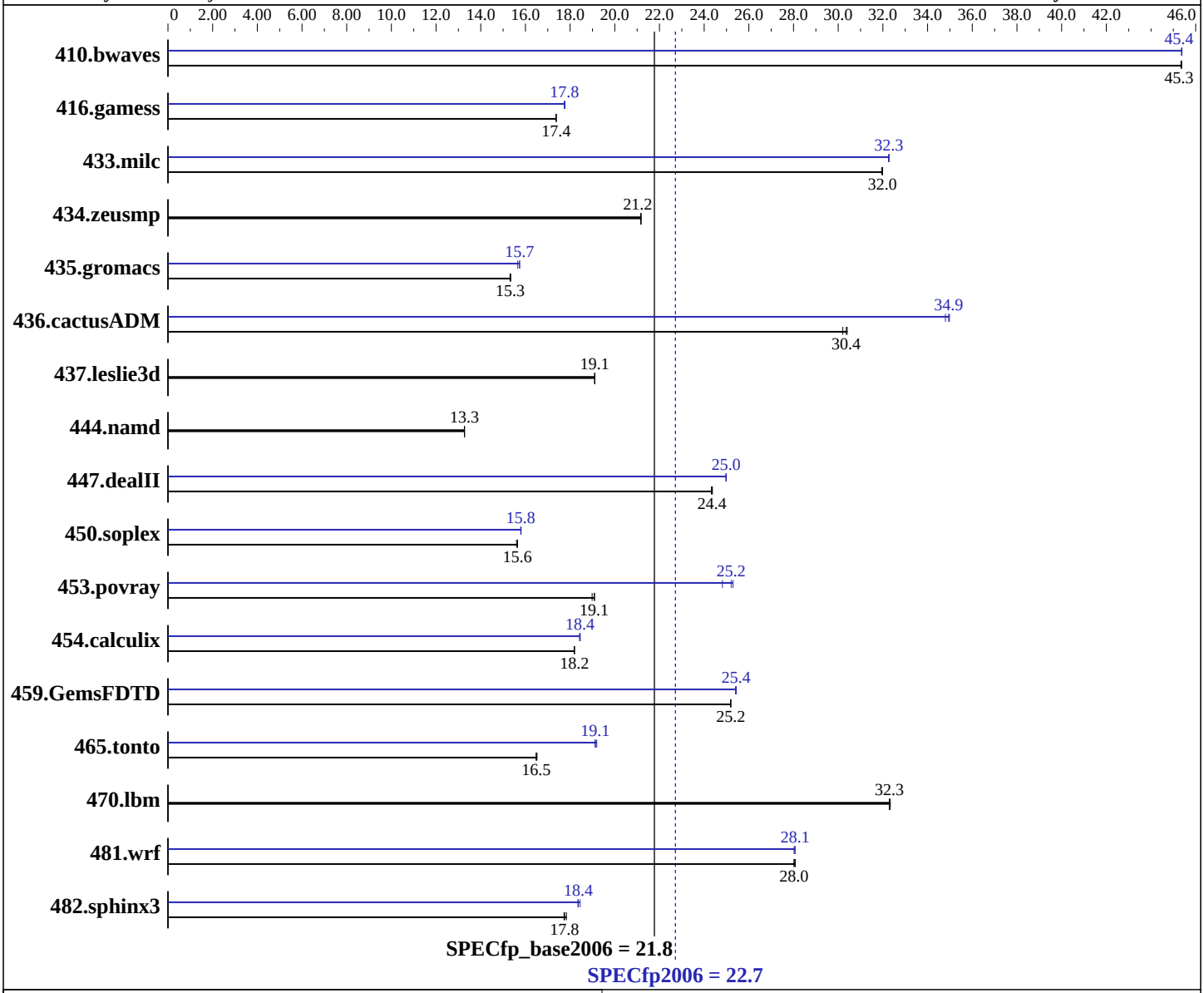
PRIMERGY RX100 S6, Intel Celeron G1101, 2.26 GHz

SPECfp®2006 = 22.7

SPECfp_base2006 = 21.8

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jan-2010
Hardware Availability: Jan-2010
Software Availability: Nov-2009



Hardware

CPU Name:	Intel Celeron G1101
CPU Characteristics:	
CPU MHz:	2267
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-smp
Compiler:	Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091012 Package ID: l_cproc_p_11.1.059, l_cprof_p_11.1.059
Auto Parallel:	Yes
File System:	ext3
System State:	Multi-User Run Level 3

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L3 Cache: 2 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (2x4 GB PC3-10600E, 2 rank, CL9-9-9, ECC, see add'l detail in notes)
Disk Subsystem: 1 x SATA, 250 GB, 7200 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	**300**	**45.3**	300	45.3	300	45.4	300	45.4	**300**	**45.4**	300	45.4
416.gamess	1127	17.4	1127	17.4	**1127**	**17.4**	1104	17.7	**1103**	**17.8**	1102	17.8
433.milc	287	32.0	**287**	**32.0**	287	31.9	**284**	**32.3**	285	32.3	284	32.3
434.zeusmp	**430**	**21.2**	430	21.2	430	21.2	**430**	**21.2**	430	21.2	430	21.2
435.gromacs	465	15.3	466	15.3	**466**	**15.3**	**454**	**15.7**	454	15.7	456	15.7
436.cactusADM	396	30.2	393	30.4	**394**	**30.4**	342	35.0	**342**	**34.9**	343	34.8
437.leslie3d	492	19.1	492	19.1	**492**	**19.1**	492	19.1	492	19.1	**492**	**19.1**
444.namd	**604**	**13.3**	604	13.3	604	13.3	**604**	**13.3**	604	13.3	604	13.3
447.dealII	**470**	**24.4**	469	24.4	470	24.3	458	25.0	**458**	**25.0**	458	25.0
450.soplex	533	15.6	535	15.6	**533**	**15.6**	528	15.8	**528**	**15.8**	528	15.8
453.povray	**279**	**19.1**	279	19.1	280	19.0	210	25.3	**211**	**25.2**	214	24.8
454.calculix	453	18.2	**453**	**18.2**	454	18.2	448	18.4	**447**	**18.4**	447	18.4
459.GemsFDTD	**421**	**25.2**	421	25.2	421	25.2	417	25.4	**417**	**25.4**	418	25.4
465.tonto	596	16.5	597	16.5	**597**	**16.5**	513	19.2	**514**	**19.1**	515	19.1
470.lbm	425	32.3	426	32.3	**426**	**32.3**	425	32.3	426	32.3	**426**	**32.3**
481.wrf	398	28.1	399	28.0	**399**	**28.0**	399	28.0	398	28.1	**398**	**28.1**
482.sphinx3	**1098**	**17.8**	1099	17.7	1093	17.8	1062	18.3	**1061**	**18.4**	1056	18.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

The system automatically configures the memory to run at 1066 MHz.

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M
For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSSE3 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSSE3 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:
-xSSSE3 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xSSSE3 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -ipo -O3 -no-prec-div -static -fno-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `-xSSSE3 -ipo -O3 -no-prec-div -static -unroll2`

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -scalar-rep- -opt-prefetch

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSSE3 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel -auto-ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-fp-linux64-revF.20100202.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-fp-linux64-revF.20100202.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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