



# SPEC® CINT2006 Result

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## Fujitsu

**SPECint®2006 = 21.4**

PRIMERGY TX200 S5, Intel Xeon E5504, 2.0 GHz

**SPECint\_base2006 = 19.3**

CPU2006 license: 19

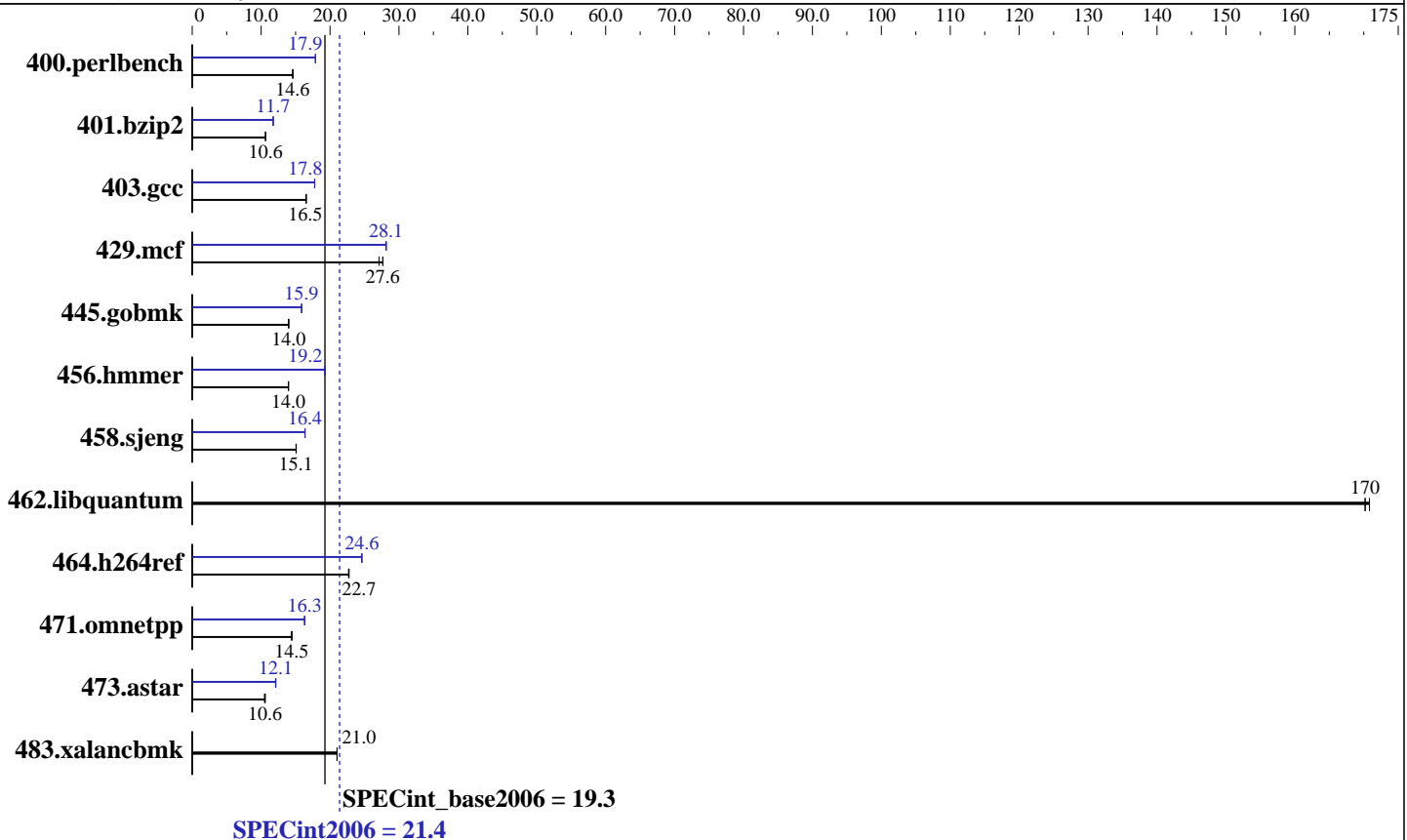
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Aug-2009

Hardware Availability: Apr-2009

Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon E5504  
 CPU Characteristics:  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)  
 Disk Subsystem: 1 x SATA, 250 GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.080  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	670	14.6	<b><u>669</u></b>	<b><u>14.6</u></b>	669	14.6	<b><u>546</u></b>	<b><u>17.9</u></b>	547	17.9	546	17.9
401.bzip2	<b><u>906</u></b>	<b><u>10.6</u></b>	908	10.6	905	10.7	821	11.8	<b><u>821</u></b>	<b><u>11.7</u></b>	822	11.7
403.gcc	487	16.5	487	16.5	<b><u>487</u></b>	<b><u>16.5</u></b>	453	17.8	<b><u>453</u></b>	<b><u>17.8</u></b>	453	17.8
429.mcf	336	27.1	<b><u>330</u></b>	<b><u>27.6</u></b>	330	27.7	<b><u>324</u></b>	<b><u>28.1</u></b>	324	28.2	325	28.1
445.gobmk	749	14.0	750	14.0	<b><u>750</u></b>	<b><u>14.0</u></b>	661	15.9	661	15.9	<b><u>661</u></b>	<b><u>15.9</u></b>
456.hammer	668	14.0	<b><u>668</u></b>	<b><u>14.0</u></b>	668	14.0	485	19.3	485	19.2	<b><u>485</u></b>	<b><u>19.2</u></b>
458.sjeng	<b><u>803</u></b>	<b><u>15.1</u></b>	801	15.1	803	15.1	740	16.4	739	16.4	<b><u>739</u></b>	<b><u>16.4</u></b>
462.libquantum	121	171	<b><u>122</u></b>	<b><u>170</u></b>	122	170	121	171	<b><u>122</u></b>	<b><u>170</u></b>	122	170
464.h264ref	972	22.8	976	22.7	<b><u>974</u></b>	<b><u>22.7</u></b>	<b><u>899</u></b>	<b><u>24.6</u></b>	899	24.6	899	24.6
471.omnetpp	432	14.5	433	14.4	<b><u>433</u></b>	<b><u>14.5</u></b>	<b><u>383</u></b>	<b><u>16.3</u></b>	383	16.3	383	16.3
473.astar	670	10.5	<b><u>664</u></b>	<b><u>10.6</u></b>	663	10.6	<b><u>579</u></b>	<b><u>12.1</u></b>	578	12.1	581	12.1
483.xalancbmk	328	21.0	328	21.0	<b><u>328</u></b>	<b><u>21.0</u></b>	328	21.0	328	21.0	<b><u>328</u></b>	<b><u>21.0</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

The system automatically configures the memory to run at 800 MHz.

## General Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter  
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## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel  
-par-runtime-control -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch  
401.bzp2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias  
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3  
429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias  
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32  
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32  
462.libquantum: basepeak = yes  
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap  
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

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## Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20091013.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20091013.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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