



SPEC® CINT2006 Result

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Dell Inc.

SPECint_rate2006 = Not Run

PowerEdge R610 (Intel Xeon E5520, 2.26 GHz)

SPECint_rate_base2006 = 87.6

CPU2006 license: 55

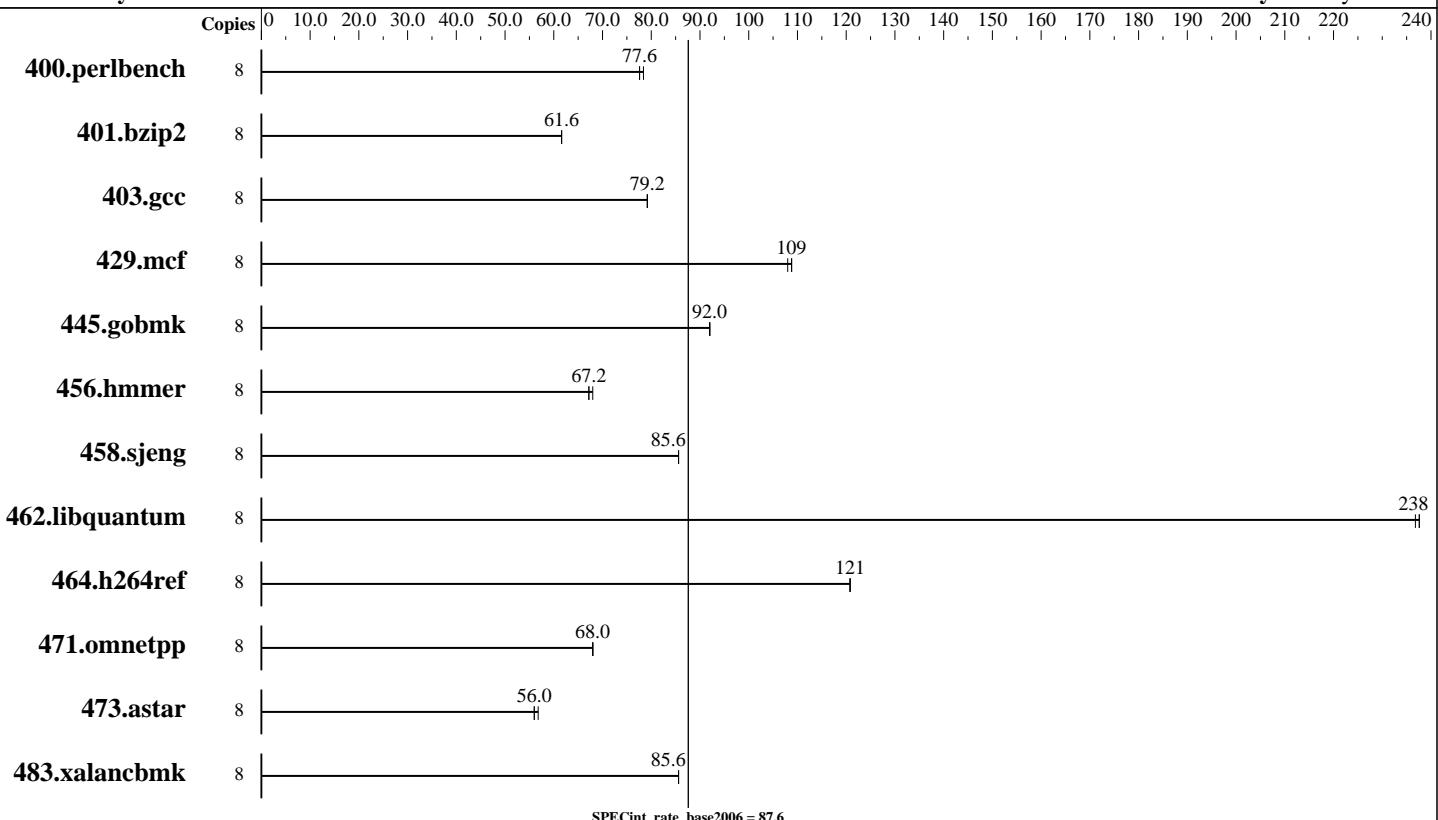
Test date: Sep-2009

Test sponsor: Dell Inc.

Hardware Availability: Mar-2009

Tested by: Dell Inc.

Software Availability: May-2009



Hardware

CPU Name: Intel Xeon E5520
CPU Characteristics: Intel Turbo Boost Technology up to 2.53 GHz
CPU MHz: 2267
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (2 x 4 GB DDR3-1066 DR RDIMM)
Disk Subsystem: 2 x 73 GB 15000 RPM SAS
Other Hardware: None

Software

Operating System: Microsoft Windows Server 2008 Enterprise x64 Edition SP2
Compiler: Intel C++ Compiler for IA-32, Version 11.0 Build 20090131 Package ID: w_cproc_p_11.0.072 Microsoft Visual Studio 2008 SP1
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: MicroQuill SmartHeap Library 8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	1001	78.4	1004	77.6	1006	77.6							
401.bzip2	8	1254	61.6	1252	61.6	1253	61.6							
403.gcc	8	814	79.2	814	79.2	810	79.2							
429.mcf	8	676	108	670	109	670	109							
445.gobmk	8	914	92.0	911	92.0	913	92.0							
456.hmmer	8	1106	67.2	1104	68.0	1105	67.2							
458.sjeng	8	1134	85.6	1131	85.6	1134	85.6							
462.libquantum	8	700	237	699	238	699	238							
464.h264ref	8	1464	121	1463	121	1463	121							
471.omnetpp	8	735	68.0	733	68.0	735	68.0							
473.astar	8	996	56.8	997	56.0	997	56.0							
483.xalancbmk	8	644	85.6	644	85.6	643	85.6							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Binaries were built on Windows Vista Business (64-bit)

Base Compiler Invocation

C benchmarks:

icl -Qvc9 -Qstd=c99

C++ benchmarks:

icl -Qvc9

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32

464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Base Optimization Flags

C benchmarks:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx_features  
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/dell.flags.ic11.0.win.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/dell.flags.ic11.0.win.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

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