



# SPEC<sup>®</sup> CINT2006 Result

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## Hewlett-Packard Company

## SPECint<sup>®</sup>\_rate2006 = Not Run

ProLiant BL480c  
(3.33 GHz, Intel Xeon X5470)

## SPECint\_rate\_base2006 = 140

CPU2006 license: 3

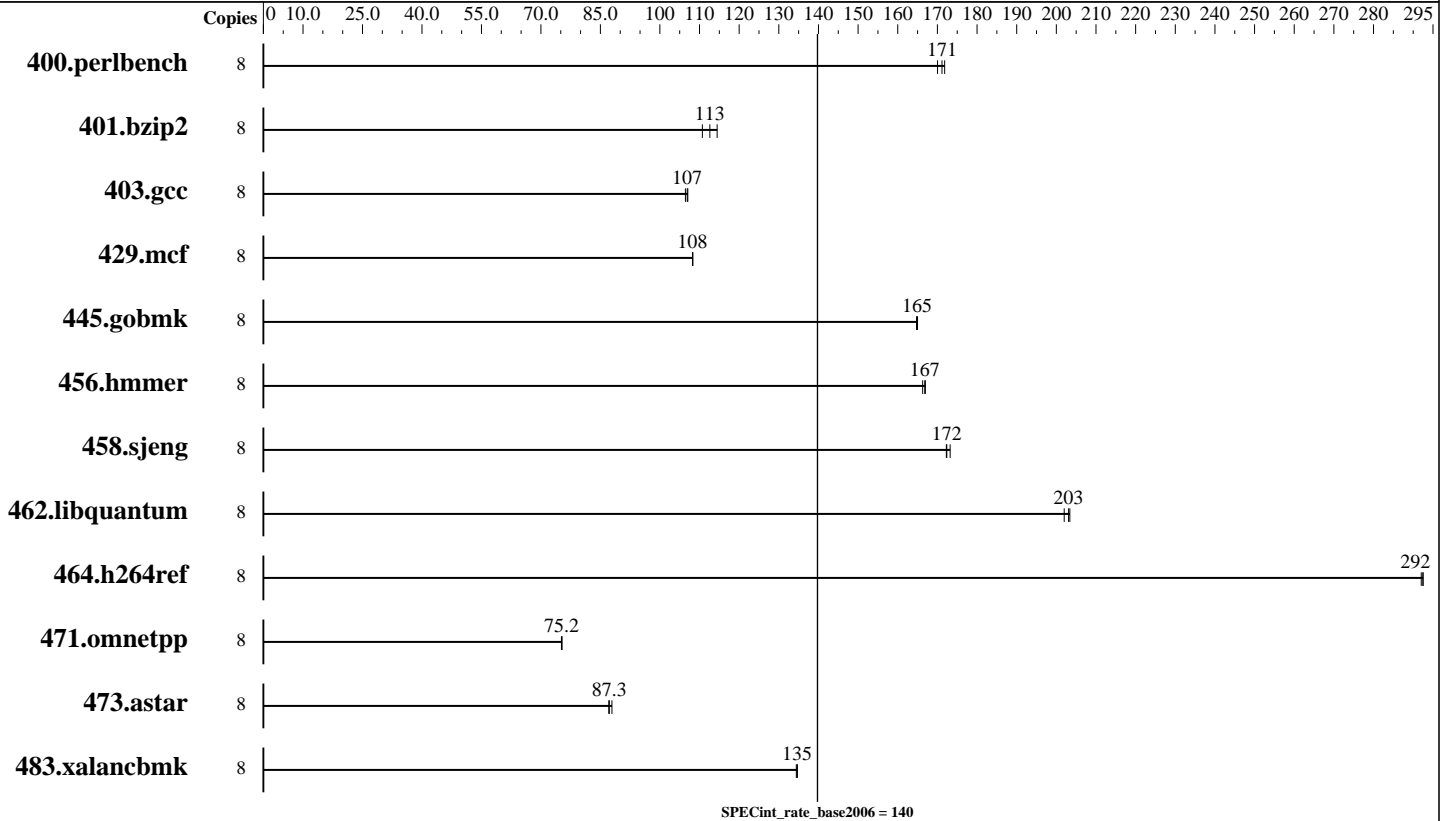
Test date: Aug-2008

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2008

Tested by: Hewlett-Packard Company

Software Availability: Nov-2008



### Hardware

CPU Name: Intel Xeon X5470  
 CPU Characteristics: 3.33 GHz, 2x6 MB L2 shared, 1333 MHz system bus  
 CPU MHz: 3333  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB PC2-5300F CL5)  
 Disk Subsystem: 1x146 GB 10 K SAS  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l\_cproc\_b\_11.0.042  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: MicroQuill SmartHeap Library 8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	<u>457</u>	<u>171</u>	455	172	460	170							
401.bzip2	8	697	111	<u>686</u>	<u>113</u>	675	114							
403.gcc	8	605	106	<u>602</u>	<u>107</u>	602	107							
429.mcf	8	<u>674</u>	<u>108</u>	674	108	674	108							
445.gobmk	8	<u>509</u>	<u>165</u>	509	165	509	165							
456.hammer	8	<u>447</u>	<u>167</u>	447	167	449	166							
458.sjeng	8	562	172	<u>562</u>	<u>172</u>	559	173							
462.libquantum	8	821	202	<u>816</u>	<u>203</u>	815	203							
464.h264ref	8	605	293	606	292	<u>605</u>	<u>292</u>							
471.omnetpp	8	665	75.2	<u>664</u>	<u>75.2</u>	664	75.3							
473.astar	8	639	87.9	<u>644</u>	<u>87.3</u>	645	87.1							
483.xalancbmk	8	<u>410</u>	<u>135</u>	410	135	411	134							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
taskset was used to bind processes to cores

## Platform Notes

BIOS configuration:  
Power Regulator set to Static High Performance Mode  
Adjacent Sector Prefetch Disabled  
Hardware Prefetcher Disabled

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Base Portability Flags (Continued)

483.xalanbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revD.html>  
<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090713.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revD.xml>  
<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090713.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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