



SPEC[®] CINT2006 Result

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Bull SAS

NovaScale B280
(Intel Xeon E5450, 3.00 GHz)

SPECint[®]_rate2006 = 129

SPECint_rate_base2006 = 105

CPU2006 license: 20

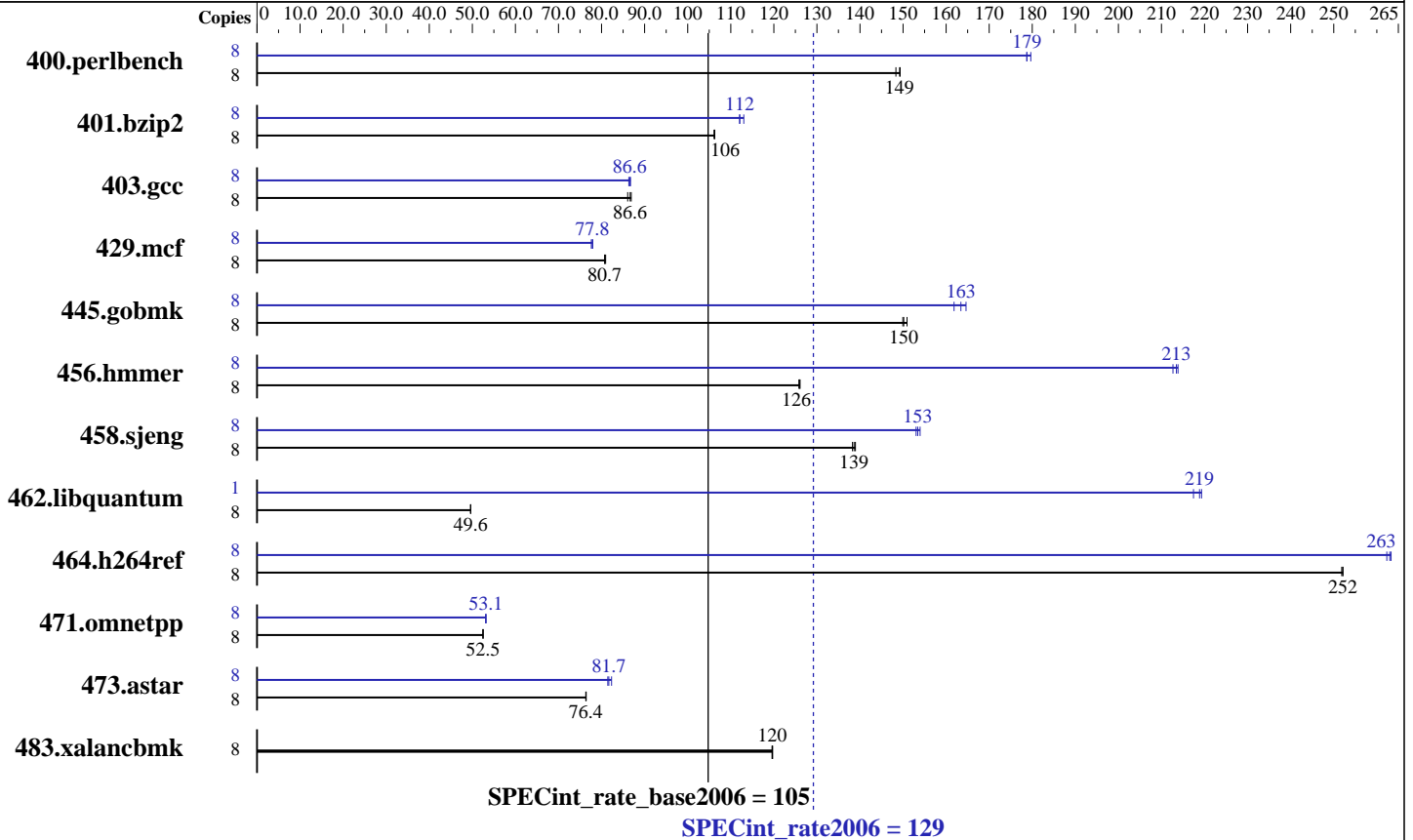
Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: Aug-2008

Hardware Availability: Jan-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5450
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB) FB-DIMM PC2-5300F ECC CL5
 Disk Subsystem: 1x73 GB SAS, 10000 RPM
 Other Hardware: None

Software

Operating System: SUSE LINUX Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ext2
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Binutils 2.17.50.0.15 SmartHeap library V8.1



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	527	148	<u>524</u>	<u>149</u>	523	149	8	435	180	<u>437</u>	<u>179</u>	437	179
401.bzip2	8	<u>726</u>	<u>106</u>	726	106	728	106	8	<u>689</u>	<u>112</u>	689	112	683	113
403.gcc	8	741	86.9	748	86.1	<u>743</u>	<u>86.6</u>	8	743	86.7	<u>744</u>	<u>86.6</u>	746	86.3
429.mcf	8	901	80.9	<u>904</u>	<u>80.7</u>	904	80.7	8	<u>937</u>	<u>77.8</u>	940	77.6	936	78.0
445.gobmk	8	560	150	<u>559</u>	<u>150</u>	556	151	8	<u>513</u>	<u>163</u>	519	162	510	165
456.hmmmer	8	593	126	592	126	<u>592</u>	<u>126</u>	8	349	214	<u>350</u>	<u>213</u>	351	213
458.sjeng	8	<u>698</u>	<u>139</u>	700	138	697	139	8	633	153	629	154	<u>631</u>	<u>153</u>
462.libquantum	8	<u>3341</u>	<u>49.6</u>	3341	49.6	3342	49.6	1	<u>94.7</u>	<u>219</u>	94.5	219	95.3	217
464.h264ref	8	<u>702</u>	<u>252</u>	702	252	703	252	8	672	263	675	262	<u>673</u>	<u>263</u>
471.omnetpp	8	953	52.5	953	52.5	<u>953</u>	<u>52.5</u>	8	941	53.1	941	53.1	<u>941</u>	<u>53.1</u>
473.astar	8	735	76.4	<u>735</u>	<u>76.4</u>	735	76.4	8	690	81.4	<u>688</u>	<u>81.7</u>	682	82.3
483.xalancbmk	8	461	120	<u>461</u>	<u>120</u>	462	120	8	461	120	<u>461</u>	<u>120</u>	462	120

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmmer, for peak, are compiled in 64-bit mode
BIOS settings :
Hardware Prefetcher : Enabled
Adjacent Cache-Line Prefetch : Disabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/EM64T_Intel101_int_flags.20090714.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/EM64T_Intel101_int_flags.20090714.xml

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