



# SPEC<sup>®</sup> CFP2006 Result

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## HITACHI

SPECfp<sup>®</sup>\_rate2006 = 56.6

### BladeSymphony BS320 (Intel Xeon X5260)

SPECfp\_rate\_base2006 = 50.3

CPU2006 license: 872

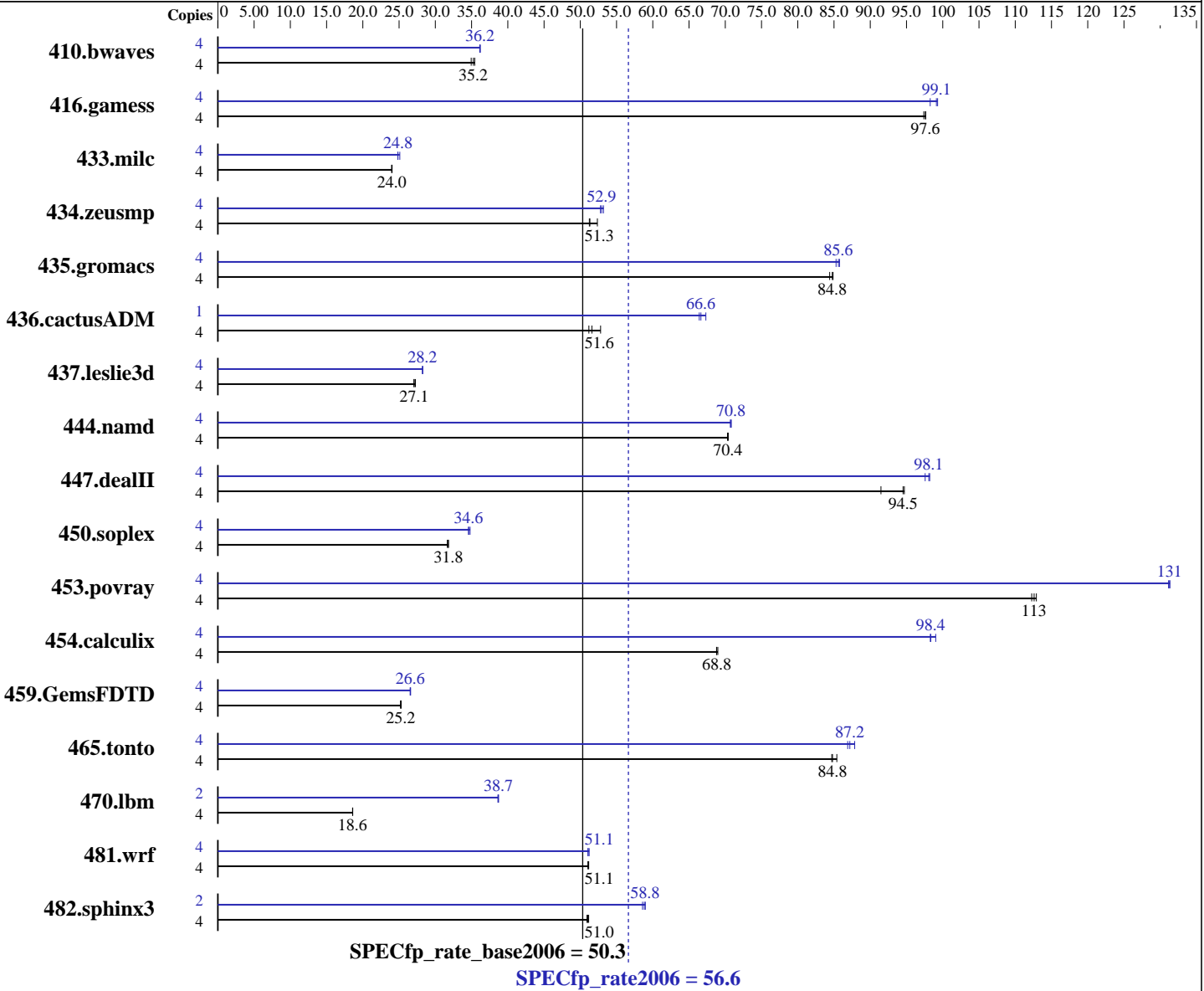
Test sponsor: HITACHI

Tested by: HITACHI

Test date: Feb-2008

Hardware Availability: Dec-2007

Software Availability: Nov-2007



#### Hardware

CPU Name: Intel Xeon X5260  
 CPU Characteristics: 1333MHz system bus  
 CPU MHz: 3333  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip

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#### Software

Operating System: Red Hat Enterprise Linux Server release 5.1 (Tikanga)  
 Kernel 2.6.18-53.el5 on an x86\_64  
 Compiler: Intel C++ and Fortran Compiler 10.1 for Linux  
 Build 20070913 Package ID: l\_cc\_p\_10.1.008, l\_fc\_p\_10.1.008

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L3 Cache: None  
Other Cache: None  
Memory: 16 GB(4 x 4 GB PC2-5300F CAS 5-5-5)  
Disk Subsystem: 1 x 147 GB 10000 rpm SAS  
Other Hardware: None

Auto Parallel: Yes  
File System: ext3  
System State: Multi-user run level 3  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	<b><u>1542</u></b>	<b><u>35.2</u></b>	1555	34.9	1534	35.4	4	1502	36.2	1505	36.1	<b><u>1503</u></b>	<b><u>36.2</u></b>
416.gamess	4	<b><u>802</u></b>	<b><u>97.6</u></b>	804	97.4	802	97.7	4	<b><u>790</u></b>	<b><u>99.1</u></b>	789	99.3	797	98.3
433.milc	4	<b><u>1530</u></b>	<b><u>24.0</u></b>	1531	24.0	1528	24.0	4	1463	25.1	<b><u>1478</u></b>	<b><u>24.8</u></b>	1478	24.8
434.zeusmp	4	710	51.2	<b><u>709</u></b>	<b><u>51.3</u></b>	695	52.4	4	<b><u>687</u></b>	<b><u>52.9</u></b>	690	52.8	684	53.2
435.gromacs	4	338	84.4	<b><u>337</u></b>	<b><u>84.8</u></b>	337	84.9	4	<b><u>334</u></b>	<b><u>85.6</u></b>	333	85.8	335	85.3
436.cactusADM	4	934	51.2	905	52.8	<b><u>926</u></b>	<b><u>51.6</u></b>	1	180	66.4	178	67.3	<b><u>179</u></b>	<b><u>66.6</u></b>
437.leslie3d	4	1390	27.1	<b><u>1388</u></b>	<b><u>27.1</u></b>	1380	27.3	4	1332	28.2	<b><u>1331</u></b>	<b><u>28.2</u></b>	1331	28.3
444.namd	4	456	70.4	<b><u>456</u></b>	<b><u>70.4</u></b>	456	70.3	4	453	70.8	454	70.7	<b><u>453</u></b>	<b><u>70.8</u></b>
447.dealII	4	500	91.5	<b><u>484</u></b>	<b><u>94.5</u></b>	483	94.7	4	466	98.2	469	97.6	<b><u>467</u></b>	<b><u>98.1</u></b>
450.soplex	4	1049	31.8	<b><u>1049</u></b>	<b><u>31.8</u></b>	1054	31.7	4	<b><u>964</u></b>	<b><u>34.6</u></b>	959	34.8	965	34.6
453.povray	4	188	113	<b><u>189</u></b>	<b><u>113</u></b>	190	112	4	162	131	162	131	<b><u>162</u></b>	<b><u>131</u></b>
454.calculix	4	478	69.0	<b><u>480</u></b>	<b><u>68.8</u></b>	480	68.8	4	333	99.0	<b><u>336</u></b>	<b><u>98.4</u></b>	336	98.3
459.GemsFDTD	4	1683	25.2	<b><u>1682</u></b>	<b><u>25.2</u></b>	1678	25.3	4	1600	26.5	1596	26.6	<b><u>1598</u></b>	<b><u>26.6</u></b>
465.tonto	4	<b><u>464</u></b>	<b><u>84.8</u></b>	461	85.4	465	84.7	4	<b><u>451</u></b>	<b><u>87.2</u></b>	453	86.9	448	87.9
470.lbm	4	2957	18.6	<b><u>2956</u></b>	<b><u>18.6</u></b>	2953	18.6	2	<b><u>710</u></b>	<b><u>38.7</u></b>	710	38.7	711	38.6
481.wrf	4	875	51.1	873	51.2	<b><u>875</u></b>	<b><u>51.1</u></b>	4	872	51.2	875	51.1	<b><u>875</u></b>	<b><u>51.1</u></b>
482.sphinx3	4	1531	50.9	1524	51.2	<b><u>1528</u></b>	<b><u>51.0</u></b>	2	<b><u>663</u></b>	<b><u>58.8</u></b>	665	58.6	661	59.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
'/bin/taskset' used to bind processes to CPUs  
OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to physical,0

## Base Compiler Invocation

C benchmarks:  
icc

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**CPU2006 license:** 872

**Test sponsor:** HITACHI

**Tested by:** HITACHI

**Test date:** Feb-2008

**Hardware Availability:** Dec-2007

**Software Availability:** Nov-2007

## Base Compiler Invocation (Continued)

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-fast

Fortran benchmarks:

-fast

Benchmarks using both Fortran and C:

-fast



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## Peak Compiler Invocation

C benchmarks (except as noted below):

```
/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include
```

433.milc: icc

C++ benchmarks (except as noted below):

icpc

```
450.soplex: /opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include
```

Fortran benchmarks (except as noted below):

ifort

```
437.leslie3d: /opt/intel/fc/10.1.008/bin/ifort -L/opt/intel/fc/10.1.008/lib
-I/opt/intel/fc/10.1.008/include
```

Benchmarks using both Fortran and C:

icc ifort

## Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

## Peak Optimization Flags

C benchmarks:

```
433.milc: -prof_gen(pass 1) -prof_use(pass 2) -fast -fno-alias
-auto-ilp32
```

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## Peak Optimization Flags (Continued)

470.lbm: -prof\_gen(pass 1) -prof\_use(pass 2) -fast -unroll2  
-scalar-rep- -prefetch -opt-malloc-options=3

482.sphinx3: -fast -unroll2

### C++ benchmarks:

444.namd: -prof\_gen(pass 1) -prof\_use(pass 2) -fast -fno-alias  
-auto-ilp32

447.dealII: -prof\_gen(pass 1) -prof\_use(pass 2) -fast -unroll2  
-ansi-alias -scalar-rep-

450.soplex: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-opt-malloc-options=3

453.povray: -prof\_gen(pass 1) -prof\_use(pass 2) -fast -unroll4  
-ansi-alias

### Fortran benchmarks:

410.bwaves: -fast -prefetch

416.gamess: -prof\_gen(pass 1) -prof\_use(pass 2) -fast -unroll2 -Ob0  
-ansi-alias -scalar-rep-

434.zeusmp: -prof\_gen(pass 1) -prof\_use(pass 2) -fast

437.leslie3d: -prof\_gen(pass 1) -prof\_use(pass 2) -fast -prefetch  
-opt-malloc-options=3

459.GemsFDTD: -prof\_gen(pass 1) -prof\_use(pass 2) -fast -unroll2 -Ob0  
-prefetch

465.tonto: -prof\_gen(pass 1) -prof\_use(pass 2) -fast -unroll4 -auto

### Benchmarks using both Fortran and C:

435.gromacs: -prof\_gen(pass 1) -prof\_use(pass 2) -fast -prefetch  
-auto-ilp32

436.cactusADM: -prof\_gen(pass 1) -prof\_use(pass 2) -fast -unroll2  
-prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

481.wrf: -fast -auto-ilp32



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090713.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090713.01.xml>

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