SPEC® CFP2006 Result

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Fujitsu Siemens Computers

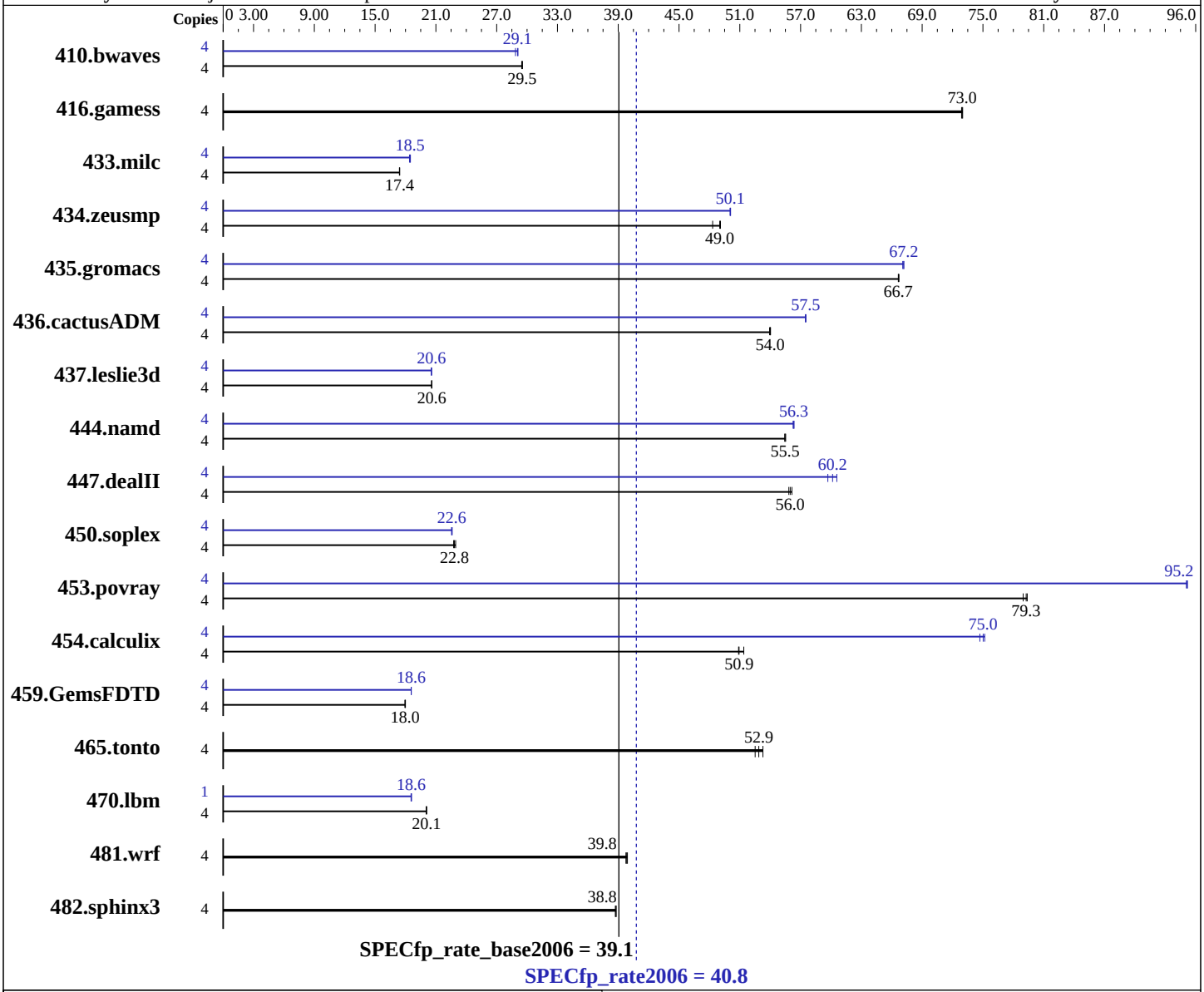
CELSIUS M460, Intel Core 2 Quad Q6700 processor

SPECfp®_rate2006 = 40.8

SPECfp_rate_base2006 = 39.1

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Jan-2008
Hardware Availability: Nov-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Core 2 Quad Q6700
CPU Characteristics:	
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	Microsoft Windows Vista Ultimate (x64)
Compiler:	Intel C++ and Fortran Compilers for Intel64, Version 10.1, Build 20070913 Microsoft Visual Studio 2005 with SP1 (for libraries)
Auto Parallel:	No
File System:	NTFS

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L3 Cache: None
Other Cache: None
Memory: 4 GB (4x1 GB PC2-6400 CL6 SDRAM)
Disk Subsystem: 1 x 400 GB SATA II 7200 RPM
Other Hardware: None

System State: Default
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other Software: MicroQuill SmartHeap Library, Version 8.0 (64 bit)

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1845	29.5	1840	29.5	**1841**	**29.5**	4	1884	28.9	1868	29.1	**1871**	**29.1**
416.gamess	4	**1074**	**73.0**	1074	72.9	1073	73.0	4	**1074**	**73.0**	1074	72.9	1073	73.0
433.milc	4	2107	17.4	2111	17.4	**2109**	**17.4**	4	1986	18.5	**1990**	**18.5**	1996	18.4
434.zeusmp	4	753	48.3	**743**	**49.0**	741	49.1	4	727	50.1	**727**	**50.1**	728	50.0
435.gromacs	4	429	66.6	428	66.7	**428**	**66.7**	4	425	67.2	**425**	**67.2**	426	67.1
436.cactusADM	4	886	53.9	885	54.0	**885**	**54.0**	4	832	57.5	**831**	**57.5**	831	57.5
437.leslie3d	4	**1827**	**20.6**	1827	20.6	1826	20.6	4	**1829**	**20.6**	1830	20.6	1828	20.6
444.namd	4	578	55.5	**578**	**55.5**	579	55.4	4	569	56.3	570	56.3	**569**	**56.3**
447.dealII	4	815	56.1	819	55.8	**817**	**56.0**	4	**761**	**60.2**	755	60.6	767	59.7
450.soplex	4	1466	22.8	1454	23.0	**1461**	**22.8**	4	**1479**	**22.6**	1476	22.6	1479	22.6
453.povray	4	**268**	**79.3**	268	79.4	269	79.0	4	**224**	**95.2**	224	95.1	224	95.2
454.calculix	4	642	51.4	649	50.9	**648**	**50.9**	4	439	75.2	442	74.7	**440**	**75.0**
459.GemsFDTD	4	2365	17.9	2358	18.0	**2363**	**18.0**	4	**2286**	**18.6**	2286	18.6	2287	18.6
465.tonto	4	**744**	**52.9**	739	53.3	750	52.5	4	**744**	**52.9**	739	53.3	750	52.5
470.lbm	4	**2737**	**20.1**	2736	20.1	2743	20.0	1	**740**	**18.6**	741	18.5	739	18.6
481.wrf	4	1120	39.9	1124	39.8	**1122**	**39.8**	4	1120	39.9	1124	39.8	**1122**	**39.8**
482.sphinx3	4	2010	38.8	**2010**	**38.8**	2014	38.7	4	2010	38.8	**2010**	**38.8**	2014	38.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
BIOS default settings have been used.
```

General Notes

```
For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
icl -Qvc8

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc8 -Qc99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -Qlowercase -assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 -TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -Qlowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
-fast -F950000000 shlW64M.lib

C++ benchmarks:
-fast -Qcxx-features -F950000000 shlW64M.lib

Fortran benchmarks:
-fast -F950000000

Benchmarks using both Fortran and C:
-fast -F950000000

Peak Compiler Invocation

C benchmarks:
icl -Qvc8 -Qc99

C++ benchmarks:
icl -Qvc8

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc8 -Qc99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F950000000

470.lbm: -fast -Qunroll2 -Qscalar-rep- -Qprefetch -F950000000

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -fast -Oa -F950000000 shlW64M.lib

447.dealII: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features -F950000000

450.soplex: Same as 447.dealII

453.povray: Same as 447.dealII

Fortran benchmarks:

410.bwaves: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F950000000

416.gamess: basepeak = yes

434.zeusmp: -QxT -O2 -Qprec-div- -Qunroll0 -Qscalar-rep- -F950000000

Continued on next page

Peak Optimization Flags (Continued)

437.leslie3d: `Same as 410.bwaves`

459.GemsFDTD: `-fast -Qunroll2 -Ob0 -Qprefetch -F950000000`

465.tonto: `basepeak = yes`

Benchmarks using both Fortran and C:

435.gromacs: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -F950000000`

436.cactusADM: `-fast -Qunroll2 -Qprefetch -F950000000`

454.calculix: `-fast -Qunroll-aggressive -F950000000`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.03.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.03.xml`

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For other inquiries, please contact webmaster@spec.org.

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