SPEC® CINT2006 Result
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Intel Corporation

Supermicro X7DB8+ (Intel Xeon processor 5150, 2.66 GHz)

SPECint®_rate2006 = 60.4
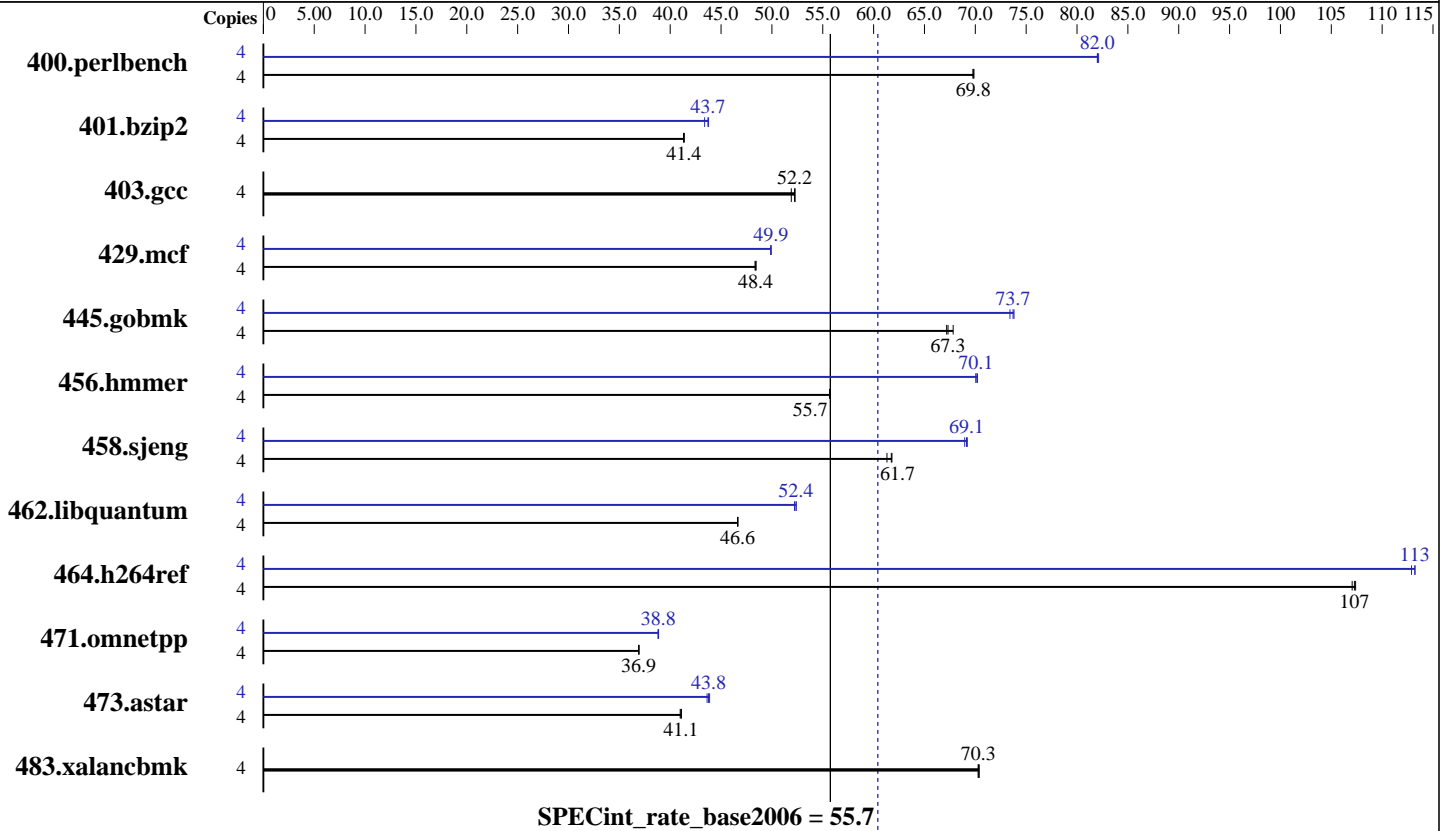
SPECint_rate_base2006 = 55.7

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: May-2007
Hardware Availability: Nov-2006
Software Availability: Jun-2007

Copies

Benchmark	Copies	Value
400.perlbench	4	82.0
400.perlbench	4	69.8
401.bzip2	4	43.7
401.bzip2	4	41.4
403.gcc	4	52.2
429.mcf	4	49.9
429.mcf	4	48.4
445.gobmk	4	73.7
445.gobmk	4	67.3
456.hmmer	4	70.1
456.hmmer	4	55.7
458.sjeng	4	69.1
458.sjeng	4	61.7
462.libquantum	4	52.4
462.libquantum	4	46.6
464.h264ref	4	113
464.h264ref	4	107
471.omnetpp	4	38.8
471.omnetpp	4	36.9
473.astar	4	43.8
473.astar	4	41.1
483.xalancbmk	4	70.3

SPECint_rate_base2006 = 55.7

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Hardware

CPU Name: Intel Xeon 5150
CPU Characteristics: Dual Core, 2.66 GHz
CPU MHz: 2666
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip
L3 Cache: None
Other Cache: None
Memory: 8 GB (8 * 1GB Samsung DDR2 5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem: Seagate, SCSI, 73GB, 10Krpm, 1 disk only
Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp for x86_64
Compiler: Intel C++ Compiler for Linux32 version 10.0 Build 20070426 Package ID: l_cc_p_10.0.023
Auto Parallel: No
File System: ReiserFS
System State: Multi-user, run level 3
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: SmartHeap library V8.1 Binutils 2.17.50.0.15

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Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**560**	**69.8**	559	69.9	560	69.8	4	477	82.0	**477**	**82.0**	476	82.1
401.bzip2	4	935	41.3	**933**	**41.4**	933	41.4	4	890	43.4	882	43.8	**883**	**43.7**
403.gcc	4	620	51.9	616	52.3	**617**	**52.2**	4	620	51.9	616	52.3	**617**	**52.2**
429.mcf	4	**754**	**48.4**	753	48.4	755	48.3	4	731	49.9	**731**	**49.9**	731	49.9
445.gobmk	4	**623**	**67.3**	625	67.2	619	67.8	4	569	73.8	572	73.4	**569**	**73.7**
456.hmmer	4	670	55.7	**670**	**55.7**	670	55.7	4	533	70.1	**533**	**70.1**	531	70.2
458.sjeng	4	783	61.8	**784**	**61.7**	789	61.3	4	699	69.2	702	69.0	**700**	**69.1**
462.libquantum	4	**1777**	**46.6**	1777	46.7	1777	46.6	4	1587	52.2	**1583**	**52.4**	1582	52.4
464.h264ref	4	827	107	824	107	**825**	**107**	4	782	113	784	113	**782**	**113**
471.omnetpp	4	677	36.9	**677**	**36.9**	677	36.9	4	643	38.9	644	38.8	**644**	**38.8**
473.astar	4	683	41.1	685	41.0	**684**	**41.1**	4	644	43.6	640	43.9	**642**	**43.8**
483.xalancbmk	4	393	70.3	**393**	**70.3**	392	70.4	4	393	70.3	**393**	**70.3**	392	70.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Bios settings:
Hardware Prefetcher:       Enabled
Adjacent Sector Prefetch: Disabled
ulimit -s unlimited used to set stack size to unlimited
taskset command used to assign copies to cores
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode
```

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

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Base Optimization Flags

C benchmarks:
    -fast

C++ benchmarks:
    -xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
    -L/spec/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc

    401.bzip2: /opt/intel/cce/10.0.023/bin/icc
               -L/opt/intel/cce/10.0.023/lib
               -I/opt/intel/cce/10.0.023/include

    456.hmmer: /opt/intel/cce/10.0.023/bin/icc
               -L/opt/intel/cce/10.0.023/lib
               -I/opt/intel/cce/10.0.023/include

C++ benchmarks:
    icpc

Peak Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
       401.bzip2: -DSPEC_CPU_LP64
       456.hmmer: -DSPEC_CPU_LP64
   462.libquantum: -DSPEC_CPU_LINUX
   483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

```
    400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
                   -prefetch

        401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast

          403.gcc: basepeak = yes

          429.mcf: -fast -prefetch

        445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
                   -no-prec_div -ansi-alias

        456.hmmer: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
                   -ansi-alias

        458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

   462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0
                   -prefetch -opt-streaming-stores always

       464.h264ref: Same as 456.hmmer
```

C++ benchmarks:

```
      471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
                   -no-prec_div -ansi-alias -Wl,-z,muldefs
                   -L/spec/cpu2006.1.0/lib -lsmartheap

        473.astar: Same as 471.omnetpp

     483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
          403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090715.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090715.00.xml

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