



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

PRIMERGY TX150 S5, Intel Pentium D processor 945,
3.40 GHz

SPECint®_rate2006 = 21.4

SPECint_rate_base2006 = 20.5

CPU2006 license: 22

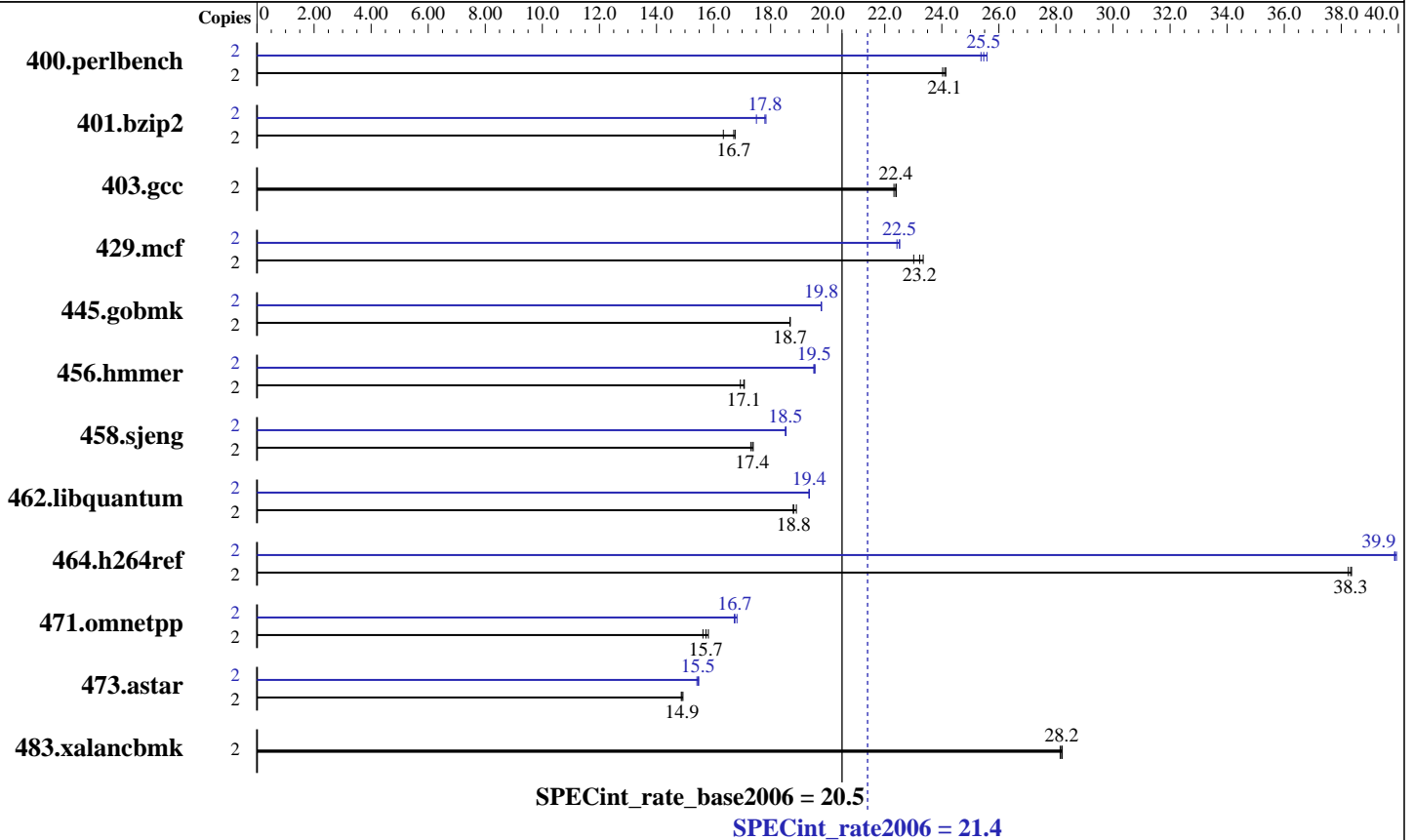
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: May-2007

Hardware Availability: Sep-2006

Software Availability: Mar-2007



Hardware

CPU Name: Intel Pentium D 945
 CPU Characteristics: 800 MHz system bus
 CPU MHz: 3400
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 12 K micro-ops I + 16 KB D on chip per core
 Secondary Cache: 2 MB I+D on chip per core
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4x2 GB DDR2 PC2-4200E, 2 rank, CAS 4-4-4, with ECC)
 Disk Subsystem: SATA(160GB 7200 rpm)
 Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
 Auto Parallel: No
 File System: ReiserFS
 System State: Multiuser, Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Smart Heap Library, Version 8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	809	24.1	811	24.1	813	24.0	2	764	25.6	767	25.5	770	25.4
401.bzip2	2	1152	16.8	1155	16.7	1181	16.3	2	1082	17.8	1103	17.5	1084	17.8
403.gcc	2	721	22.3	719	22.4	719	22.4	2	721	22.3	719	22.4	719	22.4
429.mcf	2	793	23.0	781	23.3	785	23.2	2	813	22.4	810	22.5	810	22.5
445.gobmk	2	1123	18.7	1123	18.7	1123	18.7	2	1061	19.8	1061	19.8	1061	19.8
456.hmmer	2	1101	16.9	1092	17.1	1094	17.1	2	956	19.5	956	19.5	954	19.6
458.sjeng	2	1398	17.3	1394	17.4	1392	17.4	2	1305	18.5	1307	18.5	1306	18.5
462.libquantum	2	2192	18.9	2203	18.8	2205	18.8	2	2142	19.3	2141	19.4	2140	19.4
464.h264ref	2	1154	38.4	1157	38.2	1155	38.3	2	1110	39.9	1110	39.9	1108	39.9
471.omnetpp	2	800	15.6	794	15.7	790	15.8	2	747	16.7	743	16.8	746	16.7
473.astar	2	943	14.9	941	14.9	944	14.9	2	907	15.5	911	15.4	908	15.5
483.xalancbmk	2	489	28.2	490	28.1	489	28.2	2	489	28.2	490	28.1	489	28.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

General Notes

The system bus runs at 800 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmartheap

445.gobmk: Same as 429.mcf

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo
-no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmartheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml

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