SPEC® CINT2006 Result

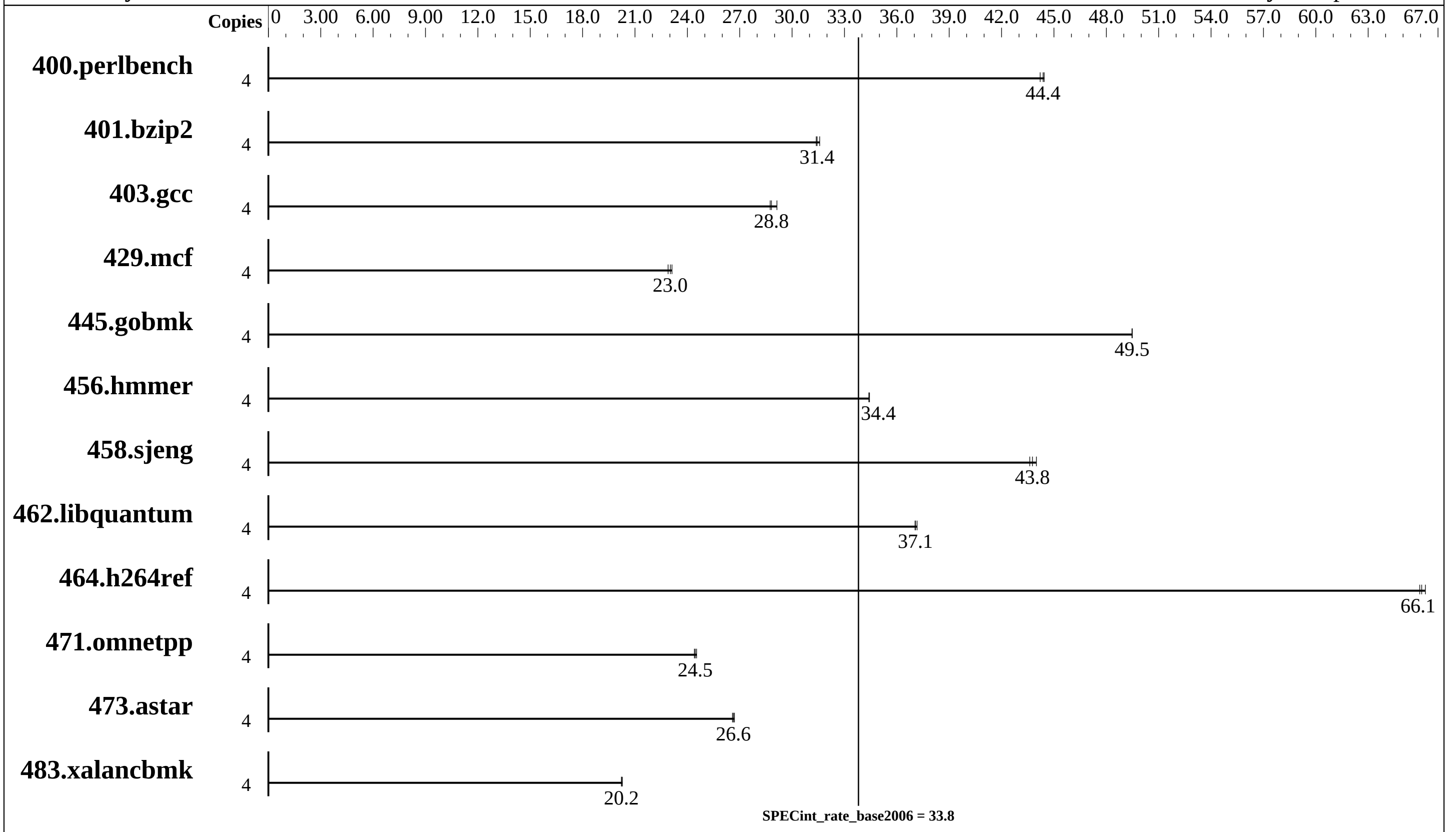
Tyan
(Test Sponsor: Advanced Micro Devices)

Tyan Thunder K8SD Pro (S2882-D) (AMD Opteron 275)

SPECint®_rate2006 = --

SPECint_rate_base2006 = 33.8

CPU2006 license: 49		**Test date:**	Apr-2006
Test sponsor:	Advanced Micro Devices	**Hardware Availability:**	May-2005
Tested by:	Advanced Micro Devices	**Software Availability:**	Apr-2006



Hardware

CPU Name:	AMD Opteron 275
CPU Characteristics:	
CPU MHz:	2200
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8 x 1 GB DDR400 CL3 Reg)
Disk Subsystem:	250GB SATA
Other Hardware:	None

Software

Operating System:	SUSE Linux 10.1 (for AMD64)
Compiler:	gcc , g++ 4.1.0 (for AMD64)
Auto Parallel:	No
File System:	ext3
System State:	runlevel 3
Base Pointers:	64-bit
Peak Pointers:	Not Applicable
Other Software:	None

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Results Table

	Base							Peak						
Benchmark	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**
400.perlbench	4	879	44.4	884	44.2	**881**	**44.4**							
401.bzip2	4	1222	31.6	1230	31.4	**1228**	**31.4**							
403.gcc	4	1105	29.1	1120	28.7	**1117**	**28.8**							
429.mcf	4	**1584**	**23.0**	1593	22.9	1578	23.1							
445.gobmk	4	848	49.5	848	49.5	**848**	**49.5**							
456.hmmer	4	1084	34.4	1085	34.4	**1084**	**34.4**							
458.sjeng	4	1100	44.0	1110	43.6	**1106**	**43.8**							
462.libquantum	4	2237	37.0	2230	37.2	**2236**	**37.1**							
464.h264ref	4	1336	66.3	**1340**	**66.1**	1342	66.0							
471.omnetpp	4	1019	24.5	1025	24.4	**1022**	**24.5**							
473.astar	4	1052	26.7	**1054**	**26.6**	1056	26.6							
483.xalancbmk	4	1365	20.2	**1365**	**20.2**	1361	20.3							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested by Advanced Micro Devices
 The system can be assembled using an ATX power supply
 such as an Antec true 550, and a case such as the
 Chenbro SR105

Taskset utility used to bind process to cores.
```

Base Compiler Invocation

C benchmarks:
`gcc`

C++ benchmarks:
`g++`

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-O3 -fno-inline-functions -funroll-loops

C++ benchmarks:

-O3 -funroll-loops

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.04.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.04.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v91.
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