



CINT2000 Result

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Itautec

Servidor Itautec MX221 (Intel Xeon processor E5320, 1.86 GHz)

SPECint_rate2000 = 147

SPECint_rate_base2000 = 147

SPEC license #: 9001 | Tested by: Itautec | Test date: Feb-2007 | Hardware Avail: Feb-2007 | Software Avail: May-2006

| Benchmark | Base Copies | Base Runtime | Base Ratio | Copies | Runtime | Ratio |
|-------------|-------------|--------------|------------|--------|---------|-------|
| 164.gzip | 8 | 126 | 103 | 8 | 126 | 103 |
| 175.vpr | 8 | 124 | 105 | 8 | 124 | 105 |
| 176.gcc | 8 | 60.4 | 169 | 8 | 60.4 | 169 |
| 181.mcf | 8 | 155 | 108 | 8 | 155 | 108 |
| 186.crafty | 8 | 58.4 | 159 | 8 | 58.4 | 159 |
| 197.parser | 8 | 132 | 127 | 8 | 132 | 127 |
| 252.eon | 8 | 53.9 | 224 | 8 | 53.9 | 224 |
| 253.perlbnk | 8 | 86.6 | 193 | 8 | 86.6 | 193 |
| 254.gap | 8 | 93.4 | 109 | 8 | 93.4 | 109 |
| 255.vortex | 8 | 70.8 | 249 | 8 | 70.8 | 249 |
| 256.bzip2 | 8 | 126 | 110 | 8 | 126 | 110 |
| 300.twolf | 8 | 141 | 197 | 8 | 141 | 197 |

Hardware

CPU: Intel Xeon processor E5320 (1.86 GHz, 2x4MB L2, 1066 MHz system bus)
CPU MHz: 1860
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2
Parallel: No
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per chip (4 MB shared per 2 cores)
L3 Cache: N/A
Other Cache: N/A
Memory: 8x1GB DDR2-RAM PC2-5300F (CAS 5-5-5)
Disk Subsystem: 1 x 73GB SCSI 10000RPM
Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler: Intel C++ Compiler 9.1
Build 20060519Z (for 32-bit applications),
Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)
Microquill SmartHeap Library v.8.0 for SMP
File System: NTFS
System State: Default

Notes/Tuning Information

GENERAL

Basepeak=yes is used for all programs
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Portability flags

176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Base tuning flags

for C programs: -fast +FDO shlSMPMt.lib
for C++ programs: -fast -Qcxx_features +FDO shlSMPMt.lib

EXTRA LIBRARIES

MicroQuill SmartHeap Library v.8.0 for SMP
<http://www.microquill.com>

The Servidor Itautec MX221 and the Servidor Itautec MX201 are electronically equivalent.