



CINT2000 Result

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Dell

PowerEdge 840 (Intel Xeon processor X3220, 2.40GHz)

SPECint_rate2000 = 102

SPECint_rate_base2000 = 101

SPEC license #: 55 | Tested by: Dell, Round Rock, TX | Test date: Jan-2007 | Hardware Avail: Jan-2007 | Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	4	95.4	68.1	4	95.5	68.1
175.vpr	4	88.3	73.6	4	87.0	74.7
176.gcc	4	43.4	118	4	43.4	118
181.mcf	4	90.4	92.4	4	90.4	92.4
186.crafty	4	45.7	102	4	45.7	101
197.parser	4	104	80.2	4	104	80.3
252.eon	4	41.3	146	4	41.4	146
253.perlbnk	4	62.4	134	4	62.4	134
254.gap	4	63.4	80.5	4	62.7	81.4
255.vortex	4	50.5	174	4	50.5	174
256.bzip2	4	94.9	73.3	4	91.9	75.8
300.twolf	4	109	128	4	109	128

Hardware

CPU: Intel Xeon processor X3220 (1066MHz system bus)
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1
Parallel: No
Primary Cache: 32KB(I) + 32KB(D) on chip per core
Secondary Cache: 8 MB I+D on chip (4 MB shared per 2 cores)
L3 Cache: N/A
Other Cache: N/A
Memory: 4 x 1GB DDR2-667 Dual Ranked SDRAM
Disk Subsystem: 1 x 73GB SAS 10000 RPM
Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition
Compiler: Intel C++ Compiler 9.1 for IA32(20060519Z)
Microsoft Visual Studio .NET 2003(7.1.3088)
MicroQuill SmartHeap Library 8.0
File System: NTFS
System State: Default

Notes/Tuning Information

GENERAL

ONESTEP=yes
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

PORTABILITY FLAGS

176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

BASE TUNING

C: -fast +FDO shlw32M.lib
C++: -fast -Qcxx_features +FDO

PEAK TUNING

164.gzip: -fast +FDO
175.vpr: -fast +FDO
176.gcc: basepeak=yes
181.mcf: basepeak=yes
186.crafty: -fast -Oa +FDO shlw32M.lib
197.parser: -fast +FDO
252.eon: -fast +FDO
253.perlbnk: basepeak=yes



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Notes/Tuning Information (Continued)

254.gap: -fast +FDO
255.vortex basepeak=yes
256.bzip2: -fast -Qunroll11 -Oa +FDO
300.twolf: basepeak=yes
Adjacent Sector Prefetch disabled in BIOS, default enabled