



CFP2000 Result

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ION
A20 (Intel Xeon processor 5080, 3.73GHz)

SPECfp2000 = 2018
SPECfp_base2000 = 2018

SPEC license #: 2697 Tested by: ION Test date: Jul-2006 Hardware Avail: Jul-2006 Software Avail: Jun-2006

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
168.wupwise	1600	46.4	3452	46.4	3452	
171.swim	3100	117	2658	117	2658	
172.mgrid	1800	98.6	1826	98.6	1826	
173.applu	2100	114	1849	114	1849	
177.mesa	1400	72.7	1924	72.7	1924	
178.galgel	2900	82.8	3502	82.8	3502	
179.art	2600	42.8	6074	42.8	6074	
183.quake	1300	62.7	2074	62.7	2074	
187.facerec	1900	111	1718	111	1718	
188.amp	2200	161	1367	161	1367	
189.lucas	2000	115	1747	115	1747	
191.fma3d	2100	121	1743	121	1743	
200.sixtrack	1100	151	731	151	731	
301.apsi	2600	192	1355	192	1355	

Hardware

CPU: Intel Xeon Processor 5080 (1066Mhz System Bus)
CPU MHz: 3733
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip (HyperThreading Technology disabled)
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 12KB(I) + 16KB(D) on chip, per core
Secondary Cache: 2MB (I+D) on chip, per core
L3 Cache: None
Other Cache: None
Memory: 8 GB (8*1GB PC2-5300 FB-DIMMs)
Disk Subsystem: 1 x 400GB SATA, 7200rpm
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux ES 4 Update 3 for AMD64/Intel EM64T
Compiler: Intel(R) Fortran Compiler for EM64T, V9.1 (Build 20060519)
Intel(R) C++ Compiler for EM64T, V9.1 (Build 20060519)
File System: ext3
System State: Multi-user

Notes/Tuning Information

Tested by ION Computer Systems
BIOS 2.00 Build 54
HyperThreading Technology Disabled in BIOS
Enhanced SpeedStep Disabled in BIOS
kernel 2.6.9-34.0.1.ELsmp
+FDO: PASS1=-prof_gen PASS2=-prof_use

SPEC CFP2000 Baseline optimization flags
C programs: -fast -ansi_alias +FDO
Fortran programs: -fast +FDO
+FDO: PASS1=-prof_gen PASS2=-prof_use

SPEC CFP2000 Peak optimization flags:
basepeak=yes set globally

Portability Flags:
178.galgel: -FI