



CINT2000 Result

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Hewlett-Packard Company

ProLiant DL360 G5 (3.73GHz, Intel Xeon processor 5080)

SPECint_rate2000 = 82.5

SPECint_rate_base2000 = 81.9

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Jun-2006 Hardware Avail: Jun-2006 Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	8	175	74.3	8	175	74.2
175.vpr	8	257	50.6	8	246	52.9
176.gcc	8	89.6	114	8	89.6	114
181.mcf	8	423	39.5	8	421	39.7
186.crafty	8	147	63.2	8	142	65.1
197.parser	8	201	82.9	8	200	83.6
252.eon	8	95.1	127	8	95.3	127
253.perlbnk	8	177	94.1	8	176	95.0
254.gap	8	113	90.3	8	114	89.7
255.vortex	8	130	136	8	130	136
256.bzip2	8	210	66.3	8	210	66.3
300.twolf	8	262	106	8	264	106

Hardware

CPU: Intel Xeon processor 5080 (3.73GHz, 2x2MB L2, 1066MHz bus)
CPU MHz: 3730
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip (Hyper-Threading Technology enabled)
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 12K micro-ops I + 16KBD (on chip) per core
Secondary Cache: 2048KB(I+D) (on chip) per core
L3 Cache: N/A
Other Cache: N/A
Memory: 8x1024MB PC2-5300F
Disk Subsystem: 1x36GB 10K SAS
Other Hardware:

Software

Operating System: Windows Server 2003 Enterprise SP1
Compiler: Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z)
Microsoft Visual Studio .NET 7.0.9466 (for libraries)
MicroQuill Smartheap Library 7.0
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO ONESTEP=yes shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO ONESTEP=yes
Portability flags:
 176.gcc: -Dalloca=_alloca /F10000000
 186.crafty: -DNT_i386
 253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
 254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: -fast +FDO ONESTEP=yes
175.vpr: -fast +FDO ONESTEP=yes
176.gcc: basepeak=1
181.mcf: -fast +FDO ONESTEP=yes shlw32M.lib
186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib
197.parser: -fast +FDO ONESTEP=yes
252.eon: -fast +FDO
253.perlbnk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib
254.gap: -fast +FDO ONESTEP=yes
255.vortex: -fast +FDO ONESTEP=yes shlw32M.lib
256.bzip2: basepeak=1
```



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Notes/Tuning Information (Continued)

300.twolf: -fast +FDO shlw32M.lib
BIOS Configuration Notes
Power Regulator set to Static High Performance Mode
Hyper-Threading Technology enabled