



# CINT2000 Result

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**Hewlett-Packard Company**  
ProLiant ML350 G5 (3.0GHz, Intel Xeon processor 5160)

SPECint\_rate2000 = **61.8**  
SPECint\_rate\_base2000 = **61.6**

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Jun-2006 Hardware Avail: Jun-2006 Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	77.5	41.9	2	77.7	41.8
175.vpr	2	77.0	42.2	2	74.6	43.5
176.gcc	2	41.8	61.1	2	41.8	61.1
181.mcf	2	72.8	57.3	2	72.7	57.4
186.crafty	2	36.4	63.7	2	36.4	63.7
197.parser	2	83.4	50.1	2	83.0	50.3
252.eon	2	33.2	90.8	2	33.2	90.8
253.perlbnk	2	52.5	79.6	2	52.2	80.0
254.gap	2	42.8	59.6	2	42.5	60.0
255.vortex	2	44.7	98.7	2	45.0	98.0
256.bzip2	2	78.3	44.4	2	78.3	44.4
300.twolf	2	87.2	79.8	2	87.2	79.8

### Hardware

CPU: Intel Xeon processor 5160 (3.0GHz, 4MB L2 shared, 1333MHz bus)  
CPU MHz: 3000  
FPU: Integrated  
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
CPU(s) orderable: 1,2 chips  
Parallel: No  
Primary Cache: 32KBI + 32KBD (on chip) per core  
Secondary Cache: 4096KB(I+D) (on chip) shared  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 4x2048MB PC2-5300F  
Disk Subsystem: 1x146GB 15K SAS  
Other Hardware:

### Software

Operating System: Windows Server 2003 Enterprise SP1  
Compiler: Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z)  
Microsoft Visual Studio .NET 7.0.9466 (for libraries)  
MicroQuill Smartheap Library 7.0  
File System: NTFS  
System State: Default

## Notes/Tuning Information

+FDO: PASS1=-Qprof\_gen PASS2=-Qprof\_use

Base tuning for C programs: -fast +FDO ONESTEP=yes shlw32M.lib

Base tuning for C++ programs: -fast -Qcxx\_features +FDO ONESTEP=yes

Portability flags:

176.gcc: -Dalloca=\_alloca /F10000000

186.crafty: -DNT\_i386

253.perlbnk: -DSPEC\_CPU2000\_NTOS -DPERLDLL /MT

254.gap: -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_HAS\_MALLOC\_PROTO

Peak tuning:

164.gzip: -fast +FDO ONESTEP=yes

175.vpr: -fast +FDO ONESTEP=yes

176.gcc: basepeak=1

181.mcf: -fast +FDO ONESTEP=yes shlw32M.lib

186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

197.parser: -fast +FDO ONESTEP=yes

252.eon: -fast +FDO

253.perlbnk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

254.gap: -fast +FDO ONESTEP=yes

255.vortex: -fast +FDO ONESTEP=yes shlw32M.lib



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## Notes/Tuning Information (Continued)

256.bzip2: basepeak=1  
300.twolf: -fast +FDO shlw32M.lib  
BIOS Configuration Notes  
Power Regulator set to Static High Performance Mode  
Adjacent Sector Prefetch Disabled