



CFP2000 Result

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Hewlett-Packard Company
ProLiant DL360 G5 (3.0GHz, Intel Xeon processor 5160)

SPECfp2000 = 2748
SPECfp_base2000 = 2731

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Jun-2006 Hardware Avail: Jun-2006 Software Avail: May-2006

| Benchmark | Reference Time | Base Runtime | Base Ratio | Runtime | Ratio | |
|--------------|----------------|--------------|------------|---------|-------|--|
| 168.wupwise | 1600 | 42.1 | 3798 | 42.1 | 3798 | |
| 171.swim | 3100 | 109 | 2835 | 109 | 2836 | |
| 172.mgrid | 1800 | 99.7 | 1806 | 99.6 | 1807 | |
| 173.applu | 2100 | 99.2 | 2118 | 99.2 | 2118 | |
| 177.mesa | 1400 | 49.6 | 2822 | 49.2 | 2844 | |
| 178.galgel | 2900 | 40.4 | 7177 | 40.4 | 7177 | |
| 179.art | 2600 | 24.7 | 10531 | 24.7 | 10531 | |
| 183.quake | 1300 | 40.4 | 3219 | 40.4 | 3219 | |
| 187.facerec | 1900 | 70.9 | 2680 | 71.0 | 2675 | |
| 188.amp | 2200 | 108 | 2031 | 101 | 2184 | |
| 189.lucas | 2000 | 96.3 | 2078 | 95.8 | 2089 | |
| 191.fma3d | 2100 | 102 | 2062 | 102 | 2062 | |
| 200.sixtrack | 1100 | 91.6 | 1201 | 91.6 | 1201 | |
| 301.apsi | 2600 | 160 | 1622 | 160 | 1622 | |

Hardware

CPU: Intel Xeon processor 5160 (3.0GHz, 4MB L2 shared, 1333MHz bus)
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 32KBI + 32KBD (on chip) per core
Secondary Cache: 4096KB(I+D) (on chip) shared
L3 Cache: N/A
Other Cache: N/A
Memory: 8x1024MB PC2-5300F
Disk Subsystem: 1x36GB 10K SAS
Other Hardware:

Software

Operating System: Windows Server 2003 Enterprise SP1
Compiler: Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z)
Intel Fortran Compiler for 32-bit applications, (Version 9.1 Build 20060323Z)
Microsoft Visual Studio .NET 7.0.9466 (for libraries)
MicroQuill Smarheap Library 7.0
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for Fortran programs: -fast -Qansi_alias +FDO
Base tuning for C programs: -fast +FDO shlw32M.lib
Portability flags:
 178.galgel: -FI /F32000000
Peak tuning:
168.wupwise: -fast -Qansi_alias +FDO
171.swim: -fast +FDO ONESTEP=yes
172.mgrid: -fast -Qansi_alias +FDO
173.applu: basepeak=1
177.mesa: -fast +FDO ONESTEP=yes
178.galgel: basepeak=1
179.art: basepeak=1
183.quake: -fast -Oa -Qrcd +FDO ONESTEP=yes shlw32M.lib
187.facerec: -fast -Qunroll1 -Qscalar_rep- +FDO ONESTEP=yes
188.amp: -fast -Qansi_alias +FDO
189.lucas: -fast
```



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Notes/Tuning Information (Continued)

```
191.fma3d:    basepeak=1
200.sixtrack: -fast                +FDO
301.apsi:     basepeak=1
BIOS Configuration Notes
Power Regulator set to Static High Performance Mode
```