



# CFP2000 Result

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## Acer Incorporated

Acer Altos R510 Mk2 (3.0 GHz Intel Xeon processor, 2MB L2, 800MHz system bus)

SPECfp2000 = 1664

SPECfp\_base2000 = 1655

SPEC license #: 97 Tested by: Acer Incorporated Test date: Dec-2005 Hardware Avail: Jan-2006 Software Avail: Jul-2005

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
168.wupwise	1600	68.0	2353	68.0	2351	
171.swim	3100	137	2267	136	2272	
172.mgrid	1800	136	1323	136	1322	
173.applu	2100	151	1389	151	1389	
177.mesa	1400	107	1313	107	1313	
178.galgel	2900	92.9	3122	92.9	3121	
179.art	2600	64.0	4065	64.0	4065	
183.quake	1300	52.7	2468	52.5	2475	
187.facerec	1900	111	1717	111	1717	
188.amp	2200	223	986	208	1060	
189.lucas	2000	105	1910	105	1911	
191.fma3d	2100	159	1320	159	1320	
200.sixtrack	1100	184	599	184	598	
301.apsi	2600	231	1124	231	1123	

### Hardware

CPU: Intel Xeon (3.0 GHz, 2MB L2, 800MHz system bus)  
CPU MHz: 3000  
FPU: Integrated  
CPU(s) enabled: 1 core, 1 chip, 1 core/chip(Hyper-Threading Technology Disabled)  
CPU(s) orderable: 1,2  
Parallel: No  
Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip  
Secondary Cache: 2048KB(I+D) on chip  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 6 x 512MB ECC DDR2-400 SDRAM  
Disk Subsystem: 1 x 160GB 7200RPM SATA HDD  
Other Hardware:

### Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790)  
Compiler: Intel C++ 9.0 Build 20050430Z for IA32 and Fortran Compiler 9.0 Build 20050722Z for IA32  
Microsoft Visual Studio .Net 2003(7.1.3091, for libraries)  
shlw32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/  
File System: NTFS  
System State: Default

## Notes/Tuning Information

```
+FDO: PASS1= -Qprof_gen PASS2=-Qprof_use
Base tuning for all Fortran Programs : -fast -Qansi_alias +FDO
Base tuning for all C Programs: -fast +FDO shlw32M.lib
Portability:
178.galgel: -FI /F32000000
192.fma3d: Approved windowsdp src.alt used
Peak tuning:
168.wupwise: -fast -Qansi_alias +FDO
171.swim: -fast -Qansi_alias +FDO
172.mgrid: -fast -Qansi_alias +FDO
173.applu: basepeak=yes
177.mesa: basepeak=yes
178.galgel: -fast -Qansi_alias +FDO
179.art: basepeak=yes
183.quake: -fast -Oa -Qrcd -Qprof_gen shlw32M.lib
187.facerec: basepeak=yes
188.amp: -Oa -QxP
```



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## Notes/Tuning Information (Continued)

189.lucas: -fast -Qprefetch- +FDO

191.fma3d: basepeak=yes

200.sixtrack: -Qipo -QxP +FDO

301.apsi: -fast +FDO

Enable hardware prefetching and adjacent cache line prefetch BIOS setting

Product description located at <http://www.acer.com>