



CINT2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

Itautec

Servidor Itautec MX201 (Intel Xeon 5030 processor)

SPECint_rate2000 = 57.9

SPECint_rate_base2000 = 57.9

SPEC license #: 9001 | Tested by: Itautec | Test date: Oct-2006 | Hardware Avail: Jul-2006 | Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	8	246	52.8	8	246	52.8
175.vpr	8	357	36.4	8	357	36.4
176.gcc	8	127	80.4	8	127	80.4
181.mcf	8	656	25.5	8	656	25.5
186.crafty	8	200	46.5	8	200	46.5
197.parser	8	281	59.4	8	281	59.4
252.eon	8	135	89.4	8	135	89.4
253.perlbnk	8	255	65.4	8	255	65.4
254.gap	8	162	63.0	8	162	63.0
255.vortex	8	177	99.4	8	177	99.4
256.bzip2	8	295	47.2	8	295	47.2
300.twolf	8	366	76.0	8	366	76.0

Hardware

CPU: Intel Xeon 5030 processor (2.66 GHz, 2xMB L2, 667 MHz system bus)
CPU MHz: 2667
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip (Hyper-Threading Technology enabled)
CPU(s) orderable: 1,2
Parallel: No
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 2 MB I+D on chip per core
L3 Cache: N/A
Other Cache: N/A
Memory: 8x1GB 533MHz ECC CL4 FB-DIMM DDR2 SDRAM
Disk Subsystem: 1 x 73GB SCSI 10000 RPM
Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler: Intel C++ Compiler 9.1
Build 20060519Z (for 32-bit applications),
Microsoft Visual Studio .NET 7.0.9466 (for libraries),
MicroQuill SmartHeap for SMP Library 8.0
File System: NTFS
System State: Default

Notes/Tuning Information

PORTABILITY FLAGS

176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
252.eon: -DHAS_ERRLIST
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

GENERAL

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Base tuning flags

for C programs: -fast -QxP +FDO shlsMPMt.lib
for C++ programs: -fast -Qcxx_features +FDO

EXTRA LIBRARIES

MicroQuill SmartHeap for SMP Library 8.0 available from
<http://www.microquill.com/>