



CINT2000 Result

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Hewlett-Packard Company
ProLiant DL365 (AMD Opteron (TM) 2218)

SPECint2000 = 1860
SPECint_base2000 = 1726

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Aug-2006 Hardware Avail: Nov-2006 Software Avail: Apr-2006

| Benchmark | Reference Time | Base Runtime | Base Ratio | Runtime | Ratio | 1000 2000 3000 4000 | | | |
|-------------|----------------|--------------|------------|---------|-------|--------------------------------|--|--|--|
| 164.gzip | 1400 | 96.8 | 1447 | 96.8 | 1447 | [Bar chart showing ratio 1447] | | | |
| 175.vpr | 1400 | 101 | 1392 | 99.3 | 1410 | [Bar chart showing ratio 1410] | | | |
| 176.gcc | 1100 | 56.0 | 1963 | 57.0 | 1929 | [Bar chart showing ratio 1929] | | | |
| 181.mcf | 1800 | 156 | 1157 | 127 | 1413 | [Bar chart showing ratio 1413] | | | |
| 186.crafty | 1000 | 59.4 | 1684 | 52.9 | 1891 | [Bar chart showing ratio 1891] | | | |
| 197.parser | 1800 | 111 | 1623 | 112 | 1608 | [Bar chart showing ratio 1608] | | | |
| 252.eon | 1300 | 52.0 | 2500 | 39.7 | 3274 | [Bar chart showing ratio 3274] | | | |
| 253.perlbnk | 1800 | 95.1 | 1893 | 95.1 | 1893 | [Bar chart showing ratio 1893] | | | |
| 254.gap | 1100 | 57.8 | 1903 | 57.8 | 1903 | [Bar chart showing ratio 1903] | | | |
| 255.vortex | 1900 | 71.4 | 2661 | 65.3 | 2910 | [Bar chart showing ratio 2910] | | | |
| 256.bzip2 | 1500 | 112 | 1342 | 107 | 1397 | [Bar chart showing ratio 1397] | | | |
| 300.twolf | 3000 | 173 | 1734 | 142 | 2108 | [Bar chart showing ratio 2108] | | | |

Hardware

CPU: AMD Opteron (TM) 2218
CPU MHz: 2600
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 64 KB I + 64 KB D on chip per core
Secondary Cache: 1 MB I+D on chip per core
L3 Cache: N/A
Other Cache: N/A
Memory: 8 GB (4x2 GB PC2-5300P CL5.0)
Disk Subsystem: 1x36 GB 10 K SAS
Other Hardware:

Software

Operating System: Windows Server 2003 Enterprise SP1
Compiler: Intel C++ Compiler for 32-bit applications, (version 9.1 Build 20060323Z), Microsoft Visual Studio .NET 7.0.9466 (for libraries), MicroQuill Smartheap Library 7.0
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast -arch:SSE2 -Oi- +FDO ONESTEP=yes
Base tuning for C++ programs: -fast -Qcxx_features -arch:SSE2 +FDO ONESTEP=yes
Portability flags:
 176.gcc: -Dalloca=_alloca /F10000000
 186.crafty: -DNT_i386
 253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
 254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
 164.gzip: basepeak=yes
 175.vpr: -fast -arch:SSE2 -Qoption,c,-ip_ninl_max_stats=2000
          -Qoption,c,-ip_ninl_max_total_stats=4500 +FDO ONESTEP=yes
 176.gcc: -fast -arch:SSE2 -Oi- -Qunroll3 +FDO
 181.mcf: -O3 -Qipo -QaxN +FDO ONESTEP=yes shlw32M.lib
 186.crafty: -fast -arch:SSE2 +FDO ONESTEP=yes
 197.parser: -arch:SSE2 -Oi- -Qipo +FDO ONESTEP=yes
 252.eon: -fast -arch:SSE2 -Qansi_alias -Qoption,c,-ip_ninl_max_stats=2000
          -Qoption,c,-ip_ninl_max_total_stats=4500 +FDO ONESTEP=yes
 253.perlbnk: basepeak=yes
 254.gap: basepeak=yes
```



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| | | | | | | | | | |
|-----------------|---|------------|-------------------------|------------|----------|-----------------|----------|-----------------|----------|
| SPEC license #: | 3 | Tested by: | Hewlett-Packard Company | Test date: | Aug-2006 | Hardware Avail: | Nov-2006 | Software Avail: | Apr-2006 |
|-----------------|---|------------|-------------------------|------------|----------|-----------------|----------|-----------------|----------|

Notes/Tuning Information (Continued)

```
255.vortex: -fast -arch:SSE2 -Oi- -Qoption,c,-ip_ninl_max_stats=2000
            -Qoption,c,-ip_ninl_max_total_stats=4500 +FDO ONESTEP=yes shlw32M.lib
256.bzip2:  -O3 -Qipo -QaxN -Qunroll2                               shlw32M.lib
300.twolf:  -fast -arch:SSE2 -Qansi_alias                          +FDO ONESTEP=yes shlw32M.lib
```

BIOS Configuration Notes
Node Interleaving Disabled

Other Configuration Notes
The start /b /wait /affinity command is used to bind CPU(s) to processes.