



CINT2000 Result

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Acer Incorporated
Acer Altos R520 (3.73 GHz Intel Xeon Processor 5080)

SPECint_rate2000 = 42.2
SPECint_rate_base2000 = 42.1

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: May-2006 | Hardware Avail: Nov-2006 | Software Avail: Nov-2005

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	4	170	38.3	4	170	38.3
175.vpr	4	248	26.2	4	239	27.2
176.gcc	4	87.8	58.2	4	87.8	58.2
181.mcf	4	299	28.0	4	299	28.0
186.crafty	4	144	32.2	4	144	32.2
197.parser	4	200	41.7	4	198	42.1
252.eon	4	111	54.3	4	111	54.1
253.perlbnk	4	176	47.5	4	176	47.4
254.gap	4	111	45.8	4	111	45.8
255.vortex	4	131	67.1	4	131	67.1
256.bzip2	4	200	34.8	4	199	34.9
300.twolf	4	274	50.9	4	276	50.5

Hardware

CPU: Intel Xeon Processor 5080 (3.73 GHz, 2x2MB L2, 1066MHz system bus)
 CPU MHz: 3733
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip (Hyper-Threading Technology enabled)
 CPU(s) orderable: 1,2
 Parallel: No
 Primary Cache: 12K(I) micro-ops + 16KB(D) on chip, per core
 Secondary Cache: 2048KB(I+D) on chip, per core
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 8 x 2048MB ECC FB-DIMM 667
 Disk Subsystem: 1 x 73GB 10000RPM SAS HDD
 Other Hardware:

Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790), Service Pack 1
 Compiler: Intel C++ Compiler 9.0 Build 20051130Z for IA32
 Microsoft Visual Studio .Net 2003 (7.1.3091, for libraries)
 shlw32M.lib is SmartHeap Library Version 7.4 from <http://www.microquill.com/>
 File System: NTFS
 System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO shlw32M.lib
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: basepeak=yes
175.vpr: -fast -Qansi_alias +FDO
176.gcc: basepeak=yes
181.mcf: basepeak=yes
186.crafty: basepeak=yes
197.parser: -fast -Qansi_alias +FDO
252.eon: -fast +FDO
253.perlbnk: -fast -Qansi_alias +FDO shlw32M.lib
254.gap: basepeak=yes
255.vortex: basepeak=yes
256.bzip2: -fast -Oa -Qunroll1 +FDO
```



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Notes/Tuning Information (Continued)

300.twolf: -fast +FDO shlw32M.lib
Enable hardware prefetching and adjacent cache line prefetch BIOS setting

This result was measured on the Acer Alto R720.
The Altos R520 and Altos R720 are electronically equivalent.