



CFP2000 Result

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Fujitsu Siemens Computers
CELSIUS R640, Intel Xeon 5130 processor

SPECfp_rate2000 = 70.6
SPECfp_rate_base2000 = 70.1

SPEC license #: 22 Tested by: Fujitsu Siemens Computers Test date: Aug-2006 Hardware Avail: Aug-2006 Software Avail: May-2006

| Benchmark | Base Copies | Base Runtime | Base Ratio | Copies | Runtime | Ratio |
|--------------|-------------|--------------|------------|--------|---------|-------|
| 168.wupwise | 4 | 71.2 | 104 | 4 | 71.2 | 104 |
| 171.swim | 4 | 271 | 53.2 | 4 | 270 | 53.2 |
| 172.mgrid | 4 | 192 | 43.5 | 4 | 193 | 43.2 |
| 173.applu | 4 | 185 | 52.6 | 4 | 185 | 52.6 |
| 177.mesa | 4 | 75.1 | 86.5 | 4 | 74.2 | 87.5 |
| 178.galgel | 4 | 75.4 | 178 | 4 | 75.5 | 178 |
| 179.art | 4 | 60.1 | 201 | 4 | 60.0 | 201 |
| 183.quake | 4 | 92.8 | 65.0 | 4 | 92.8 | 65.0 |
| 187.facerec | 4 | 104 | 85.0 | 4 | 104 | 85.2 |
| 188.amp | 4 | 192 | 53.1 | 4 | 164 | 62.1 |
| 189.lucas | 4 | 161 | 57.8 | 4 | 161 | 57.8 |
| 191.fma3d | 4 | 169 | 57.5 | 4 | 185 | 52.7 |
| 200.sixtrack | 4 | 140 | 36.5 | 4 | 140 | 36.5 |
| 301.apsi | 4 | 244 | 49.4 | 4 | 240 | 50.2 |

Hardware

CPU: Intel Xeon 5130 processor (2.0 GHz, 1333 MHz system bus)
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 32KBI + 32KBD on chip, per core
Secondary Cache: 4096KB(I+D) on chip, per chip
L3 Cache: N/A
Other Cache: N/A
Memory: 8 GB (8x1 GB DDRII 5300F, 2rank, CL5-5-5, with ECC)
Disk Subsystem: Serial ATA 7200 rpm
Other Hardware:

Software

Operating System: Windows XP, 64 bit Edition
Compiler: Intel(R) C++ Compiler for 32-bit app., Version 9.1,
- Build 20060519Z Package ID: W_CC_P_9.1.025
Intel(R) Fortran Compiler for 32-bit app., Version 9.1,
- Build 20060519Z Package ID: W_FC_P_9.1.025
Microsoft Visual Studio .NET 2003 (for libraries)
MicroQuill SmartHeap Library, Version 8 (shlW32M.lib)
File System: NTFS
System State: Default

Notes/Tuning Information

Portability flags:
178.galgel: -FI /F32000000

Feedback optimization:
+FDO: PASS1= -Qprof_gen PASS2= -Qprof_use

Baseline Tuning Flags:
for C programs:
-fast +FDO shlW32M.lib
for Fortran programs:
-fast +FDO shlW32M.lib

Peak Tuning Flags:
173.applu: basepeak=yes
168.wupwise: -fast +FDO
171.swim: -fast +FDO
172.mgrid: -fast +FDO



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Notes/Tuning Information (Continued)

```
173.applu: basepeak=yes
177.mesa: -fast +FDO ONESTEP=yes
178.galgel: -fast +FDO
179.art: -fast +FDO shlW32M.lib
183.equake: -fast +FDO shlW32M.lib
187.facerec: -fast +FDO
188.amp: -fast +FDO
189.lucas: basepeak=yes
191.fma3d: -fast +FDO
200.sixtrack: -fast +FDO
301.apsi: -fast +FDO
```

Extra Libraries:

shlW32M.lib = Microquill SmartHeap Library 8
see www.microquill.com

Compilation was done in
Windows XP Professional, Service Pack 2

Snoop Filter was enabled in BIOS

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<http://www.fujitsu-siemens.com/countries>