



CFP2000 Result

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Hewlett-Packard Company

ProLiant DL380 G5 (2.66GHz, Intel Xeon processor 5150)

SPECfp_rate2000 = 80.0

SPECfp_rate_base2000 = 75.5

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Jul-2006 Hardware Avail: Jun-2006 Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	4	67.2	111	4	67.2	111
171.swim	4	267	53.9	4	264	54.4
172.mgrid	4	181	46.2	4	180	46.4
173.applu	4	182	53.5	4	134	72.7
177.mesa	4	53.2	122	4	48.0	135
178.galgel	4	75.1	179	4	75.1	179
179.art	4	68.3	177	4	68.3	177
183.quake	4	129	46.9	4	99.8	60.4
187.facerec	4	88.9	99.2	4	81.6	108
188.amp	4	131	78.2	4	131	78.2
189.lucas	4	171	54.2	4	170	54.5
191.fma3d	4	165	58.9	4	165	58.9
200.sixtrack	4	110	46.3	4	110	46.3
301.apsi	4	188	64.2	4	182	66.2

Hardware

CPU: Intel Xeon processor 5160 (2.66GHz, 4MB L2 shared, 1333MHz bus)
CPU MHz: 2660
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip
L3 Cache: N/A
Other Cache: N/A
Memory: 16 GB (8x2048 MB PC2-5300F)
Disk Subsystem: 1x36 GB 10 K SAS
Other Hardware:

Software

Operating System: RedHat Enterprise Linux 4.0 Advanced Server for AMD64/EM64T, Update 3
Kernel 2.6.9-34.ELsmp
Compiler: Intel C++ Compiler for EM64T-based applications, (Version 9.1 Build 20060323)
Intel Fortran Compiler for EM64T-based applications, (Version 9.1 Build 20060323)
PathScale EKOPath(TM) Compiler Suite, Release 2.4
File System: ext2
System State: Default

Notes/Tuning Information

```
+FDO: PASS1= -prof_gen PASS2=-prof_use (Intel Compiler)
+FDO: PASS1= -fb_create fbdata PASS2=-fb_opt fbdata (PathScale Compiler)
ifort is the Intel Fortran compiler, icc is the Intel C++ compiler; and
pathf95 is PathScale Fortran compiler, pathcc is the PathScale C compiler.
Base tuning for C programs: icc -fast -auto_ilp32 +FDO
Base tuning for FORTRAN programs: ifort -fast +FDO
Portability:
-DSPEC_CPU2000_LP64 applied to all benchmarks
178.galgel: -FI
Peak tuning:
168.wupwise: basepeak=1
171.swim: pathf95 -Ofast -LNO:fusion=2:simd=0 -WOPT:val=0 -march=em64t
172.mgrid: pathf95 -Ofast -CG:load_exe=0 -LNO:blocking=off:prefetch_ahead=5
-Opt:ro=3:unroll_size=256 -WOPT:mem_opnds=on -march=em64t
173.applu: pathf95 -O3 -ipa -CG:load_exe=0
-LNO:fission=1:fusion=2:blocking=off:full_unroll_size=9000
-Opt:IEEE_a=3:ro=3 -TENV:X=3 -march=em64t
```



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Notes/Tuning Information (Continued)

```
177.mesa: pathcc -O2 -ipa -OPT:Ofast -fno-math-errno -CG:local_fwd_sched=on
          -GRA:optimize_boundary=on -march=em64t +FDO
178.galgel: basepeak=1
179.art: basepeak=1
183.quake: icc -fast +FDO ONESTEP=yes -rcd -auto-ilp32
187.facerec: pathf95 -Ofast -IPA:plimit=1500 -LNO:fusion=2
            -OPT:IEEE_NaN_Inf=off:ro=3:unroll_size=0 -march=em64t +FDO
188.amm: basepeak=1
189.lucas: ifort -fast ONESTEP=yes
191.fma3d: basepeak=1
200.sixtrack: basepeak=1
301.apsi: pathf95 -Ofast -CG:load_exe=0 -LNO:opt=0:prefetch=1 -march=em64t
```

BIOS Configuration Notes
Power Regulator set to Static High Performance Mode