



CINT2000 Result

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Hewlett-Packard Company
ProLiant DL380 G5 (2.0GHz, Intel Xeon processor 5130)

SPECint2000 = 2125
SPECint_base2000 = 2117

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Jul-2006 Hardware Avail: Jun-2006 Software Avail: May-2006

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	1000 2000 3000 4000			
164.gzip	1400	112	1245	113	1244	[Bar chart showing ratio]			
175.vpr	1400	90.9	1541	87.5	1600	[Bar chart showing ratio]			
176.gcc	1100	45.3	2428	45.3	2428	[Bar chart showing ratio]			
181.mcf	1800	53.5	3363	53.5	3363	[Bar chart showing ratio]			
186.crafty	1000	54.4	1837	54.5	1835	[Bar chart showing ratio]			
197.parser	1800	108	1672	107	1679	[Bar chart showing ratio]			
252.eon	1300	49.7	2616	49.9	2607	[Bar chart showing ratio]			
253.perlbmk	1800	74.2	2425	73.7	2441	[Bar chart showing ratio]			
254.gap	1100	53.0	2077	52.9	2080	[Bar chart showing ratio]			
255.vortex	1900	53.2	3569	53.3	3564	[Bar chart showing ratio]			
256.bzip2	1500	95.8	1566	95.8	1566	[Bar chart showing ratio]			
300.twolf	3000	131	2288	131	2288	[Bar chart showing ratio]			

Hardware

CPU: Intel Xeon processor 5130 (2.0GHz, 4MB L2 shared, 1333MHz bus)
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip
L3 Cache: N/A
Other Cache: N/A
Memory: 16 GB (8x2048MB PC2-5300F)
Disk Subsystem: 1x36 GB 10 K SAS
Other Hardware:

Software

Operating System: Windows Server 2003 Enterprise SP1
Compiler: Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z)
Microsoft Visual Studio .NET 7.0.9466 (for libraries)
MicroQuill Smartheap Library 7.0
File System: NTFS
System State: Default

Notes/Tuning Information

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Base tuning for C programs: -fast +FDO ONESTEP=yes shlw32M.lib

Base tuning for C++ programs: -fast -Qcxx_features +FDO ONESTEP=yes

Portability flags:

176.gcc: -Dalloca=_alloca /F10000000

186.crafty: -DNT_i386

253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT

254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Peak tuning:

164.gzip: -fast +FDO ONESTEP=yes

175.vpr: -fast +FDO ONESTEP=yes

176.gcc: basepeak=1

181.mcf: -fast +FDO ONESTEP=yes shlw32M.lib

186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

197.parser: -fast +FDO ONESTEP=yes

252.eon: -fast +FDO

253.perlbmk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

254.gap: -fast +FDO ONESTEP=yes

255.vortex: -fast +FDO ONESTEP=yes shlw32M.lib



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Notes/Tuning Information (Continued)

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256.bzip2:    basepeak=1
300.twolf:    -fast                +FDO                shlw32M.lib
BIOS Configuration Notes
Power Regulator set to Static High Performance Mode
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