



# CINT2000 Result

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**Hewlett-Packard Company**  
ProLiant DL360 G5 (2.66 GHz, Intel Xeon processor 5150)

SPECint\_rate2000 = 111  
SPECint\_rate\_base2000 = 110

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Aug-2006 Hardware Avail: May-2006 Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	4	87.5	74.2	4	87.4	74.3
175.vpr	4	84.6	76.8	4	80.6	80.6
176.gcc	4	44.5	115	4	44.5	115
181.mcf	4	86.4	96.7	4	86.3	96.8
186.crafty	4	41.0	113	4	41.1	113
197.parser	4	91.3	91.4	4	90.7	92.0
252.eon	4	37.4	161	4	37.4	161
253.perlbnk	4	58.8	142	4	58.7	142
254.gap	4	48.8	105	4	48.5	105
255.vortex	4	49.4	178	4	49.4	179
256.bzip2	4	88.4	78.8	4	88.4	78.8
300.twolf	4	98.3	142	4	98.3	142

### Hardware

CPU: Intel Xeon processor 5150 (2.66 GHz, 4 MB L2 shared, 1333 MHz bus)  
CPU MHz: 2660  
FPU: Integrated  
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
CPU(s) orderable: 1,2 chips  
Parallel: No  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 4 MB I+D on chip per core  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 8 GB (8x1024 MB PC2-5300F)  
Disk Subsystem: 1x36 GB 10 K SAS  
Other Hardware:

### Software

Operating System: Windows Server 2003 Enterprise SP1  
Compiler: Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z)  
Microsoft Visual Studio .NET 7.0.9466 (for libraries)  
MicroQuill Smartheap Library 7.0  
File System: NTFS  
System State: Default

## Notes/Tuning Information

+FDO: PASS1=-Qprof\_gen PASS2=-Qprof\_use

Base tuning for C programs: -fast +FDO ONESTEP=yes shlw32M.lib

Base tuning for C++ programs: -fast -Qcxx\_features +FDO ONESTEP=yes

Portability flags:

176.gcc: -Dalloca=\_alloca /F10000000

186.crafty: -DNT\_i386

253.perlbnk: -DSPEC\_CPU2000\_NTOS -DPERL DLL /MT

254.gap: -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_HAS\_MALLOC\_PROTO

Peak tuning:

164.gzip: -fast +FDO ONESTEP=yes

175.vpr: -fast +FDO ONESTEP=yes

176.gcc: basepeak=1

181.mcf: -fast +FDO ONESTEP=yes shlw32M.lib

186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

197.parser: -fast +FDO ONESTEP=yes

252.eon: -fast +FDO

253.perlbnk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

254.gap: -fast +FDO ONESTEP=yes

255.vortex: -fast +FDO ONESTEP=yes shlw32M.lib



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## Notes/Tuning Information (Continued)

256.bzip2: basepeak=1  
300.twolf: -fast +FDO shlw32M.lib  
BIOS Configuration Notes  
Power Regulator set to Static High Performance Mode  
Adjacent Sector Prefetch disabled