



# CINT2000 Result

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**Hewlett-Packard Company**  
ProLiant BL20P G4 (3.73GHz, Intel Xeon processor 5080)

SPECint2000 = **1802**  
SPECint\_base2000 = **1793**

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Jun-2006 Hardware Avail: Jun-2006 Software Avail: May-2006

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	1000 2000 3000 4000			
164.gzip	1400	109	1284	109	1283				
175.vpr	1400	120	1171	116	1203				
176.gcc	1100	50.1	2195	50.1	2195				
181.mcf	1800	94.5	1905	94.3	1908				
186.crafty	1000	71.5	1400	71.3	1404				
197.parser	1800	118	1531	117	1539				
252.eon	1300	51.1	2544	51.1	2544				
253.perlbmk	1800	82.1	2192	81.5	2208				
254.gap	1100	54.6	2016	53.8	2046				
255.vortex	1900	63.1	3012	63.2	3008				
256.bzip2	1500	117	1279	117	1279				
300.twolf	3000	158	1897	158	1896				

### Hardware

CPU: Intel Xeon processor 5080 (3.73GHz, 2x2MB L2, 1066MHz bus)  
CPU MHz: 3730  
FPU: Integrated  
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip (Hyper-Threading Technology disabled)  
CPU(s) orderable: 1,2 chips  
Parallel: No  
Primary Cache: 12K micro-ops I + 16KBD (on chip) per core  
Secondary Cache: 2048KB(I+D) (on chip) per core  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 8x1024MB PC2-5300F  
Disk Subsystem: 1x36GB 10K SAS  
Other Hardware:

### Software

Operating System: Windows Server 2003 Enterprise SP1  
Compiler: Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z)  
Microsoft Visual Studio .NET 7.0.9466 (for libraries)  
MicroQuill Smartheap Library 7.0  
File System: NTFS  
System State: Default

## Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO ONESTEP=yes shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO ONESTEP=yes
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: -fast +FDO ONESTEP=yes
175.vpr: -fast +FDO ONESTEP=yes
176.gcc: basepeak=1
181.mcf: -fast +FDO ONESTEP=yes shlw32M.lib
186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib
197.parser: -fast +FDO ONESTEP=yes
252.eon: -fast +FDO
253.perlbmk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib
254.gap: -fast +FDO ONESTEP=yes
255.vortex: -fast +FDO ONESTEP=yes shlw32M.lib
256.bzip2: basepeak=1
```



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## Notes/Tuning Information (Continued)

300.twolf: -fast +FDO shlw32M.lib  
BIOS Configuration Notes  
Power Regulator set to Static High Performance Mode  
Hyper-Threading Technology disabled