



# CFP2000 Result

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## Acer Incorporated

Acer Altos R510Mk2 (3.6 GHz Intel Xeon processor, 2MB L2)

SPECfp\_rate2000 = 21.8

SPECfp\_rate\_base2000 = 21.7

SPEC license #: 97 Tested by: Acer Incorporated Test date: Jan-2006 Hardware Avail: Jan-2006 Software Avail: Nov-2005

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	2	125	29.7	2	125	29.8
171.swim	2	265	27.1	2	265	27.1
172.mgrid	2	214	19.6	2	213	19.6
173.applu	2	290	16.8	2	290	16.8
177.mesa	2	175	18.6	2	175	18.6
178.galgel	2	184	36.6	2	184	36.6
179.art	2	163	36.9	2	163	36.9
183.quake	2	87.4	34.5	2	87.4	34.5
187.facerec	2	173	25.5	2	173	25.5
188.amp	2	363	14.1	2	324	15.8
189.lucas	2	224	20.7	2	224	20.7
191.fma3d	2	292	16.7	2	292	16.7
200.sixtrack	2	246	10.4	2	251	10.2
301.apsi	2	355	17.0	2	355	17.0

### Hardware

CPU: Intel Xeon (3.6 GHz, 2MB L2, 800MHz system bus)  
CPU MHz: 3600  
FPU: Integrated  
CPU(s) enabled: 1 core, 1 chip, 1 core/chip(Hyper-Threading Technology enabled)  
CPU(s) orderable: 1,2  
Parallel: No  
Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip  
Secondary Cache: 2048KB(I+D) on chip  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 6 x 512MB ECC DDR2-400 SDRAM  
Disk Subsystem: 1 x 160GB 7200RPM SATA HDD  
Other Hardware:

### Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790)  
Compiler: Intel C++ 9.0 Build 20051130Z for IA32 and Fortran Compiler 9.0 Build 20051130Z for IA32  
Microsoft Visual Studio .Net 2003(7.1.3091, for libraries)  
shlw32M.lib is Microquill SmartHeap Library Version 7.4  
File System: NTFS  
System State: Default

## Notes/Tuning Information

```
+FDO: PASS1= -Qprof_gen PASS2=-Qprof_use
Base tuning for all Fortran Programs : -fast -Qansi_alias +FDO
Base tuning for all C Programs: -fast +FDO shlw32M.lib
Portability:
178.galgel: -FI /F32000000
Peak tuning:
168.wupwise: -fast -Qansi_alias +FDO
171.swim: -fast -Qansi_alias +FDO
172.mgrid: -fast -Qansi_alias +FDO
173.applu: basepeak=yes
177.mesa: basepeak=yes
178.galgel: -fast -Qansi_alias +FDO
179.art: basepeak=yes
183.quake: -fast -Oa -Qrcd +FDO shlw32M.lib
187.facerec: basepeak=yes
188.amp: -Oa -QxP
189.lucas: -fast -Qprefetch- +FDO
```



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## Notes/Tuning Information (Continued)

191.fma3d: basepeak=yes

200.sixtrack: -Qipo -QxP +FDO

301.apsi: -fast +FDO

Enable hardware prefetching and adjacent cache line prefetch BIOS setting

Product description located at <http://www.acer.com>