



CFP2000 Result

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Acer Incorporated

Acer Altos R710 (3.8 GHz Intel Xeon processor, 2MB L2, 800MHz system bus)

SPECfp_rate2000 = 22.5

SPECfp_rate_base2000 = 22.4

SPEC license #: 97 Tested by: Acer Incorporated Test date: Nov-2005 Hardware Avail: Oct-2005 Software Avail: Apr-2005

| Benchmark | Base Copies | Base Runtime | Base Ratio | Copies | Runtime | Ratio |
|--------------|-------------|--------------|------------|--------|---------|-------|
| 168.wupwise | 2 | 120 | 31.0 | 2 | 120 | 31.0 |
| 171.swim | 2 | 264 | 27.2 | 2 | 264 | 27.2 |
| 172.mgrid | 2 | 204 | 20.4 | 2 | 204 | 20.4 |
| 173.applu | 2 | 285 | 17.1 | 2 | 285 | 17.1 |
| 177.mesa | 2 | 164 | 19.8 | 2 | 164 | 19.8 |
| 178.galgel | 2 | 184 | 36.7 | 2 | 181 | 37.2 |
| 179.art | 2 | 161 | 37.4 | 2 | 161 | 37.4 |
| 183.quake | 2 | 86.3 | 34.9 | 2 | 86.2 | 35.0 |
| 187.facerec | 2 | 168 | 26.3 | 2 | 168 | 26.3 |
| 188.amp | 2 | 352 | 14.5 | 2 | 312 | 16.4 |
| 189.lucas | 2 | 218 | 21.3 | 2 | 218 | 21.2 |
| 191.fma3d | 2 | 281 | 17.3 | 2 | 281 | 17.3 |
| 200.sixtrack | 2 | 231 | 11.0 | 2 | 238 | 10.7 |
| 301.apsi | 2 | 338 | 17.9 | 2 | 337 | 17.9 |

Hardware

CPU: Intel Xeon (3.8 GHz, 2MB L2, 800MHz system bus)
 CPU MHz: 3800
 FPU: Integrated
 CPU(s) enabled: 1 core, 1 chip, 1 core/chip(Hyper-Threading Technology Enable)
 CPU(s) orderable: 1,2
 Parallel: No
 Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip
 Secondary Cache: 2048KB(I+D) on chip
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 6 x 512MB ECC DDR2-400 SDRAM
 Disk Subsystem: 1 x 146GB 10000RPM U320 SCSI HDD
 Other Hardware:

Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790)
 Compiler: Intel C++ 9.0 Build 20050430Z for IA32 and Fortran Compiler 9.0 Build 20050722Z for IA32
Microsoft Visual Studio .Net 2003(7.1.3091, for libraries)
 shlw32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/
 File System: NTFS
 System State: Default

Notes/Tuning Information

```
+FDO: PASS1= -Qprof_gen PASS2=-Qprof_use
Base tuning for all Fortran Programs : -fast -Qansi_alias +FDO
Base tuning for all C Programs: -fast +FDO shlw32M.lib
Portability:
178.galgel: -FI /F32000000
192.fma3d: Approved windowsdp src.alt used
Peak tuning:
168.wupwise: -fast -Qansi_alias +FDO
171.swim: -fast -Qansi_alias +FDO
172.mgrid: -fast -Qansi_alias +FDO
173.applu: basepeak=yes
177.mesa: basepeak=yes
178.galgel: -fast -Qansi_alias +FDO
179.art: basepeak=yes
183.quake: -fast -Oa -Qrcd -Qprof_gen shlw32M.lib
187.facerec: basepeak=yes
188.amp: -Oa -QxP
```



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Notes/Tuning Information (Continued)

189.lucas: -fast -Qprefetch- +FDO

191.fma3d: basepeak=yes

200.sixtrack: -Qipo -QxP +FDO

301.apsi: -fast +FDO

Enable hardware prefetching and adjacent cache line prefetch BIOS setting

Product description located at <http://www.acer.com>