



# CINT2000 Result

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Hewlett-Packard Company  
ProLiant ML370 G4 (3.6GHz, Intel Xeon)

SPECint\_rate2000 = 33.8  
SPECint\_rate\_base2000 = 33.1

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Jul-2004 Hardware Avail: Nov-2004 Software Avail: Aug-2004

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	120	27.0	2	121	26.9
175.vpr	2	154	21.1	2	149	21.8
176.gcc	2	62.0	41.1	2	62.0	41.1
181.mcf	2	214	19.5	2	214	19.5
186.crafty	2	75.0	30.9	2	75.0	30.9
197.parser	2	136	30.7	2	136	30.7
252.eon	2	73.6	41.0	2	61.6	49.0
253.perlbnk	2	98.2	42.5	2	94.9	44.0
254.gap	2	67.2	38.0	2	65.8	38.8
255.vortex	2	72.9	60.5	2	72.9	60.5
256.bzip2	2	146	23.8	2	145	24.0
300.twolf	2	168	41.5	2	168	41.5

### Hardware

CPU: Intel Xeon (3.6GHz, 1MB L2, 800MHz bus)  
CPU MHz: 3600  
FPU: Integrated  
CPU(s) enabled: 2 cores, 2 chips, 1 core/chip (HT Technology disabled)  
CPU(s) orderable: 1,2  
Parallel: No  
Primary Cache: 16K micro-ops I + 16KBD on chip  
Secondary Cache: 1024KB(I+D) on chip  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 8x512MB DDR2-400  
Disk Subsystem: 1x18.2GB 15K Ultra320 SCSI  
Other Hardware:

### Software

Operating System: Windows Server 2003 Standard (Build 3790)  
Compiler: Intel C++ Compiler 8.0 Build 20031211Z  
Microsoft Visual Studio .NET 7.0.9466 (for libraries)  
MicroQuill Smartheap Library 7.0  
File System: NTFS  
System State: Default

## Notes/Tuning Information

+FDO: PASS1=-Qprof\_gen PASS2=-Qprof\_use  
+FDO\*: FDO (-QxP not used in PASS1 to enable cross-compilation)

Base tuning for C programs: -QxP -fast +FDO\* shlw32M.lib

Base tuning for C++ programs: -QxP -fast -GX -GR

Portability flags:

176.gcc: -Dalloca=\_alloca /F10000000

186.crafty: -DNT\_i386

253.perlbnk: -DSPEC\_CPU2000\_NTOS -DPERLDLL /MT

254.gap: -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_HAS\_MALLOC\_PROTO

Peak tuning:

164.gzip: -QxP -fast -Oa +FDO ONESTEP=yes

175.vpr: -QxP -fast +FDO ONESTEP=yes

176.gcc: basepeak = 1

181.mcf: -QxP -fast +FDO ONESTEP=yes shlw32M.lib

186.crafty: -QxP -fast -Oa +FDO ONESTEP=yes shlw32M.lib

197.parser: -QxP -fast +FDO ONESTEP=yes

252.eon: -QxP -O2 -Qipo +FDO\*

253.perlbnk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

254.gap: -QxP -fast +FDO ONESTEP=yes



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## Notes/Tuning Information (Continued)

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255.vortex:      -QxP -fast          +FDO  ONESTEP=yes  shlw32M.lib
256.bzip2:      -fast -Oa -Qunroll1 +FDO  ONESTEP=yes  shlw32M.lib
300.twolf:      -QxP -fast -O3      +FDO*                shlw32M.lib
BIOS Configuration Notes
HT Technology disabled
```