



# CINT2000 Result

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**SGI**  
SGI Altix 3000 (1500MHz, Itanium 2)

SPECint\_rate2000 = --  
SPECint\_rate\_base2000 = 57.2

SPEC license #: 4 | Tested by: SGI | Test date: Jan-2004 | Hardware Avail: Jul-2003 | Software Avail: Dec-2003

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	4	151	42.9			
175.vpr	4	135	48.2			
176.gcc	4	83.8	60.9			
181.mcf	4	95.8	87.2			
186.crafty	4	83.4	55.6			
197.parser	4	192	43.6			
252.eon	4	85.8	70.3			
253.perlbmk	4	151	55.2			
254.gap	4	132	38.8			
255.vortex	4	97.6	90.3			
256.bzip2	4	129	54.2			
300.twolf	4	220	63.2			

**Hardware**

CPU: Intel Itanium 2  
CPU MHz: 1500  
FPU: Integrated  
CPU(s) enabled: 4 cores, 4 chips, 1 core/chip  
CPU(s) orderable: 4-64  
Parallel: No  
Primary Cache: 16KBI + 16KBD (on chip) per CPU  
Secondary Cache: 256KB (on chip) per CPU  
L3 Cache: 6.0MB (on chip) per CPU  
Other Cache: N/A  
Memory: 4 GB (8\*512MB PC2700 DIMMS per 4cpu module)  
Disk Subsystem: 1 x 36 GB SCSI (Seagate Cheetah 15k rpm)  
Other Hardware: None

**Software**

Operating System: SGI ProPack(TM) v2.3  
Compiler: Intel(R) C++ Compiler for Linux 8.0 (Build 20031017)  
MicroQuill SmartHeap Library 7.01 (www.microquill.com)  
File System: xfs  
System State: Single-user

## Notes/Tuning Information

+FDO: PASS1=-prof\_gen PASS2=-prof\_use

Baseline optimization flags:

C programs: -fast -auto\_ilp32 +FDO  
C++ programs: -fast -ansi\_alias +FDO  
Extra Libraries: libsmartheap64.a

Portability Flags:

176.gcc: -DSPEC\_CPU2000\_LP64 -Dalloca=\_builtin\_alloca -D\_LIBC  
186.crafty: -DLINUX\_i386  
252.eon: -DSPEC\_CPU2000\_LP64 -DHAS\_ERRLIST -DFMAX\_IS\_DOUBLE  
253.perlbmk: -DSPEC\_CPU2000\_LP64 -DSPEC\_CPU2000\_NEED\_BOOL  
-DSPEC\_CPU2000\_LINUX\_IA64 -DSPEC\_CPU2000\_GLIBC22  
254.gap: -DSPEC\_CPU2000\_LP64 -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_IS\_USG  
-DSYS\_HAS\_IOCTL\_PROTO -DSYS\_HAS\_TIME\_PROTO -DSYS\_HAS\_SIGNAL\_PROTO  
255.vortex: -DSPEC\_CPU2000\_LP64

Processes were bound to CPUs using dplace.