



# CINT2000 Result

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**Dell**  
**PowerEdge 4600 (2.0 GHz Xeon)**

SPECint2000 = **701**  
SPECint\_base2000 = **685**

SPEC license #: 55 Tested by: Dell, Round Rock, TX Test date: Dec-2001 Hardware Avail: Feb-2002 Software Avail: Jun-2001

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
164.gzip	1400	185	756	184	762	
175.vpr	1400	372	376	357	392	
176.gcc	1100	135	813	136	812	
181.mcf	1800	381	473	379	474	
186.crafty	1000	148	676	148	675	
197.parser	1800	266	678	264	682	
252.eon	1300	142	913	122	1061	
253.perlbmk	1800	195	923	195	923	
254.gap	1100	125	881	122	898	
255.vortex	1900	195	973	186	1022	
256.bzip2	1500	285	527	285	526	
300.twolf	3000	536	560	534	562	

### Hardware

CPU: Intel Xeon  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 1 core, 1 chip, 1 core/chip  
 CPU(s) orderable: 1, 2  
 Parallel: No  
 Primary Cache: 12K(I) micro-ops + 8KB(D) on chip  
 Secondary Cache: 512KB(I+D) on chip  
 L3 Cache: N/A  
 Other Cache: N/A  
 Memory: 16 x 256MB PC1600 DDR  
 Disk Subsystem: 1 x 18GB SEAGATE U160  
 Other Hardware:

### Software

Operating System: Windows 2000 Server Service Pack 2 Build 2195  
 Compiler: Intel C++ Compiler 5.0.1 (010727Z)  
 Microsoft Visual C++ 6.0 (SP5)  
 MicroQuill SmartHeap Library 5.0  
 File System: NTFS  
 System State: Default

## Notes/Tuning Information

### PORTABILITY FLAGS

176.gcc: -Dalloca=\_alloca /F10000000  
 186.crafy: -DNT\_i386  
 253.perlbmk: -DSPEC\_CPU2000\_NTOS -DPERLDLL /MT  
 254.gap: -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_HAS\_MALLOC\_PROTO

### FEEDBACK-DIRECTED OPTIMIZATION

FDO: PASS1= -Qprof\_gen PASS2= -Qprof\_use

### BASE TUNING

C: -Qipo +FDO -QxW shlw32M.lib  
 C++: -Qipo -QxW -GX -GR

### PEAK TUNING

164.gzip: -Qipo +FDO -QxW -Oi- -Oa  
 175.vpr: -Qwp\_ipo +FDO -QxW  
 176.gcc: -Qipo +FDO -QxW shlw32M.lib  
 181.mcf: -Qipo +FDO -QxW shlw32M.lib  
 186.crafty: -Qipo +FDO -QxW -Oa shlw32M.lib  
 197.parser: -Qipo +FDO -QxW  
 252.eon: -Qipo +FDO -QxW  
 253.perlbmk: -Qipo +FDO -QxW shlw32M.lib  
 254.gap: -Qipo +FDO -QaxW



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## Notes/Tuning Information (Continued)

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255.vortex: -Qwp_ipo +FDO -QxW -Oa shlw32M.lib
256.bzip2:  -Qipo +FDO -QxW shlw32M.lib
300.twolf:  -Qipo +FDO -QxW shlw32M.lib
EXTRA LIBRARIES
shlw32M.lib: MicroQuill SmartHeap Library 5.0
              www.microquill.com
/numprocs=1 was added to c:\boot.ini file to disable additional CPUs on this system
```