



OMPM2001 Result

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Intel Corporation

Intel(R) Server Board S5000XAL (Intel(R) Xeon(R) Processor 5160)

SPECompMpeak2001 = --

SPECompMbase2001 = 10689

SPEC license #HPG0013 | Tested by: Intel Corporation | Test site: Dupont, WA | Test date: Nov-2006 | Hardware Avail: Nov-2006 | Software Avail: Dec-2006

| Benchmark | Reference Time | Base Runtime | Base Ratio | Peak Runtime | Peak Ratio | |
|---------------|----------------|--------------|------------|--------------|------------|--|
| 310.wupwise_m | 6000 | 432 | 13880 | | | |
| 312.swim_m | 6000 | 742 | 8086 | | | |
| 314.mgrid_m | 7300 | 954 | 7655 | | | |
| 316.applu_m | 4000 | 483 | 8286 | | | |
| 318.galgel_m | 5100 | 442 | 11535 | | | |
| 320.quake_m | 2600 | 234 | 11133 | | | |
| 324.apsi_m | 3400 | 298 | 11401 | | | |
| 326.gafort_m | 8700 | 702 | 12401 | | | |
| 328.fma3d_m | 4600 | 593 | 7762 | | | |
| 330.art_m | 6400 | 246 | 26007 | | | |
| 332.ammp_m | 7000 | 877 | 7979 | | | |

Hardware

CPU: Dual-Core Intel(R) Xeon(R) Processor 5160
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32KB(I)+32KB(D)/core on chip
 Secondary Cache: 4096KB/chip (I+D) on chip
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 8x1GB FBDIMM-667MHz CL5
 Disk Subsystem: Samsung HM080J1 80GB 2.5in 5400RPM 8MB buffer SATA
 Other Hardware:

Software

OpenMP Threads: 4
 Parallel: OpenMP
 Operating System: Red Hat Enterprise Linux AS release 4 (Nahant Update 4)
 Compiler: Intel(R) C++ Compiler 9.1.045 for Intel(R) 64 Linux (Build 20061101)
 Intel(R) FORTRAN Compiler 9.1.040 for Intel(R) 64 Linux (Build 20061101)
 GNU C Compiler (3.4.6 20060404 (Red Hat 3.4.6-3))
 File System: Linux ext3
 System State: Default

Notes/Tuning Information

```
Baseline optimization flags:
COPTIMIZE = -O2 -xT -ipo -openmp -ansi_alias
FOPTIMIZE = -O2 -xT -ipo -openmp
export KMP_AFFINITY=compact,0
export KMP_LIBRARY=turnaround
export KMP_STACKSIZE=31M
export OMP_DYNAMIC=FALSE
ONESTEP=yes
ulimit -s 64000
BIOS version S5000.86B.05.00.0070.111720061421 11/17/2006
  Snoop Filter enabled on BIOS
  HiBW FSB option enabled on BIOS
Portability Flags:
  318.galgel_m: -FI -132
Extra Flags:
  330.art_m: -DINTS_PER_CACHELINE=32 -DDBLS_PER_CACHELINE=16
  all: -gcc-name=/usr/bin/gcc
GCC and binutils used for include/lib files and linking.
Flags Description in
  INTEL-20061120-Linux-Intel9.1-Intel64.txt
```