



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_int_base = 14.1

PowerEdge R470 (Intel Xeon 6505P)

SPECspeed®2017_int_peak = 14.3

CPU2017 License: 6573

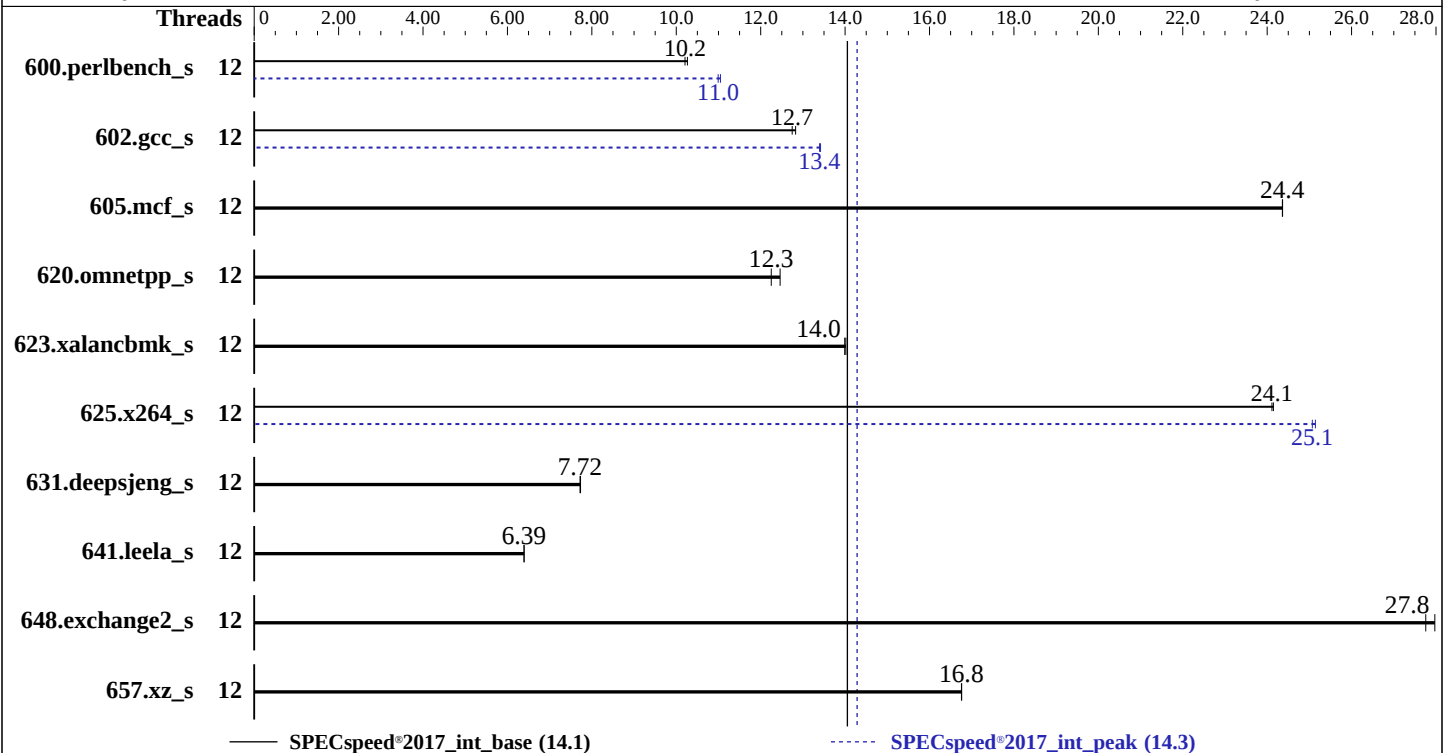
Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: May-2025

Hardware Availability: Apr-2025

Software Availability: Jun-2024



Hardware

CPU Name: Intel Xeon 6505P
Max MHz: 4100
Nominal: 2200
Enabled: 12 cores, 1 chip
Orderable: 1 chip
Cache L1: 64 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 48 MB I+D on chip per chip
Other: None
Memory: 256 GB (8 x 32 GB 2Rx8 PC5-6400B-R)
Storage: 40 GB on tmpfs
Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP6
6.4.0-150600.21-default
Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++
Compiler for Linux;
Fortran: Version 2024.1 of Intel Fortran Compiler
for Linux;
Parallel: Yes
Firmware: Version 1.3.1 released Apr-2025
File System: tmpfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS set to prefer performance at the cost of
additional power usage.



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_int_base = 14.1

PowerEdge R470 (Intel Xeon 6505P)

SPECspeed®2017_int_peak = 14.3

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: May-2025

Hardware Availability: Apr-2025

Software Availability: Jun-2024

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	12	173	10.3	<u>174</u>	<u>10.2</u>			12	161	11.0	<u>161</u>	<u>11.0</u>		
602.gcc_s	12	310	12.8	<u>312</u>	<u>12.7</u>			12	<u>297</u>	<u>13.4</u>	297	13.4		
605.mcf_s	12	<u>194</u>	<u>24.4</u>	194	24.4			12	<u>194</u>	<u>24.4</u>	194	24.4		
620.omnetpp_s	12	131	12.5	<u>133</u>	<u>12.3</u>			12	131	12.5	<u>133</u>	<u>12.3</u>		
623.xalancbmk_s	12	101	14.0	<u>101</u>	<u>14.0</u>			12	101	14.0	<u>101</u>	<u>14.0</u>		
625.x264_s	12	73.0	24.1	<u>73.2</u>	<u>24.1</u>			12	70.2	25.1	<u>70.4</u>	<u>25.1</u>		
631.deepsjeng_s	12	<u>186</u>	<u>7.72</u>	185	7.73			12	<u>186</u>	<u>7.72</u>	185	7.73		
641.leela_s	12	<u>267</u>	<u>6.39</u>	267	6.40			12	<u>267</u>	<u>6.39</u>	267	6.40		
648.exchange2_s	12	<u>106</u>	<u>27.8</u>	105	28.0			12	<u>106</u>	<u>27.8</u>	105	28.0		
657.xz_s	12	369	16.8	<u>369</u>	<u>16.8</u>			12	369	16.8	<u>369</u>	<u>16.8</u>		

SPECspeed®2017_int_base = 14.1

SPECspeed®2017_int_peak = 14.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH =

"/mnt/ramdisk/cpu2017-1.1.9-ic2024.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.9-ic2024.1/je5.0.1-64"

MALLOC_CONF = "retain:true"

OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM

memory using Redhat Enterprise Linux 8.0

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

sync; echo 3> /proc/sys/vm/drop_caches

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)

is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)

is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)

is mitigated in the system as tested and documented.

Benchmark run from a 40 GB ramdisk created with the cmd: "mount -t tmpfs -o size=40G tmpfs /mnt/ramdisk"



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_int_base = 14.1

PowerEdge R470 (Intel Xeon 6505P)

SPECspeed®2017_int_peak = 14.3

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: May-2025

Hardware Availability: Apr-2025

Software Availability: Jun-2024

Platform Notes

BIOS Settings:

Logical Processor : Disabled
Virtual NUMA : Disabled
Sub NUMA Cluster : Enabled
MADT Core Enumeration : Linear
LLC Prefetch : Enabled
Optimizer Mode : Enabled

System Profile : Custom
CPU Power Management : Maximum Performance
Energy Efficient Turbo : Disabled
C1E : Disabled
C-States : Autonomous
Latency Optimized Mode : Enabled
Energy Efficient Policy : Performance
CPU Interconnect Bus -
Link Power Management : Disabled
PCI ASPM L1 Link Power Management : Disabled
Correctable Memory ECC SMI : Disabled
DIMM Self Healing -
on Uncorrectable Memory Error : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-ic2024.1/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on 1234567-R470 Mon May 26 02:35:18 2025

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

1. uname -a
Linux 1234567-R470 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
x86_64 x86_64 x86_64 GNU/Linux

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_int_base = 14.1

PowerEdge R470 (Intel Xeon 6505P)

SPECspeed®2017_int_peak = 14.3

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: May-2025

Hardware Availability: Apr-2025

Software Availability: Jun-2024

Platform Notes (Continued)

```
-----
2. w
  02:35:18 up 3 min,  1 user,  load average: 0.05, 0.03, 0.00
USER      TTY      FROM          LOGIN@   IDLE   JCPU   PCPU WHAT
root      tty1      -             02:32   46.00s 0.72s  0.00s /bin/bash
/home/DellFiles/bin/Intel/dell-run-speccpu.sh speed --define DL-VERS=6.2a --output_format html,pdf,txt

-----
3. Username
  From environment variable $USER:  root

-----
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals         (-i) 1029558
max locked memory       (kbytes, -l) 8192
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size               (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 1029558
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited

-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
login -- root
-bash
/bin/bash /home/DellFiles/bin/DELL_speed.sh
/bin/bash /home/DellFiles/bin/dell-run-main.sh speed
/bin/bash /home/DellFiles/bin/dell-run-main.sh speed
/bin/bash /home/DellFiles/bin/Intel/dell-run-speccpu.sh speed --define DL-VERS=6.2a --output_format
html,pdf,txt
/bin/bash /home/DellFiles/bin/Intel/dell-run-speccpu.sh speed --define DL-VERS=6.2a --output_format
html,pdf,txt
runcpu --nobuild --action validate --define default-platform-flags -c
ic2024.1-lin-sapphirerapids-speed-20240308.cfg --define cores=12 --tune base,peak -o all --define
intspeedaffinity --define drop_caches --iterations 2 --define DL-VERS=6.2a --output_format html,pdf,txt
intspeed
runcpu --nobuild --action validate --define default-platform-flags --configfile
ic2024.1-lin-sapphirerapids-speed-20240308.cfg --define cores=12 --tune base,peak --output_format all
--define intsppedaffinity --define drop_caches --iterations 2 --define DL-VERS=6.2a --output_format
html,pdf,txt --nopower --runmode speed --tune base:peak --size refspped intspped --nopreenv --note-preenv
--logfile $SPEC/tmp/CPU2017.001/temlogs/preenv.intspped.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-ic2024.1

-----
6. /proc/cpuinfo
model name      : Intel(R) Xeon(R) 6505P
vendor_id      : GenuineIntel
cpu family     : 6
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_int_base = 14.1

PowerEdge R470 (Intel Xeon 6505P)

SPECspeed®2017_int_peak = 14.3

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: May-2025

Hardware Availability: Apr-2025

Software Availability: Jun-2024

Platform Notes (Continued)

```
model          : 173
stepping       : 1
microcode      : 0x10003a5
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs bhi
cpu cores      : 12
siblings       : 12
1 physical ids (chips)
12 processors (hardware threads)
physical id 0: core ids 0-11
physical id 0: apicids 0,2,4,6,8,10,12,14,16,18,20,22
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.39.3:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:          52 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                 12
On-line CPU(s) list:   0-11
Vendor ID:              GenuineIntel
BIOS Vendor ID:        Intel
Model name:             Intel(R) Xeon(R) 6505P
BIOS Model name:       Intel(R) Xeon(R) 6505P  CPU @ 2.2GHz
BIOS CPU family:       179
CPU family:             6
Model:                  173
Thread(s) per core:    1
Core(s) per socket:    12
Socket(s):              1
Stepping:               1
BogoMIPS:               4400.00
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                        pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
                        pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
                        nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni
                        pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
                        xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
                        tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
                        3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 intel_ppin cdp_l2
                        ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept
                        vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid
                        rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt
                        clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
                        xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
                        split_lock_detect user_shstk avx_vnni avx512_bf16 wbnoinvd dtherm ida
                        arat pln pts vnni avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni
                        vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57
                        rdpid bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear
                        serialize tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile
                        amx_int8 flush_l1d arch_capabilities
Virtualization:         VT-x
L1d cache:              576 KiB (12 instances)
L1i cache:              768 KiB (12 instances)
L2 cache:               24 MiB (12 instances)
L3 cache:               48 MiB (1 instance)
NUMA node(s):          1
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_int_base = 14.1

PowerEdge R470 (Intel Xeon 6505P)

SPECspeed®2017_int_peak = 14.3

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: May-2025

Hardware Availability: Apr-2025

Software Availability: Jun-2024

Platform Notes (Continued)

NUMA node0 CPU(s): 0-11
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling; PBRSE-IBRS Not affected; BHI BHI_DIS_S
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	576K	12	Data	1	64	1	64
L1i	64K	768K	16	Instruction	1	64	1	64
L2	2M	24M	16	Unified	2	2048	1	64
L3	48M	48M	16	Unified	3	49152	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 1 nodes (0)
node 0 cpus: 0-11
node 0 size: 257418 MB
node 0 free: 246163 MB
node distances:
node 0
0: 10

9. /proc/meminfo

MemTotal: 263596892 kB

10. who -r

run-level 3 May 26 02:32

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)

Default Target Status
multi-user running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd bluetooth cron display-manager getty@ irqbalance issue-generator kbdsettings klog lvm2-monitor nscd nvme-fc-boot-connections nvme-fc-autoconnect postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore wpa_supplicant
enabled-runtime	systemd-remount-fs
disabled	accounts-daemon autofs autostart-initscripts blk-availability bluetooth-mesh boot-sysctl ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell dnsmasq ebtables exchange-bmc-os-info firewallld fsidd gpm grub2-once haveged hwloc-dump-hwdata

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_int_base = 14.1

PowerEdge R470 (Intel Xeon 6505P)

SPECspeed®2017_int_peak = 14.3

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: May-2025

Hardware Availability: Apr-2025

Software Availability: Jun-2024

Platform Notes (Continued)

indirect
ipmi ipmievdev issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap
nm-cloud-setup nmb openvpn@ ostree-remount pppoe pppoe-server rpcbind rpmconfigcheck
rsyncd rtkit-daemon serial-getty@ smartd_generate_opts smb snmpd snmptrapd
speech-dispatcherd systemd-boot-check-no-failures systemd-confext
systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd udisks2
update-system-flatpaks upower vncserver@ wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6
wickedd-nanny wpa_supplicant@
pcscd systemd-userdbd wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
root=UUID=1c12c33d-b75a-4b70-bf85-f673b4f99355
splash=silent
resume=/dev/disk/by-uuid/8694d044-c578-4891-879e-7f98dfefe412
mitigations=auto
quiet
security=apparmor

14. cpupower frequency-info
analyzing CPU 3:
Unable to determine current policy
boost state support:
Supported: yes
Active: yes

15. sysctl
kernel.numa_balancing 0
kernel.randomize_va_space 2
vm.compaction_proactiveness 20
vm.dirty_background_bytes 0
vm.dirty_background_ratio 10
vm.dirty_bytes 0
vm.dirty_expire_centisecs 3000
vm.dirty_ratio 20
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold 500
vm.min_unmapped_ratio 1
vm.nr_hugepages 0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness 60
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode 0

16. /sys/kernel/mm/transparent_hugepage
defrag always defer defer+madvice [madvice] never
enabled [always] madvice never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force

17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag 1

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_int_base = 14.1

PowerEdge R470 (Intel Xeon 6505P)

SPECspeed®2017_int_peak = 14.3

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: May-2025

Hardware Availability: Apr-2025

Software Availability: Jun-2024

Platform Notes (Continued)

max_ptes_none 511
max_ptes_shared 256
max_ptes_swap 64
pages_to_scan 4096
scan_sleep_millisecs 10000

18. OS release

From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6

19. Disk information

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-ic2024.1
Filesystem Type Size Used Avail Use% Mounted on
tmpfs tmpfs 40G 5.0G 36G 13% /mnt/ramdisk

20. /sys/devices/virtual/dmi/id

Vendor: Dell Inc.
Product: PowerEdge R470
Product Family: PowerEdge
Serial: 1234567

21. dmidecode

Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
8x 00AD042300AD HMC688AHBRA471N 32 GB 2 rank 6400

22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Dell Inc.
BIOS Version: 1.3.1
BIOS Date: 04/24/2025
BIOS Revision: 1.3

Compiler Version Notes

C | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak)
| 657.xz_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C++ | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak)
| 641.leela_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_int_base = 14.1

PowerEdge R470 (Intel Xeon 6505P)

SPECspeed®2017_int_peak = 14.3

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: May-2025

Hardware Availability: Apr-2025

Software Availability: Jun-2024

Compiler Version Notes (Continued)

=====

Fortran | 648.exchange2_s(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_int_base = 14.1

PowerEdge R470 (Intel Xeon 6505P)

SPECspeed®2017_int_peak = 14.3

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: May-2025

Hardware Availability: Apr-2025

Software Availability: Jun-2024

Base Optimization Flags (Continued)

Fortran benchmarks:

```
-w -m64 -wl, -z, muldefs -xsapphirerapids -O3 -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -w -m64 -std=c11 -wl, -z, muldefs  
-fprofile-generate(pass 1)  
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)  
-flto -Ofast(pass 1) -xCORE-AVX512 -O3 -ffast-math  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-fiopenmp -DSPEC_OPENMP -fno-strict-overflow  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

```
602.gcc_s: -w -m64 -std=c11 -wl, -z, muldefs  
-fprofile-generate(pass 1)  
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)  
-flto -Ofast(pass 1) -xCORE-AVX512 -O3 -ffast-math  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_int_base = 14.1

PowerEdge R470 (Intel Xeon 6505P)

SPECspeed®2017_int_peak = 14.3

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: May-2025

Hardware Availability: Apr-2025

Software Availability: Jun-2024

Peak Optimization Flags (Continued)

602.gcc_s (continued):

-fiopenmp -DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-fno-alias -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes

C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: basepeak = yes

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.13.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.13.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2025-05-25 14:35:18-0400.

Report generated on 2025-07-16 11:05:27 by CPU2017 PDF formatter v6716.

Originally published on 2025-07-15.