



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(2.60 GHz, AMD EPYC 9655P)

SPECspeed®2017_int_base = 19.3

SPECspeed®2017_int_peak = 19.4

CPU2017 License: 3

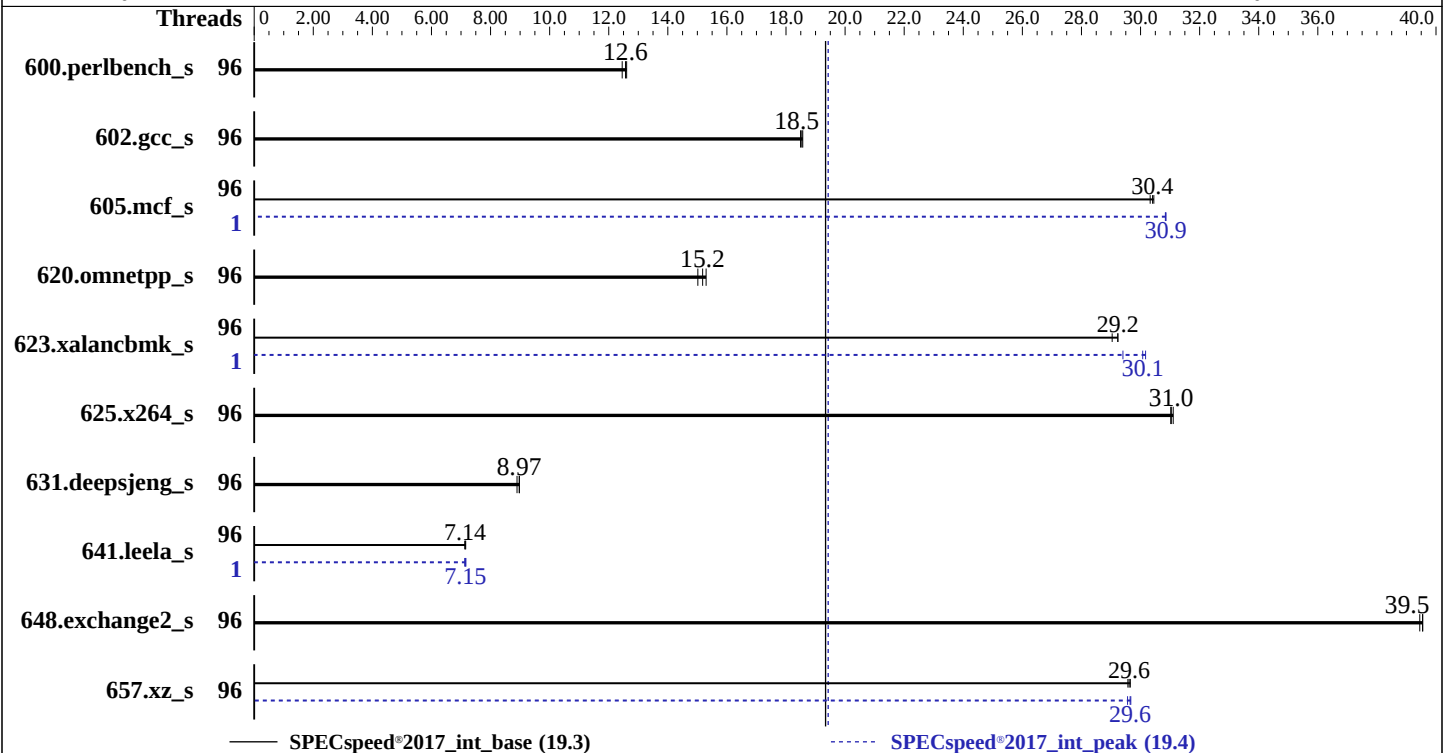
Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2025

Hardware Availability: Mar-2025

Software Availability: Oct-2024



Hardware

CPU Name: AMD EPYC 9655P
Max MHz: 4500
Nominal: 2600
Enabled: 96 cores, 1 chip
Orderable: 1 chip
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 384 MB I+D on chip per chip,
32 MB shared / 8 cores
Other: None
Memory: 384 GB (12 x 32 GB 2Rx8 PC5-6400B-R)
Storage: 1 x 480 GB SATA SSD
Other: CPU Cooling: CLC

Software

OS: SUSE Linux Enterprise Server 15 SP6
Kernel 6.4.0-150600.21-default
Compiler: C/C++/Fortran: Version 5.0.0 of AOCC
Parallel: Yes
Firmware: HPE BIOS Version v2.30 01/17/2025 released Jan-2025
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(2.60 GHz, AMD EPYC 9655P)

SPECspeed®2017_int_base = 19.3

SPECspeed®2017_int_peak = 19.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2025

Hardware Availability: Mar-2025

Software Availability: Oct-2024

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	96	141	12.6	<u>141</u>	<u>12.6</u>	143	12.5	96	141	12.6	<u>141</u>	<u>12.6</u>	143	12.5
602.gcc_s	96	<u>215</u>	<u>18.5</u>	214	18.6	215	18.5	96	<u>215</u>	<u>18.5</u>	214	18.6	215	18.5
605.mcf_s	96	156	30.3	155	30.4	<u>155</u>	<u>30.4</u>	1	153	30.9	<u>153</u>	<u>30.9</u>	153	30.8
620.omnetpp_s	96	<u>107</u>	<u>15.2</u>	109	15.0	107	15.3	96	<u>107</u>	<u>15.2</u>	109	15.0	107	15.3
623.xalancbmk_s	96	<u>48.5</u>	<u>29.2</u>	48.8	29.0	48.5	29.2	1	47.0	30.2	48.2	29.4	<u>47.1</u>	<u>30.1</u>
625.x264_s	96	56.7	31.1	56.9	31.0	<u>56.8</u>	<u>31.0</u>	96	56.7	31.1	56.9	31.0	<u>56.8</u>	<u>31.0</u>
631.deepsjeng_s	96	<u>160</u>	<u>8.97</u>	161	8.90	160	8.98	96	<u>160</u>	<u>8.97</u>	161	8.90	160	8.98
641.leela_s	96	<u>239</u>	<u>7.14</u>	239	7.13	238	7.16	1	238	7.17	239	7.12	<u>239</u>	<u>7.15</u>
648.exchange2_s	96	74.3	39.6	74.5	39.5	<u>74.3</u>	<u>39.5</u>	96	74.3	39.6	74.5	39.5	<u>74.3</u>	<u>39.5</u>
657.xz_s	96	208	29.7	<u>209</u>	<u>29.6</u>	209	29.6	96	208	29.7	<u>209</u>	<u>29.6</u>	209	29.6

SPECspeed®2017_int_base = 19.3

SPECspeed®2017_int_peak = 19.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(2.60 GHz, AMD EPYC 9655P)

SPECspeed®2017_int_base = 19.3

SPECspeed®2017_int_peak = 19.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2025

Hardware Availability: Mar-2025

Software Availability: Oct-2024

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

GOMP_CPU_AFFINITY = "0-95"

LD_LIBRARY_PATH =

"/home/cpu2017_new/amd_speed_aocc500_znver5_A_lib/lib:/home/cpu2017_new/amd_speed_aocc500_znver5_A_lib/lib32:"

LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"

MALLOC_CONF = "retain:true"

OMP_DYNAMIC = "false"

OMP_SCHEDULE = "static"

OMP_STACKSIZE = "128M"

OMP_THREAD_LIMIT = "96"

Environment variables set by runcpu during the 605.mcf_s peak run:

GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 623.xalancbmk_s peak run:

GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 641.leela_s peak run:

GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 657.xz_s peak run:

GOMP_CPU_AFFINITY = "0-95"

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9D64 CPU + 500GiB Memory using Ubuntu 22.04

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Configuration

Workload Profile set to General Peak Frequency Compute

Determinism Control set to Manual

Performance Determinism set to Power Deterministic

Memory Patrol Scrubbing set to Disabled

Last-Level Cache (LLC) as NUMA Node set to Enabled

ACPI CST C2 Latency set to 18 microseconds

Thermal Configuration set to Maximum Cooling

AMD SMT Option set to Disabled

NUMA memory domains per socket set to Four memory domains per socket

Workload Profile set to Custom

Power Regulator set to OS Control Mode

The reference code/AGESA version used in this ROM is version Turin-PI 1.0.0.3

Sysinfo program /home/cpu2017_new/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on localhost.localdomain Tue Feb 4 07:53:08 2025

SUT (System Under Test) info as seen by some common utilities.

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(2.60 GHz, AMD EPYC 9655P)

SPECspeed®2017_int_base = 19.3

SPECspeed®2017_int_peak = 19.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2025

Hardware Availability: Mar-2025

Software Availability: Oct-2024

Platform Notes (Continued)

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. tuned-adm active
17. sysctl
18. /sys/kernel/mm/transparent_hugepage
19. /sys/kernel/mm/transparent_hugepage/khugepaged
20. OS release
21. Disk information
22. /sys/devices/virtual/dmi/id
23. dmidecode
24. BIOS

1. uname -a
Linux localhost.localdomain 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024
(36c1e09) x86_64 x86_64 x86_64 GNU/Linux

2. w
07:53:08 up 10:03, 2 users, load average: 0.07, 0.02, 0.00
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
root pts/0 172.17.1.96 21:52 12.00s 0.92s 0.22s /bin/bash ./amd_speed_aocc500_znver5_A1.sh

3. Username
From environment variable \$USER: root

4. ulimit -a
core file size (blocks, -c) unlimited
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 1546341
max locked memory (kbytes, -l) 2097152
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(2.60 GHz, AMD EPYC 9655P)

SPECspeed®2017_int_base = 19.3

SPECspeed®2017_int_peak = 19.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2025

Hardware Availability: Mar-2025

Software Availability: Oct-2024

Platform Notes (Continued)

max user processes (-u) 1546341
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@pts/0
-bash
python3 ./run_intspeed.py
/bin/bash ./amd_speed_aocc500_znver5_A1.sh
runcpu --config amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 intspeed
runcpu --configfile amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode speed --tune base:peak --size test:train:refspeed intspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.005/temlogs/preenv.intspeed.005.0.log --lognum 005.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017_new
```

6. /proc/cpuinfo

```
model name      : AMD EPYC 9655P 96-Core Processor
vendor_id       : AuthenticAMD
cpu family      : 26
model           : 2
stepping        : 1
microcode       : 0xb00211e
bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size        : 192 4K pages
cpu cores       : 96
siblings        : 96
1 physical ids (chips)
96 processors (hardware threads)
physical id 0:  core ids 0-95
physical id 0:  apicids 0-95
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.39.3:

```
Architecture:      x86_64
CPU op-mode(s):    32-bit, 64-bit
Address sizes:      52 bits physical, 57 bits virtual
Byte Order:         Little Endian
CPU(s):             96
On-line CPU(s) list: 0-95
Vendor ID:          AuthenticAMD
BIOS Vendor ID:     Advanced Micro Devices, Inc.
Model name:         AMD EPYC 9655P 96-Core Processor
BIOS Model name:    AMD EPYC 9655P 96-Core Processor
BIOS CPU family:    107
CPU family:         26
Model:              2
Thread(s) per core: 1
Core(s) per socket: 96
Socket(s):          1
Stepping:           1
```

CPU @ 2.6GHz

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(2.60 GHz, AMD EPYC 9655P)

SPECspeed®2017_int_base = 19.3

SPECspeed®2017_int_peak = 19.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2025

Hardware Availability: Mar-2025

Software Availability: Oct-2024

Platform Notes (Continued)

Frequency boost: enabled
CPU(s) scaling MHz: 110%
CPU max MHz: 2600.0000
CPU min MHz: 1500.0000
BogoMIPS: 5192.29
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2 ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin cppc arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect movdiri movdir64b overflow_recov succor smca fsrm avx512_vp2intersect flush_lld debug_swap
Virtualization: AMD-V
L1d cache: 4.5 MiB (96 instances)
L1i cache: 3 MiB (96 instances)
L2 cache: 96 MiB (96 instances)
L3 cache: 384 MiB (12 instances)
NUMA node(s): 12
NUMA node0 CPU(s): 0-7
NUMA node1 CPU(s): 8-15
NUMA node2 CPU(s): 16-23
NUMA node3 CPU(s): 24-31
NUMA node4 CPU(s): 32-39
NUMA node5 CPU(s): 40-47
NUMA node6 CPU(s): 48-55
NUMA node7 CPU(s): 56-63
NUMA node8 CPU(s): 64-71
NUMA node9 CPU(s): 72-79
NUMA node10 CPU(s): 80-87
NUMA node11 CPU(s): 88-95
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP disabled; RSB filling; PBRSE-eIBRS Not affected; BHI Not affected
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(2.60 GHz, AMD EPYC 9655P)

SPECspeed®2017_int_base = 19.3

SPECspeed®2017_int_peak = 19.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2025

Hardware Availability: Mar-2025

Software Availability: Oct-2024

Platform Notes (Continued)

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	4.5M	12	Data	1	64	1	64
L1i	32K	3M	8	Instruction	1	64	1	64
L2	1M	96M	16	Unified	2	1024	1	64
L3	32M	384M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 12 nodes (0-11)

node 0 cpus: 0-7

node 0 size: 31938 MB

node 0 free: 31770 MB

node 1 cpus: 8-15

node 1 size: 32253 MB

node 1 free: 32161 MB

node 2 cpus: 16-23

node 2 size: 32253 MB

node 2 free: 32142 MB

node 3 cpus: 24-31

node 3 size: 32253 MB

node 3 free: 32046 MB

node 4 cpus: 32-39

node 4 size: 32215 MB

node 4 free: 32035 MB

node 5 cpus: 40-47

node 5 size: 32253 MB

node 5 free: 32021 MB

node 6 cpus: 48-55

node 6 size: 32253 MB

node 6 free: 32046 MB

node 7 cpus: 56-63

node 7 size: 32253 MB

node 7 free: 32128 MB

node 8 cpus: 64-71

node 8 size: 32253 MB

node 8 free: 32081 MB

node 9 cpus: 72-79

node 9 size: 32253 MB

node 9 free: 32168 MB

node 10 cpus: 80-87

node 10 size: 32253 MB

node 10 free: 32166 MB

node 11 cpus: 88-95

node 11 size: 32177 MB

node 11 free: 32082 MB

node distances:

node	0	1	2	3	4	5	6	7	8	9	10	11
0:	10	11	11	12	12	12	12	12	12	12	12	12
1:	11	10	11	12	12	12	12	12	12	12	12	12
2:	11	11	10	12	12	12	12	12	12	12	12	12
3:	12	12	12	10	11	11	12	12	12	12	12	12
4:	12	12	12	11	10	11	12	12	12	12	12	12
5:	12	12	12	12	11	11	10	12	12	12	12	12
6:	12	12	12	12	12	12	10	11	11	12	12	12
7:	12	12	12	12	12	12	11	10	11	12	12	12
8:	12	12	12	12	12	12	11	11	10	12	12	12
9:	12	12	12	12	12	12	12	12	10	11	11	11
10:	12	12	12	12	12	12	12	12	12	11	10	11
11:	12	12	12	12	12	12	12	12	12	11	11	10

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(2.60 GHz, AMD EPYC 9655P)

SPECspeed®2017_int_base = 19.3

SPECspeed®2017_int_peak = 19.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2025

Hardware Availability: Mar-2025

Software Availability: Oct-2024

Platform Notes (Continued)

9. /proc/meminfo

MemTotal: 395894180 kB

10. who -r

run-level 3 Feb 3 21:51

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)

Default Target Status

multi-user degraded

12. Failed units, from systemctl list-units --state=failed

UNIT	LOAD	ACTIVE	SUB	DESCRIPTION
------	------	--------	-----	-------------

* NetworkManager-wait-online.service	loaded	failed	failed	Network Manager Wait Online
--------------------------------------	--------	--------	--------	-----------------------------

13. Services, from systemctl list-unit-files

STATE	UNIT FILES
-------	------------

enabled	ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online
---------	--

	YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd bluetooth cron display-manager getty@ irqbalance issue-generator kbdsettings klog lvm2-monitor nscd postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore wpa_supplicant
--	--

enabled-runtime	systemd-remount-fs
-----------------	--------------------

disabled	accounts-daemon autofs autoyast-initscripts blk-availability bluetooth-mesh boot-sysctl ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell dmraid-activation dnsmasq ebttables exchange-bmc-os-info firewallld fsidd gpm grub2-once haveged hwloc-dump-hwdata ipmi ipmievd issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nmb openvpn@ ostree-remount rpcbind rpmconfigcheck rsyncd rtkit-daemon serial-getty@ smartd_generate_opts smb snmpd snmptrapd speech-dispatcherd systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd tuned udisks2 update-system-flatpaks upower vncserver@ wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny wpa_supplicant@ pcsd saned@ systemd-userdbd wickedd
----------	---

indirect	
----------	--

14. Linux kernel boot-time arguments, from /proc/cmdline

BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default

root=UUID=2ecde7f3-1fd0-4b78-b405-d37301501b53

splash=silent

mitigations=auto

quiet

security=apparmor

15. cpupower frequency-info

analyzing CPU 36:

current policy: frequency should be within 1.50 GHz and 2.60 GHz.

The governor "performance" may decide which speed to use within this range.

boost state support:

Supported: yes

Active: yes

16. tuned-adm active

No current active profile.

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(2.60 GHz, AMD EPYC 9655P)

SPECspeed®2017_int_base = 19.3

SPECspeed®2017_int_peak = 19.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2025

Hardware Availability: Mar-2025

Software Availability: Oct-2024

Platform Notes (Continued)

```
-----
17. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space      0
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                 8
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           1

-----
18. /sys/kernel/mm/transparent_hugepage
defrag          [always] defer defer+madvice madvice never
enabled         [always] madvice never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force

-----
19. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                 1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000

-----
20. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6

-----
21. Disk information
SPEC is set to: /home/cpu2017_new
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdc4       xfs   436G  144G  293G  33% /home

-----
22. /sys/devices/virtual/dmi/id
Vendor:         HPE
Product:        ProLiant DL325 Gen11
Product Family: ProLiant
Serial:         DL325G11-010

-----
23. dmidecode
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(2.60 GHz, AMD EPYC 9655P)

SPECspeed®2017_int_base = 19.3

SPECspeed®2017_int_peak = 19.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2025

Hardware Availability: Mar-2025

Software Availability: Oct-2024

Platform Notes (Continued)

Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

12x Hynix HMC88AHBRA472N 32 GB 2 rank 6400

24. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: HPE
BIOS Version: 2.30
BIOS Date: 01/17/2025
BIOS Revision: 2.30
Firmware Revision: 1.63

Compiler Version Notes

```
C | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak)
  | 657.xz_s(base, peak)
```

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

```
C++ | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak)
    | 641.leela_s(base, peak)
```

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

```
Fortran | 648.exchange2_s(base, peak)
```

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Base Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(2.60 GHz, AMD EPYC 9655P)

SPECspeed®2017_int_base = 19.3

SPECspeed®2017_int_peak = 19.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2025

Hardware Availability: Mar-2025

Software Availability: Oct-2024

Base Compiler Invocation (Continued)

Fortran benchmarks:

flang

Base Portability Flags

600.perlbench_s: -DSPEC_LINUX_X64 -DSPEC_LP64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LINUX -DSPEC_LP64
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition -Wl,-mllvm -Wl,-extra-inliner -O3
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP
-flto -fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp -lamdlibm
-lflang -lamdalloc

C++ benchmarks:

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -mllvm -unroll-threshold=100 -zopt
-fvirtual-function-elimination -fvisibility=hidden -fopenmp=libomp
-lomp -lamdlibm -lflang -lamdalloc-ext

Fortran benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(2.60 GHz, AMD EPYC 9655P)

SPECspeed®2017_int_base = 19.3

SPECspeed®2017_int_peak = 19.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2025

Hardware Availability: Mar-2025

Software Availability: Oct-2024

Base Optimization Flags (Continued)

Fortran benchmarks (continued):

-Wl,-mllvm -Wl,-enable-iv-split -Wl,-mllvm -Wl,-inline-recursion=4
-Wl,-mllvm -Wl,-lsr-in-nested-loop -O3 -march=znver5 -fveclib=AMDLIBM
-ffast-math -fopenmp -flt0 -mllvm -optimize-strided-mem-cost
-mllvm -unroll-aggressive -mllvm -unroll-threshold=150 -fopenmp=libomp
-lomp -lamdlibm -lflang -lamdalloc

Base Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(2.60 GHz, AMD EPYC 9655P)

SPECspeed®2017_int_base = 19.3

SPECspeed®2017_int_peak = 19.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2025

Hardware Availability: Mar-2025

Software Availability: Oct-2024

Peak Optimization Flags (Continued)

600.perlbench_s: basepeak = yes

602.gcc_s: basepeak = yes

605.mcf_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -flto
-DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang

625.x264_s: basepeak = yes

657.xz_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -flto
-DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang

C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=advanced -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=100 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-mllvm -do-block-reorder=advanced -fopenmp=libomp -lomp
-lamdlibm -lamdalloc-ext -lflang

631.deepsjeng_s: basepeak = yes

641.leela_s: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(2.60 GHz, AMD EPYC 9655P)

SPECspeed®2017_int_base = 19.3

SPECspeed®2017_int_peak = 19.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2025

Hardware Availability: Mar-2025

Software Availability: Oct-2024

Peak Optimization Flags (Continued)

641.leela_s (continued):

-Wl, -mllvm -Wl, -reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=100 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang

Fortran benchmarks:

648.exchange2_s: basepeak = yes

Peak Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Turin-rev1.5.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Turin-rev1.5.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2025-02-04 07:53:08-0500.

Report generated on 2025-05-08 10:01:52 by CPU2017 PDF formatter v6716.

Originally published on 2025-05-06.