



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen11

(2.50 GHz, Intel Xeon Gold 6426Y)

SPECrate®2017_int_base = 162

SPECrate®2017_int_peak = Not Run

CPU2017 License: 3

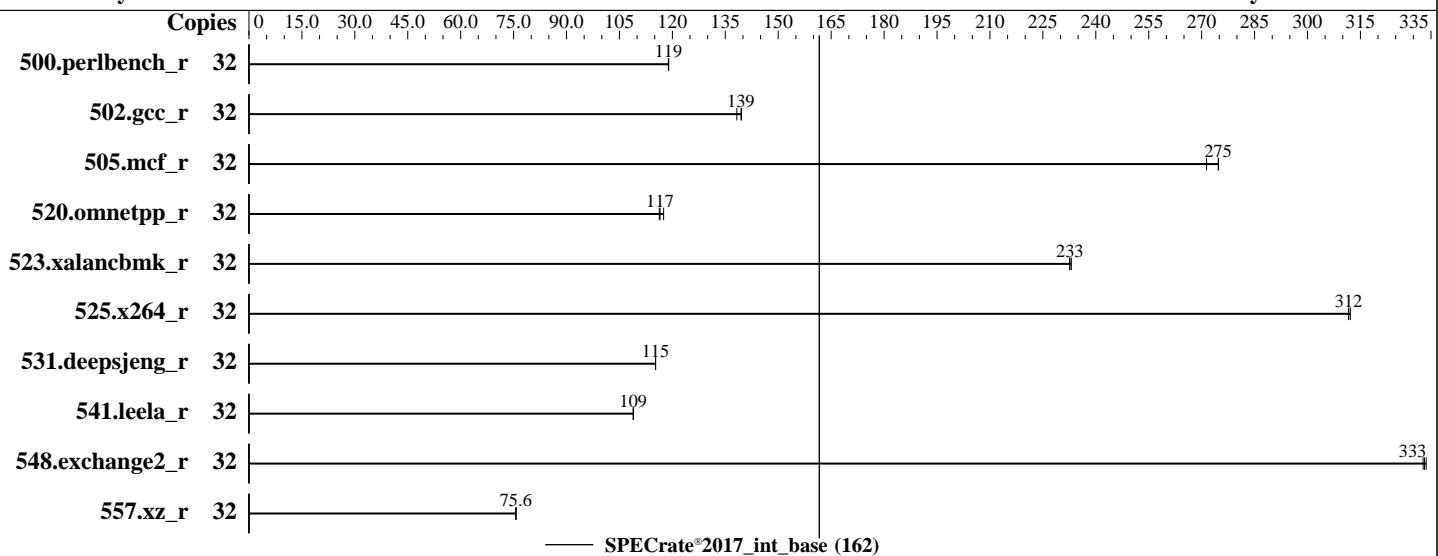
Test Date: Mar-2024

Test Sponsor: HPE

Hardware Availability: Jan-2024

Tested by: HPE

Software Availability: Dec-2023



Hardware

CPU Name: Intel Xeon Gold 6426Y
 Max MHz: 4100
 Nominal: 2500
 Enabled: 16 cores, 1 chip, 2 threads/core
 Orderable: 1 chip
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 37.5 MB I+D on chip per chip
 Other: None
 Memory: 256 GB (8 x 32 GB 2Rx8 PC5-4800B-R)
 Storage: 1 x 960 GB SATA SSD
 Other: CPU Cooling: Air

OS:

SUSE Linux Enterprise Server 15 SP4

Kernel 5.14.21-150400.22-default

C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux;
 Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;

No

HPE BIOS Version v2.16 03/01/2024 released Jan-2024

xfs

Run level 5 (multi-user)

64-bit

Not Applicable

None

Compiler:

Power Management: BIOS and OS set to prefer performance at the cost of additional power usage

Software



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	32	429	119	428	119	428	119							
502.gcc_r	32	328	138	325	140	325	139							
505.mcf_r	32	188	275	191	271	188	275							
520.omnetpp_r	32	360	117	361	116	357	118							
523.xalancbmk_r	32	145	233	145	233	145	233							
525.x264_r	32	180	312	180	312	180	312							
531.deepsjeng_r	32	318	115	318	115	318	115							
541.leela_r	32	486	109	486	109	486	109							
548.exchange2_r	32	252	333	252	333	251	334							
557.xz_r	32	457	75.6	456	75.7	457	75.6							

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
tuned-adm profile was set to throughput-performance using "tuned-adm profile throughput-performance"
```

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/CPU2017/lib/intel64:/home/CPU2017/lib/ia32:/home/CPU2017/je5.0.1-32"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.

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General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Configuration:

Workload Profile set to General Throughput Compute

Enhanced Processor Performance Profile set to Aggressive

Thermal Configuration set to Maximum Cooling

Memory Patrol Scrubbing set to Disabled

Last Level Cache (LLC) Dead Line Allocation set to Disabled

Workload Profile set to Custom

Adjacent Sector Prefetch set to Disabled

DCU Stream Prefetcher set to Disabled

Minimum Processor Idle Power Package C-State set to Package C6 (non-retention) State

Sysinfo program /home/CPU2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Mon Mar 25 10:45:58 2024

SUT (System Under Test) info as seen by some common utilities.

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11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
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23. BIOS

1. uname -a
Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)
x86_64 x86_64 x86_64 GNU/Linux

2. w
10:45:58 up 1 min, 0 users, load average: 1.71, 0.89, 0.34
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT

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Platform Notes (Continued)

3. Username
From environment variable \$USER: root

4. ulimit -a

core file size	(blocks, -c) unlimited
data seg size	(kbytes, -d) unlimited
scheduling priority	(-e) 0
file size	(blocks, -f) unlimited
pending signals	(-i) 1030617
max locked memory	(kbytes, -l) 64
max memory size	(kbytes, -m) unlimited
open files	(-n) 1024
pipe size	(512 bytes, -p) 8
POSIX message queues	(bytes, -q) 819200
real-time priority	(-r) 0
stack size	(kbytes, -s) unlimited
cpu time	(seconds, -t) unlimited
max user processes	(-u) 1030617
virtual memory	(kbytes, -v) unlimited
file locks	(-x) unlimited

5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root@notty
bash -c cd \$SPEC/ && \$SPEC/intrate.sh
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=32 -c
ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=16 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base -o all intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=32 --configfile
ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=16 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base --output_format all --nopower --runmode
rate --tune base --size refrate intrate --nopreenv --note-preenv --logfile
\$SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
specperl \$SPEC/bin/sysinfo
\$SPEC = /home/CPU2017

6. /proc/cpuinfo

model name	: Intel(R) Xeon(R) Gold 6426Y
vendor_id	: GenuineIntel
cpu family	: 6
model	: 143
stepping	: 8
microcode	: 0x2b000590
bugs	: spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores	: 16
siblings	: 32
1 physical ids (chips)	
32 processors (hardware threads)	
physical id 0: core ids 0-15	
physical id 0: apicids 0-31	

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

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Platform Notes (Continued)

7. lscpu

From lscpu from util-linux 2.37.2:

```

Architecture:          x86_64
CPU op-mode(s):       32-bit, 64-bit
Address sizes:        46 bits physical, 57 bits virtual
Byte Order:           Little Endian
CPU(s):               32
On-line CPU(s) list: 0-31
Vendor ID:            GenuineIntel
Model name:           Intel(R) Xeon(R) Gold 6426Y
CPU family:           6
Model:                143
Thread(s) per core:   2
Core(s) per socket:   16
Socket(s):            1
Stepping:             8
BogoMIPS:             5000.00
Flags:                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                      clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                      lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtTopology
                      nonstop_tsc cpuid aperfmpf tsc_known_freq pn1 pclmulqdq dtes64 monitor
                      ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtrpr pdcm pcid dca sse4_1
                      sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                      lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13
                      invpcid_single cdp_12 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow
                      vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmil hle avx2 smep
                      bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
                      avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                      xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                      cqm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
                      arat pln pts avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes
                      vpcimulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid
                      bus_lock_detect coldemote movdir64b enqcmd fsrm md_clear serialize
                      tsxldtrk pconfig arch_lbr avx512_fp16 amx_tile flush_l1d arch_capabilities
Virtualization:        VT-x
L1d cache:             768 KiB (16 instances)
L1i cache:             512 KiB (16 instances)
L2 cache:              32 MiB (16 instances)
L3 cache:              37.5 MiB (1 instance)
NUMA node(s):          2
NUMA node0 CPU(s):     0-7,16-23
NUMA node1 CPU(s):     8-15,24-31
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:    Not affected
Vulnerability Mds:    Not affected
Vulnerability Meltdown: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
Vulnerability Srbds:   Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	768K	12	Data	1	64	1	64
L1i	32K	512K	8	Instruction	1	64	1	64
L2	2M	32M	16	Unified	2	2048	1	64
L3	37.5M	37.5M	15	Unified	3	40960	1	64

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Platform Notes (Continued)

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0-7,16-23
node 0 size: 128713 MB
node 0 free: 127303 MB
node 1 cpus: 8-15,24-31
node 1 size: 128965 MB
node 1 free: 128370 MB
node distances:
node 0 1
0: 10 20
1: 20 10

9. /proc/meminfo

MemTotal: 263863492 kB

10. who -r

run-level 5 Mar 25 10:45

11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)

Default Target Status
graphical running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	ModemManager YaST2-Firstboot YaST2-Second-Stage apparmor auditd bluetooth cron display-manager getty@ haveged irqbalance iscsi issue-generator kbdsettings klog lvm2-monitor nsqd postfix purge-kernels rollback rsyslog smartd sshd wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny wpa_supplicant
enabled-runtime	systemd-remount-fs
disabled	NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon appstream-sync-cache autofs autoyast-initscripts blk-availability bluetooth-mesh boot-sysctl ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell dnsmasq ebttables exchange-bmc-os-info firewalld gpm grub2-once haveged-switch-root ipmi ipmievfd iscsid iscsiuio issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nm-cloud-setup nmb openvpn@ ostree-remount pppoe pppoe-server rdisc rpcbind rpmconfigcheck rsyncd rtkit-daemon serial-getty@ smartd_generate_opts smb snmpd snmptrapd speech-dispatcherd systemd-boot-check-no-failures systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd tuned udisks2 upower wpa_supplicant@
indirect	pcscd saned@ wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline

BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
root=UUID=16038a66-0f95-499e-a756-89b7efc430ae
splash=silent
resume=/dev/disk/by-uuid/1ac00a3a-7fbb-40b6-8213-f8e2decaf1b0
mitigations=auto
quiet
security=apparmor

14. cpupower frequency-info

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Platform Notes (Continued)

```
analyzing CPU 0:  
  Unable to determine current policy  
  boost state support:  
    Supported: yes  
    Active: yes
```

```
-----  
15. tuned-adm active  
  Current active profile: throughput-performance
```

```
-----  
16. sysctl  
  kernel.numa_balancing          1  
  kernel.randomize_va_space      2  
  vm.compaction_proactiveness   20  
  vm.dirty_background_bytes     0  
  vm.dirty_background_ratio     10  
  vm.dirty_bytes                0  
  vm.dirty_expire_centisecs    3000  
  vm.dirty_ratio                20  
  vm.dirty_writeback_centisecs  500  
  vm.dirtytime_expire_seconds   43200  
  vm.extfrag_threshold          500  
  vm.min_unmapped_ratio         1  
  vm.nr_hugepages               0  
  vm.nr_hugepages_mempolicy     0  
  vm.nr_overcommit_hugepages    0  
  vm.swappiness                 10  
  vm.watermark_boost_factor    15000  
  vm.watermark_scale_factor     10  
  vm.zone_reclaim_mode          0
```

```
-----  
17. /sys/kernel/mm/transparent_hugepage  
  defrag           always defer defer+madvise [madvise] never  
  enabled          [always] madvise never  
  hpage_pmd_size  2097152  
  shmem_enabled    always within_size advise [never] deny force
```

```
-----  
18. /sys/kernel/mm/transparent_hugepage/khugepaged  
  alloc_sleep_millisecs  60000  
  defrag                  1  
  max_ptes_none          511  
  max_ptes_shared        256  
  max_ptes_swap          64  
  pages_to_scan          4096  
  scan_sleep_millisecs   10000
```

```
-----  
19. OS release  
  From /etc/*-release /etc/*-version  
  os-release SUSE Linux Enterprise Server 15 SP4
```

```
-----  
20. Disk information  
  SPEC is set to: /home/CPU2017  
  Filesystem  Type  Size  Used Avail Use% Mounted on  
  /dev/sda4    xfs   349G  43G  307G  13% /home
```

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Platform Notes (Continued)

```
-----  
21. /sys/devices/virtual/dmi/id  
    Vendor:          HPE  
    Product:         ProLiant DL360 Gen11  
    Product Family: ProLiant  
    Serial:          CNX20800PZ
```

```
-----  
22. dmidecode  
    Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you interpret this section.  
    The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately  
    determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the  
    "DMTF SMBIOS" standard.  
    Memory:  
        8x Hynix HMCG88MEBRA113N 32 GB 2 rank 4800
```

```
-----  
23. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
    BIOS Vendor:      HPE  
    BIOS Version:    2.16  
    BIOS Date:       03/01/2024  
    BIOS Revision:   2.16  
    Firmware Revision: 1.56
```

Compiler Version Notes

```
=====| 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base) 557.xz_r(base)
```

```
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
```

```
=====| 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base) 541.leela_r(base)
```

```
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
```

```
=====| 548.exchange2_r(base)
```

```
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
```

Base Compiler Invocation

C benchmarks:
icx

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Base Compiler Invocation (Continued)

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-EMR-rev1.0.html>

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-EMR-rev1.0.xml>

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2024-03-25 01:15:58-0400.

Report generated on 2024-04-24 14:30:24 by CPU2017 PDF formatter v6716.

Originally published on 2024-04-24.