



# SPEC CPU®2017 Integer Speed Result

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## Lenovo Global Technology

ThinkSystem SR665 V3  
4.10 GHz, AMD EPYC 9174F

**SPECSpeed®2017\_int\_base = 16.4**

**SPECSpeed®2017\_int\_peak = 16.6**

CPU2017 License: 9017

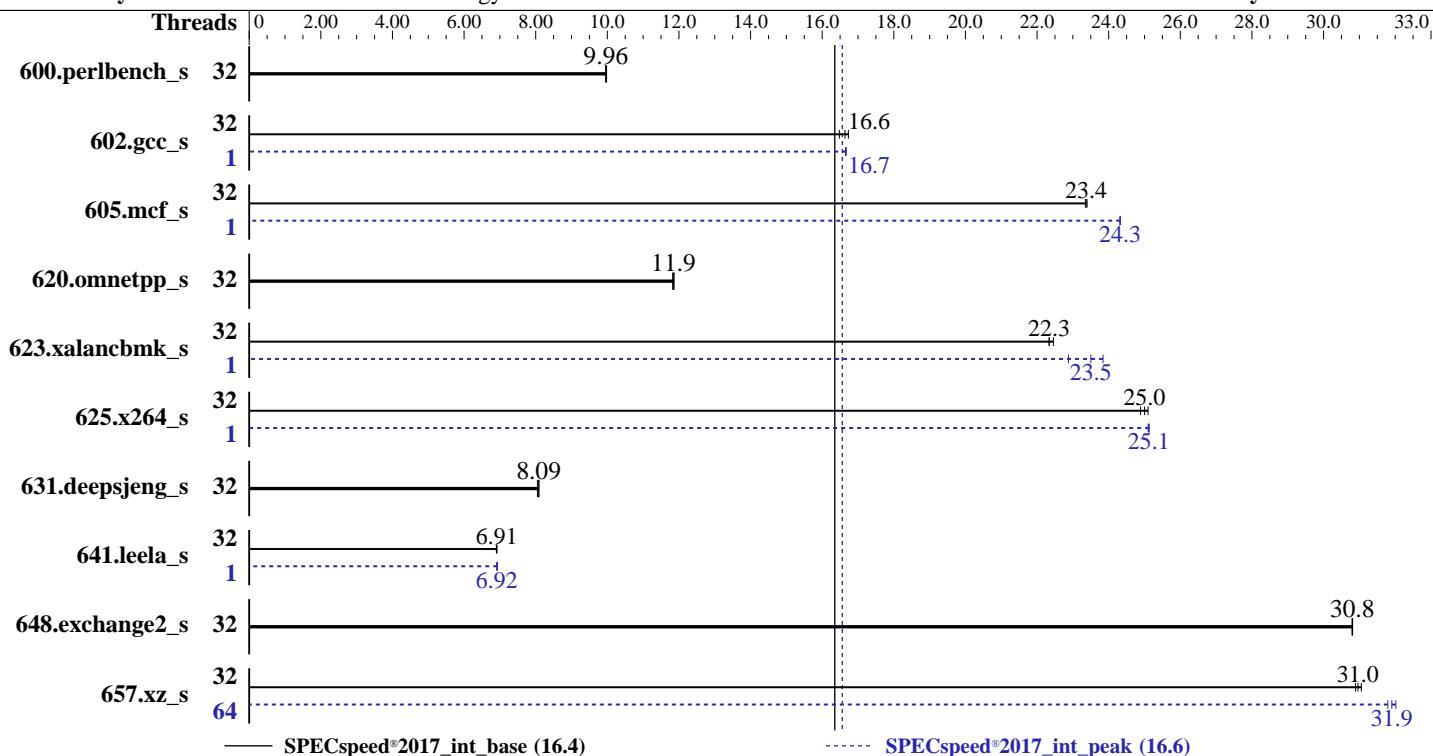
Test Date: Oct-2022

Test Sponsor: Lenovo Global Technology

Hardware Availability: Dec-2022

Tested by: Lenovo Global Technology

Software Availability: Nov-2022



Hardware		Software	
CPU Name:	AMD EPYC 9174F	OS:	Red Hat Enterprise Linux 8.6 (Ootpa)
Max MHz:	4400	Compiler:	Kernel 4.18.0-372.9.1.el8.x86_64
Nominal:	4100	Parallel:	C/C++/Fortran: Version 4.0.0 of AOCC
Enabled:	32 cores, 2 chips, 2 threads/core	Firmware:	Yes
Orderable:	1,2 chips	File System:	Lenovo BIOS Version KAE103A 1.10 released Sep-2022
Cache L1:	32 KB I + 32 KB D on chip per core	System State:	xfs
L2:	1 MB I+D on chip per core	Base Pointers:	Run level 3 (multi-user)
L3:	256 MB I+D on chip per chip, 32 MB shared / 2 cores	Peak Pointers:	64-bit
Other:	None	Other:	64-bit
Memory:	768 GB (24 x 32 GB 2Rx8 PC5-4800B-R)	Power Management:	None
Storage:	1 x 480 GB SATA SSD		BIOS and OS set to prefer performance at the cost of additional power usage
Other:	None		



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## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	32	<b><u>178</u></b>	<b><u>9.96</u></b>	178	9.95	178	9.98	32	<b><u>178</u></b>	<b><u>9.96</u></b>	178	9.95	178	9.98
602.gcc_s	32	<b><u>239</u></b>	<b><u>16.6</u></b>	238	16.7	242	16.5	1	<b><u>239</u></b>	<b><u>16.7</u></b>	<b><u>239</u></b>	<b><u>16.7</u></b>	239	16.6
605.mcf_s	32	202	23.4	202	23.3	<b><u>202</u></b>	<b><u>23.4</u></b>	1	<b><u>194</u></b>	<b><u>24.3</u></b>	194	24.3	194	24.3
620.omnetpp_s	32	138	11.8	137	11.9	<b><u>138</u></b>	<b><u>11.9</u></b>	32	138	11.8	137	11.9	<b><u>138</u></b>	<b><u>11.9</u></b>
623.xalancbmk_s	32	63.1	22.5	63.4	22.3	<b><u>63.4</u></b>	<b><u>22.3</u></b>	1	<b><u>60.3</u></b>	<b><u>23.5</u></b>	61.9	22.9	59.4	23.8
625.x264_s	32	70.9	24.9	70.3	25.1	<b><u>70.6</u></b>	<b><u>25.0</u></b>	1	70.2	25.1	70.3	25.1	<b><u>70.2</u></b>	<b><u>25.1</u></b>
631.deepsjeng_s	32	<b><u>177</u></b>	<b><u>8.09</u></b>	178	8.05	177	8.09	32	<b><u>177</u></b>	<b><u>8.09</u></b>	178	8.05	177	8.09
641.leela_s	32	<b><u>247</u></b>	<b><u>6.91</u></b>	247	6.90	247	6.92	1	<b><u>247</u></b>	<b><u>6.92</u></b>	247	6.92	246	6.93
648.exchange2_s	32	<b><u>95.4</u></b>	<b><u>30.8</u></b>	95.4	30.8	95.5	30.8	32	<b><u>95.4</u></b>	<b><u>30.8</u></b>	95.4	30.8	95.5	30.8
657.xz_s	32	200	30.9	199	31.1	<b><u>200</u></b>	<b><u>31.0</u></b>	64	193	32.0	194	31.8	<b><u>194</u></b>	<b><u>31.9</u></b>

SPECspeed®2017\_int\_base = 16.4

SPECspeed®2017\_int\_peak = 16.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

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## Operating System Notes (Continued)

To enable Transparent Hugepages (THP) for all allocations,  
'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

GOMP\_CPU\_AFFINITY = "0-63"  
LD\_LIBRARY\_PATH =  
    "/home/cpu2017-1.1.8-amd-aocc400-genoa-B1b/amd\_speed\_aocc400\_genoa\_B\_lib  
    /lib:"  
LIBOMP\_NUM\_HIDDEN\_HELPER\_THREADS = "0"  
MALLOC\_CONF = "oversize\_threshold:0,retain:true"  
OMP\_DYNAMIC = "false"  
OMP\_SCHEDULE = "static"  
OMP\_STACKSIZE = "128M"  
OMP\_THREAD\_LIMIT = "64"

Environment variables set by runcpu during the 602.gcc\_s peak run:

GOMP\_CPU\_AFFINITY = "15"

Environment variables set by runcpu during the 605.mcf\_s peak run:

GOMP\_CPU\_AFFINITY = "15"

Environment variables set by runcpu during the 623.xalancbmk\_s peak run:

GOMP\_CPU\_AFFINITY = "15"

Environment variables set by runcpu during the 625.x264\_s peak run:

GOMP\_CPU\_AFFINITY = "15"

Environment variables set by runcpu during the 641.leela\_s peak run:

GOMP\_CPU\_AFFINITY = "15"

Environment variables set by runcpu during the 657.xz\_s peak run:

GOMP\_CPU\_AFFINITY = "0-63"

LIBOMP\_NUM\_HIDDEN\_HELPER\_THREADS = "8"

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)

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## General Notes (Continued)

is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS configuration:

Operating Mode set to Maximum Performance and then set it to Custom Mode

NUMA Nodes per Socket set to NPS4

```
Sysinfo program /home/cpu2017-1.1.8-amd-aocc400-genoa-B1b/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d
running on localhost.localdomain Tue Oct 25 16:56:10 2022
```

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : AMD EPYC 9174F 16-Core Processor
  2 "physical id"s (chips)
  64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 16
  siblings   : 32
  physical 0: cores 0 1 8 9 16 17 24 25 32 33 40 41 48 49 56 57
  physical 1: cores 0 1 8 9 16 17 24 25 32 33 40 41 48 49 56 57
```

From lscpu from util-linux 2.32.1:

```
Architecture:           x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:            Little Endian
CPU(s):                64
On-line CPU(s) list:  0-63
Thread(s) per core:   2
Core(s) per socket:   16
Socket(s):             2
NUMA node(s):          8
Vendor ID:             AuthenticAMD
BIOS Vendor ID:       Advanced Micro Devices, Inc.
CPU family:            25
Model:                 17
Model name:            AMD EPYC 9174F 16-Core Processor
BIOS Model name:       AMD EPYC 9174F 16-Core Processor
```

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## Platform Notes (Continued)

Stepping: 1  
CPU MHz: 4100.000  
CPU max MHz: 4408.2998  
CPU min MHz: 1500.0000  
BogoMIPS: 8187.53  
Virtualization: AMD-V  
L1d cache: 32K  
L1i cache: 32K  
L2 cache: 1024K  
L3 cache: 32768K  
NUMA node0 CPU(s): 0-3,32-35  
NUMA node1 CPU(s): 4-7,36-39  
NUMA node2 CPU(s): 8-11,40-43  
NUMA node3 CPU(s): 12-15,44-47  
NUMA node4 CPU(s): 16-19,48-51  
NUMA node5 CPU(s): 20-23,52-55  
NUMA node6 CPU(s): 24-27,56-59  
NUMA node7 CPU(s): 28-31,60-63  
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr\_opt pdpe1gb rdtscp lm constant\_tsc rep\_good nopl nonstop\_tsc cpuid extd\_apicid aperfmpf perf\_pni pclmulqdq monitor ssse3 fma cx16 pcid sse4\_1 sse4\_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf\_lm cmp\_legacy svm extapic cr8\_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr\_core perfctr\_nb bpext perfctr\_llc mwaitx cpb cat\_13 cdp\_13 invpcid\_single hw\_pstate ssbd mba ibrs ibpb stibp vmmcall fsgsbase bmil avx2 smep bmi2 erms invpcid cqmq rdt\_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd sha\_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqmq\_llc cqmq\_occup\_llc cqmq\_mbm\_total cqmq\_mbm\_local avx512\_bf16 clzero irperf xsaveerptr wbnoinvd amd\_ppin arat npt lbrv svm\_lock nrrip\_save tsc\_scale vmcb\_clean flushbyasid decodeassists pausefilter pfthreshold avic v\_vmsave\_vmlload vgif v\_spec\_ctrl avx512vbmi umip pkus ospke avx512\_vbmi2 gfni vaes vpclmulqdq avx512\_vnni avx512\_bitalg avx512\_vpopcntdq la57 rdpid overflow\_recov succor smca fsrm flush\_lld

/proc/cpuinfo cache data  
cache size : 1024 KB

From numactl --hardware  
WARNING: a numactl 'node' might or might not correspond to a physical chip.  
available: 8 nodes (0-7)  
node 0 cpus: 0 1 2 3 32 33 34 35  
node 0 size: 96381 MB  
node 0 free: 95834 MB  
node 1 cpus: 4 5 6 7 36 37 38 39  
node 1 size: 96712 MB  
node 1 free: 96440 MB  
node 2 cpus: 8 9 10 11 40 41 42 43  
node 2 size: 96754 MB

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## Platform Notes (Continued)

```
node 2 free: 96540 MB
node 3 cpus: 12 13 14 15 44 45 46 47
node 3 size: 96754 MB
node 3 free: 96544 MB
node 4 cpus: 16 17 18 19 48 49 50 51
node 4 size: 96754 MB
node 4 free: 96563 MB
node 5 cpus: 20 21 22 23 52 53 54 55
node 5 size: 96689 MB
node 5 free: 96500 MB
node 6 cpus: 24 25 26 27 56 57 58 59
node 6 size: 96754 MB
node 6 free: 96557 MB
node 7 cpus: 28 29 30 31 60 61 62 63
node 7 size: 96754 MB
node 7 free: 96558 MB
node distances:
node   0   1   2   3   4   5   6   7
  0: 10 12 12 12 32 32 32 32
  1: 12 10 12 12 32 32 32 32
  2: 12 12 10 12 32 32 32 32
  3: 12 12 12 10 32 32 32 32
  4: 32 32 32 32 10 12 12 12
  5: 32 32 32 32 12 10 12 12
  6: 32 32 32 32 12 12 10 12
  7: 32 32 32 32 12 12 12 10
```

From /proc/meminfo

```
MemTotal:      792118756 kB
HugePages_Total:        0
Hugepagesize:     2048 kB
```

```
/sbin/tuned-adm active
  Current active profile: throughput-performance
```

```
/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
  performance
```

From /etc/\*release\* /etc/\*version\*

```
os-release:
  NAME="Red Hat Enterprise Linux"
  VERSION="8.6 (Ootpa)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="8.6"
  PLATFORM_ID="platform:el8"
  PRETTY_NAME="Red Hat Enterprise Linux 8.6 (Ootpa)"
```

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## Platform Notes (Continued)

ANSI\_COLOR="0;31"

redhat-release: Red Hat Enterprise Linux release 8.6 (Ootpa)

system-release: Red Hat Enterprise Linux release 8.6 (Ootpa)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:8::baseos

uname -a:

```
Linux localhost.localdomain 4.18.0-372.9.1.el8.x86_64 #1 SMP Fri Apr 15 22:12:19 EDT
2022 x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):

Not affected

CVE-2018-3620 (L1 Terminal Fault):

Not affected

Microarchitectural Data Sampling:

Not affected

CVE-2017-5754 (Meltdown):

Not affected

CVE-2018-3639 (Speculative Store Bypass):

Mitigation: Speculative Store Bypass disabled via prctl and seccomp

CVE-2017-5753 (Spectre variant 1):

Mitigation: usercopy/swaps barriers and \_\_user pointer sanitization

CVE-2017-5715 (Spectre variant 2):

Mitigation: Retpolines, IBPB: conditional, IBRS\_FW, STIBP: always-on, RSB filling

CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected

CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Oct 25 05:22

SPEC is set to: /home/cpu2017-1.1.8-amd-aocc400-genoa-B1b

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda4	xfs	373G	114G	260G	31%	/home

From /sys/devices/virtual/dmi/id

Vendor:	Lenovo
Product:	ThinkSystem SR665 V3 MB, Genoa, Kauai, DDR5, Kauai, 2U
Product Family:	ThinkSystem
Serial:	1234567890

Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

6x SK Hynix HMCG88AEBRA115N 32 GB 2 rank 4800

18x SK Hynix HMCG88AEBRA168N 32 GB 2 rank 4800

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## Platform Notes (Continued)

BIOS:

BIOS Vendor: Lenovo  
BIOS Version: KAE103A-1.10  
BIOS Date: 09/26/2022  
BIOS Revision: 1.10  
Firmware Revision: 1.0

(End of data from sysinfo program)

## Compiler Version Notes

```
=====
C      | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base,
      | peak) 625.x264_s(base, peak) 657.xz_s(base, peak)
-----
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
  LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
-----

=====
C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak)
      | 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)
-----
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
  LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
-----

=====
Fortran | 648.exchange2_s(base, peak)
-----
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
  LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
-----
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## Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

## Base Portability Flags

600.perlbench\_s: -DSPEC\_LINUX\_X64 -DSPEC\_LP64

602.gcc\_s: -DSPEC\_LP64

605.mcf\_s: -DSPEC\_LP64

620.omnetpp\_s: -DSPEC\_LP64

623.xalancbmk\_s: -DSPEC\_LINUX -DSPEC\_LP64

625.x264\_s: -DSPEC\_LP64

631.deepsjeng\_s: -DSPEC\_LP64

641.leela\_s: -DSPEC\_LP64

648.exchange2\_s: -DSPEC\_LP64

657.xz\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

-Wl,-mllvm -Wl,-reduce-array-computations=3

-Wl,-allow-multiple-definition -O3 -march=znver4 -fveclib=AMDLIBM

-ffast-math -fopenmp -flto -fstruct-layout=7

-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000

-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3

-DSPEC\_OPENMP -zopt -fopenmp=libomp -lomp -lamdlibm -lflang

-lamdalloc

C++ benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver4

-fveclib=AMDLIBM -ffast-math -fopenmp -flto

-mllvm -unroll-threshold=100 -finline-aggressive

-mllvm -loop-unswitch-threshold=200000

-mllvm -reduce-array-computations=3 -DSPEC\_OPENMP -zopt

-fvirtual-function-elimination -fvisibility=hidden -fopenmp=libomp

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## Base Optimization Flags (Continued)

C++ benchmarks (continued):

-lomp -lamdlibm -lflang -lamdalloc-ext

Fortran benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop  
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver4 -fveclib=AMDLIBM  
-ffast-math -fopenmp -flto -mllvm -optimize-strided-mem-cost  
-mllvm -unroll-aggressive -mllvm -unroll-threshold=150 -fopenmp=libomp  
-lomp -lamdlibm -lflang -lamdalloc

## Base Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

## Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

## Peak Portability Flags

Same as Base Portability Flags



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## Lenovo Global Technology

ThinkSystem SR665 V3  
4.10 GHz, AMD EPYC 9174F

SPECspeed®2017\_int\_base = 16.4

SPECspeed®2017\_int\_peak = 16.6

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Oct-2022

Hardware Availability: Dec-2022

Software Availability: Nov-2022

## Peak Optimization Flags

C benchmarks:

600.perlbench\_s: basepeak = yes

602.gcc\_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-allow-multiple-definition -z muldefs -Ofast  
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp  
-flto -fstruct-layout=9 -mllvm -unroll-threshold=50  
-fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -DSPEC\_OPENMP -zopt  
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang

605.mcf\_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-allow-multiple-definition -Ofast -march=znver4  
-fveclib=AMDLIBM -ffast-math -fopenmp -flto  
-fstruct-layout=9 -mllvm -unroll-threshold=50  
-fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -DSPEC\_OPENMP -zopt  
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang

625.x264\_s: Same as 605.mcf\_s

657.xz\_s: Same as 605.mcf\_s

C++ benchmarks:

620.omnetpp\_s: basepeak = yes

623.xalancbmk\_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-do-block-reorder=aggressive -Ofast  
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp  
-flto -finline-aggressive -mllvm -unroll-threshold=100  
-mllvm -reduce-array-computations=3 -DSPEC\_OPENMP -zopt  
-mllvm -do-block-reorder=aggressive  
-fvirtual-function-elimination -fvisibility=hidden  
-fopenmp=libomp -lomp -lamdlibm -lamdalloc-ext -lflang

631.deepsjeng\_s: basepeak = yes

641.leela\_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

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## Peak Optimization Flags (Continued)

641.leela\_s (continued):

```
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -finline-aggressive -mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fvirtual-function-elimination -fvisibility=hidden
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

Fortran benchmarks:

648.exchange2\_s: basepeak = yes

## Peak Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc400-flags.html>

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Genoa-N.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc400-flags.xml>

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Genoa-N.xml>

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Tested with SPEC CPU®2017 v1.1.8 on 2022-10-25 04:56:09-0400.

Report generated on 2022-11-10 14:45:32 by CPU2017 PDF formatter v6442.

Originally published on 2022-11-10.