



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem ST50 V2  
(2.60 GHz, Intel Xeon E-2378)

SPECspeed®2017\_fp\_base = 41.6

SPECspeed®2017\_fp\_peak = 42.3

CPU2017 License: 9017

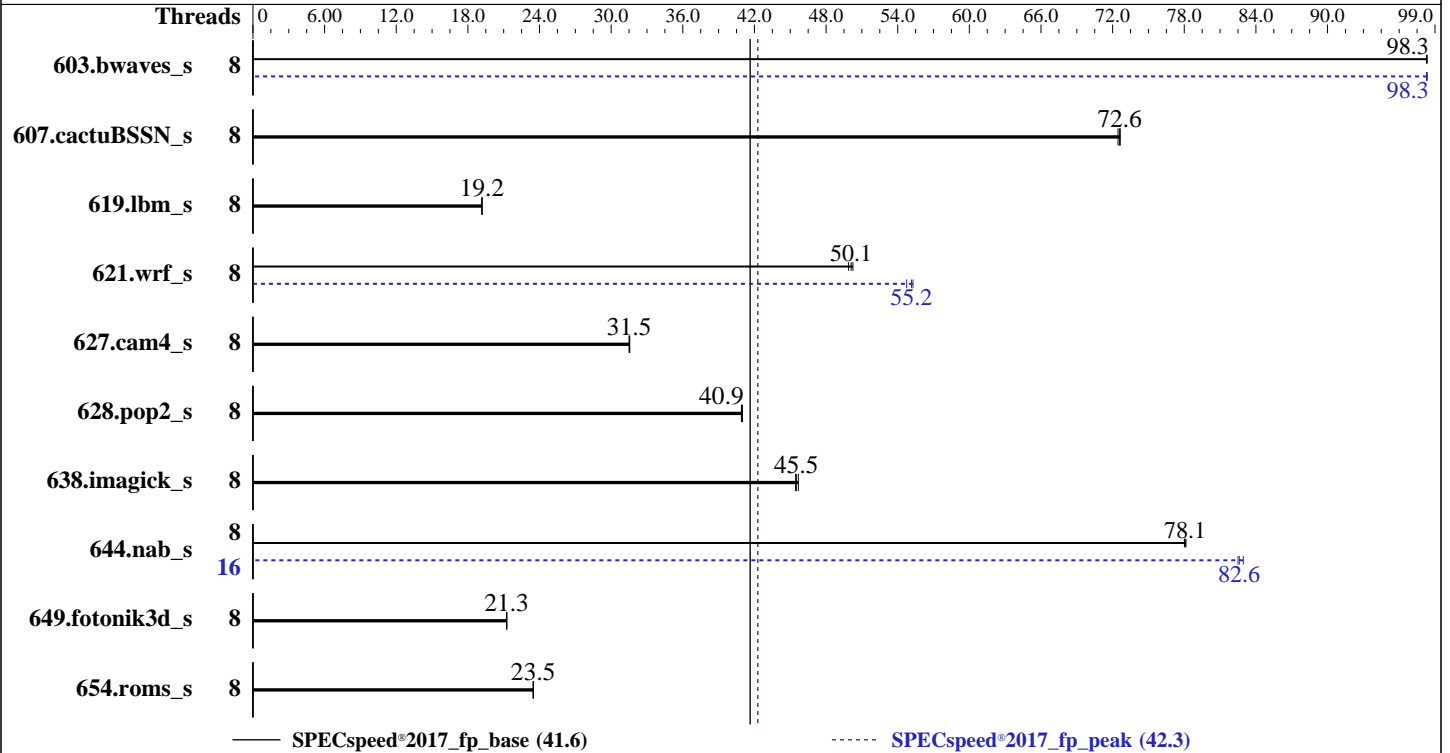
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: May-2022

Hardware Availability: Apr-2022

Software Availability: May-2021



### Hardware

CPU Name: Intel Xeon E-2378  
 Max MHz: 4800  
 Nominal: 2600  
 Enabled: 8 cores, 1 chip, 2 threads/core  
 Orderable: 1 chip  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 512 KB I+D on chip per core  
 L3: 16 MB I+D on chip per chip  
 Other: None  
 Memory: 64 GB (4 x 16 GB 2Rx8 PC4-3200AA-E, running at 2933)  
 Storage: 1 x 960 GB SATA SSD  
 Other: None

### Software

OS: Red Hat Enterprise Linux 8.4 (Ootpa)  
 Kernel 4.18.0-305.el8.x86\_64  
 Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux;  
 Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux;  
 C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux  
 Parallel: Yes  
 Firmware: Lenovo BIOS Version TOE101Q released Mar-2022  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	8	600	98.3	<b>600</b>	<b>98.3</b>	600	98.3	8	600	98.3	<b>600</b>	<b>98.3</b>	600	98.3
607.cactuBSSN_s	8	229	72.6	230	72.5	<b>230</b>	<b>72.6</b>	8	229	72.6	230	72.5	<b>230</b>	<b>72.6</b>
619.lbm_s	8	273	19.2	<b>273</b>	<b>19.2</b>	273	19.2	8	273	19.2	<b>273</b>	<b>19.2</b>	273	19.2
621.wrf_s	8	263	50.3	265	49.9	<b>264</b>	<b>50.1</b>	8	<b>240</b>	<b>55.2</b>	239	55.3	242	54.7
627.cam4_s	8	281	31.6	<b>281</b>	<b>31.5</b>	281	31.5	8	281	31.6	<b>281</b>	<b>31.5</b>	281	31.5
628.pop2_s	8	290	41.0	290	40.9	<b>290</b>	<b>40.9</b>	8	290	41.0	290	40.9	<b>290</b>	<b>40.9</b>
638.imagick_s	8	316	45.7	<b>317</b>	<b>45.5</b>	317	45.4	8	316	45.7	<b>317</b>	<b>45.5</b>	317	45.4
644.nab_s	8	224	78.1	224	78.0	<b>224</b>	<b>78.1</b>	16	211	82.9	212	82.5	<b>212</b>	<b>82.6</b>
649.fotonik3d_s	8	429	21.3	429	21.3	<b>429</b>	<b>21.3</b>	8	429	21.3	429	21.3	<b>429</b>	<b>21.3</b>
654.roms_s	8	<b>671</b>	<b>23.5</b>	670	23.5	671	23.5	8	<b>671</b>	<b>23.5</b>	670	23.5	671	23.5

SPECspeed®2017\_fp\_base = **41.6**

SPECspeed®2017\_fp\_peak = **42.3**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
KMP\_AFFINITY = "granularity=fine,compact,1,0"  
LD\_LIBRARY\_PATH =  
"/home/cpu2017-1.1.8-ic2021.1-revB/lib/intel64:/home/cpu2017-1.1.8-ic2021.1-revB/je5.0.1-64"  
MALLOC\_CONF = "retain:true"  
OMP\_STACKSIZE = "192M"

## General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)

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### General Notes (Continued)

is mitigated in the system as tested and documented.  
jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

### Platform Notes

BIOS configuration:  
C State Support set to C1C3C6

Sysinfo program /home/cpu2017-1.1.8-ic2021.1-revB/bin/sysinfo  
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d  
running on localhost.localdomain Thu May 19 20:55:53 2022

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) E-2378 CPU @ 2.60GHz  
1 "physical id"s (chips)  
16 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following  
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 8  
siblings : 16  
physical 0: cores 0 1 2 3 4 5 6 7

From lscpu from util-linux 2.32.1:  
Architecture: x86\_64  
CPU op-mode(s): 32-bit, 64-bit  
Byte Order: Little Endian  
CPU(s): 16  
On-line CPU(s) list: 0-15  
Thread(s) per core: 2  
Core(s) per socket: 8  
Socket(s): 1  
NUMA node(s): 1  
Vendor ID: GenuineIntel  
BIOS Vendor ID: Intel(R) Corporation  
CPU family: 6  
Model: 167  
Model name: Intel(R) Xeon(R) E-2378 CPU @ 2.60GHz  
BIOS Model name: Intel(R) Xeon(R) E-2378 CPU @ 2.60GHz  
Stepping: 1  
CPU MHz: 4539.653

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### Platform Notes (Continued)

```

CPU max MHz:      4800.0000
CPU min MHz:      800.0000
BogoMIPS:         5184.00
Virtualization:   VT-x
L1d cache:        48K
L1i cache:        32K
L2 cache:         512K
L3 cache:         16384K
NUMA node0 CPU(s): 0-15
Flags:             fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3
sdbg fma cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer
aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb invpcid_single
ssbd ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid ept_ad
fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid mpx avx512f avx512dq rdseed adx
smap avx512ifma clflushopt intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt
xsaves xgetbv1 xsaves dtherm ida arat pln pts hwp hwp_notify hwp_act_window hwp_epp
hwp_pkg_req avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni
avx512_bitalg avx512_vpopcntdq rdpid fsrm md_clear flush_llid arch_capabilities

```

```

/proc/cpuinfo cache data
cache size : 16384 KB

```

```

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 1 nodes (0)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
node 0 size: 64005 MB
node 0 free: 62517 MB
node distances:
node 0
0: 10

```

```

From /proc/meminfo
MemTotal:      65541824 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

/sbin/tuned-adm active
Current active profile: throughput-performance

```

```

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
performance

```

```

/usr/bin/lsb_release -d

```

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### Platform Notes (Continued)

Red Hat Enterprise Linux release 8.4 (Ootpa)

From /etc/\*release\* /etc/\*version\*

```
os-release:
  NAME="Red Hat Enterprise Linux"
  VERSION="8.4 (Ootpa)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="8.4"
  PLATFORM_ID="platform:el8"
  PRETTY_NAME="Red Hat Enterprise Linux 8.4 (Ootpa)"
  ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.4:ga
```

uname -a:

```
Linux localhost.localdomain 4.18.0-305.el8.x86_64 #1 SMP Thu Apr 29 08:54:30 EDT 2021
x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

```
CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store
Bypass disabled via prctl and
seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs
barriers and __user pointer
sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB:
conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected
```

run-level 3 May 19 17:46

SPEC is set to: /home/cpu2017-1.1.8-ic2021.1-revB

```
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-root xfs 889G 24G 865G 3% /
```

```
From /sys/devices/virtual/dmi/id
Vendor: LENOVO
Product: 7D8JCT01WW
Product Family: Lenovo Product
```

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### Platform Notes (Continued)

Serial: J300ST50V2

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

**Memory:**

4x Samsung M391A2K43DB1-CWE 16 GB 2 rank 3200, configured at 2933

**BIOS:**

BIOS Vendor: LENOVO  
BIOS Version: TOE101Q  
BIOS Date: 03/16/2022  
BIOS Revision: 1.41  
Firmware Revision: 1.1

(End of data from sysinfo program)

### Compiler Version Notes

```
=====  
C | 619.lbm_s(base, peak) 638.imagick_s(base, peak)  
  | 644.nab_s(base)
```

```
-----  
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)  
64, Version 2021.1 Build 20201112_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----
```

```
=====  
C | 644.nab_s(peak)
```

```
-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----
```

```
=====  
C | 619.lbm_s(base, peak) 638.imagick_s(base, peak)  
  | 644.nab_s(base)
```

```
-----  
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)  
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Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----
```

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### Compiler Version Notes (Continued)

=====  
C | 644.nab\_s(peak)  
-----

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
C++, C, Fortran | 607.cactuBSSN\_s(base, peak)  
-----

Intel(R) C++ Intel(R) 64 Compiler Classic for applications running on  
Intel(R) 64, Version 2021.1 Build 20201112\_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)  
64, Version 2021.1 Build 20201112\_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on  
Intel(R) 64, Version 2021.1 Build 20201112\_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
Fortran | 603.bwaves\_s(base, peak) 649.fotonik3d\_s(base, peak)  
654.roms\_s(base, peak)

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on  
Intel(R) 64, Version 2021.1 Build 20201112\_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
Fortran, C | 621.wrf\_s(base, peak) 627.cam4\_s(base, peak)  
628.pop2\_s(base, peak)

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on  
Intel(R) 64, Version 2021.1 Build 20201112\_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)  
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## Base Compiler Invocation

C benchmarks:

icc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

ifort icc

Benchmarks using Fortran, C, and C++:

icpc icc ifort

## Base Portability Flags

603.bwaves\_s: -DSPEC\_LP64

607.cactuBSSN\_s: -DSPEC\_LP64

619.lbm\_s: -DSPEC\_LP64

621.wrf\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian

627.cam4\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG

628.pop2\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian

-assume byterecl

638.imagick\_s: -DSPEC\_LP64

644.nab\_s: -DSPEC\_LP64

649.fotonik3d\_s: -DSPEC\_LP64

654.roms\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-m64 -std=c11 -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC\_OPENMP

-mbranches-within-32B-boundaries

Fortran benchmarks:

-m64 -Wl,-z,muldefs -DSPEC\_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div

-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp

-nostandard-realloc-lhs -mbranches-within-32B-boundaries

-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using both Fortran and C:

-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div

-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp

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## Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

```
-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp  
-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc
```

```
644.nab_s: icx
```

Fortran benchmarks:

```
ifort
```

Benchmarks using both Fortran and C:

```
ifort icc
```

Benchmarks using Fortran, C, and C++:

```
icpc icc ifort
```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
619.lbm_s: basepeak = yes
```

```
638.imagick_s: basepeak = yes
```

```
644.nab_s: -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -fiopenmp
```

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## Peak Optimization Flags (Continued)

644.nab\_s (continued):

```
-DSPEC_OPENMP -qopt-mem-layout-trans=4
-fimf-accuracy-bits=14:sqrt
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Fortran benchmarks:

603.bwaves\_s: -m64 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)

```
-DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -ipo -xCORE-AVX2
-O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

649.fotonik3d\_s: basepeak = yes

654.roms\_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf\_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1)

```
-prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

627.cam4\_s: basepeak = yes

628.pop2\_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN\_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-RocketA-B.html>

[http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64\\_revA.html](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.html)

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-RocketA-B.xml>

[http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64\\_revA.xml](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml)



# SPEC CPU®2017 Floating Point Speed Result

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## Lenovo Global Technology

ThinkSystem ST50 V2  
(2.60 GHz, Intel Xeon E-2378)

SPECspeed®2017\_fp\_base = 41.6

SPECspeed®2017\_fp\_peak = 42.3

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** May-2022

**Hardware Availability:** Apr-2022

**Software Availability:** May-2021

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For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.8 on 2022-05-19 08:55:52-0400.  
Report generated on 2022-06-07 15:47:52 by CPU2017 PDF formatter v6442.  
Originally published on 2022-06-07.