



SPEC® CPU2017 Integer Rate Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL560 Gen10

(3.60 GHz, Intel Xeon Platinum 8156)

SPECrate2017_int_base = 111

SPECrate2017_int_peak = Not Run

CPU2017 License: 3

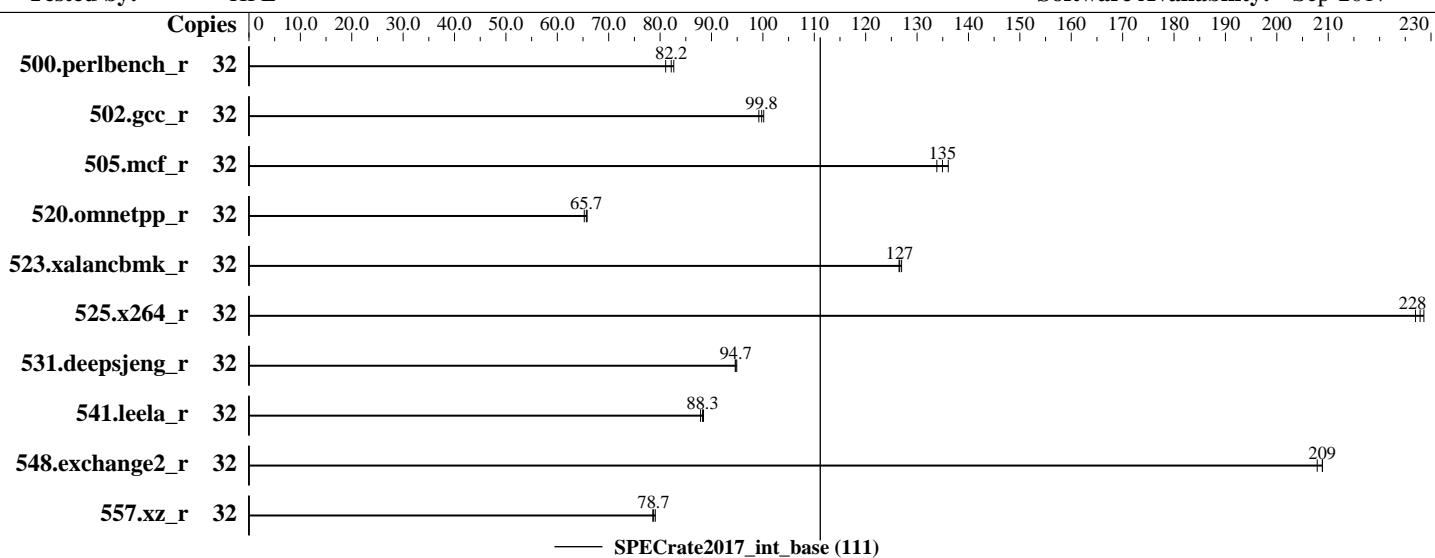
Test Date: Oct-2017

Test Sponsor: HPE

Hardware Availability: Oct-2017

Tested by: HPE

Software Availability: Sep-2017



Hardware

CPU Name: Intel Xeon Platinum 8156
 Max MHz.: 3700
 Nominal: 3600
 Enabled: 16 cores, 4 chips, 2 threads/core
 Orderable: 1, 2, 4 chip(s)
 Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 16.5 MB I+D on chip per chip
 Other: None
 Memory: 768 GB (48 x 16 GB 2Rx8 PC4-2666V-R)
 Storage: 2 x 480 GB SSD SATA, RAID 0
 Other: None

Software

OS: SUSE Linux Enterprise Server 12 (x86_64) SP2
 Compiler: Kernel 4.4.21-69-default
 C/C++: Version 18.0.0.128 of Intel C/C++
 Compiler for Linux;
 Fortran: Version 18.0.0.128 of Intel Fortran
 Compiler for Linux
 Parallel: No
 Firmware: HPE BIOS Version U34 released Oct-2017 (tested with U34 9/29/2017)
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: Not Applicable
 Other: jemalloc: jemalloc memory allocator library V5.0.1;
 jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;
 jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;
 jemalloc: sources available via jemalloc.net



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|-----------------|--------|------------|-------------|------------|-------------|------------|-------------|--------|---------|-------|---------|-------|---------|-------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 500.perlbench_r | 32 | 628 | 81.1 | 616 | 82.7 | 620 | 82.2 | | | | | | | |
| 502.gcc_r | 32 | 452 | 100 | 454 | 99.8 | 457 | 99.2 | | | | | | | |
| 505.mcf_r | 32 | 380 | 136 | 386 | 134 | 383 | 135 | | | | | | | |
| 520.omnetpp_r | 32 | 638 | 65.8 | 640 | 65.7 | 643 | 65.2 | | | | | | | |
| 523.xalancbmk_r | 32 | 266 | 127 | 267 | 126 | 267 | 127 | | | | | | | |
| 525.x264_r | 32 | 245 | 229 | 246 | 228 | 247 | 227 | | | | | | | |
| 531.deepsjeng_r | 32 | 386 | 94.9 | 387 | 94.7 | 388 | 94.6 | | | | | | | |
| 541.leela_r | 32 | 600 | 88.3 | 599 | 88.4 | 603 | 87.9 | | | | | | | |
| 548.exchange2_r | 32 | 401 | 209 | 403 | 208 | 401 | 209 | | | | | | | |
| 557.xz_r | 32 | 437 | 79.1 | 440 | 78.6 | 439 | 78.7 | | | | | | | |

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

sync; echo 3> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

General Notes

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32"

LD_LIBRARY_PATH = "\$LD_LIBRARY_PATH:/home/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4

Platform Notes

BIOS Configuration:

Thermal Configuration set to Maximum Cooling

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| | |
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| CPU2017 License: 3 Test Sponsor: HPE Tested by: HPE | Test Date: Oct-2017 Hardware Availability: Oct-2017 Software Availability: Sep-2017 |

Platform Notes (Continued)

Memory Patrol Scrubbing set to Disabled
LLC Prefetcher set to Enabled
LLC Dead Line Allocation set to Disabled
Stale A to S set to Enabled
Workload Pofile set to Throughput Frequency Compute
Minimum Processor Idle Power Core C-State set to C1E State

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on linux-smfo Wed Oct 18 17:50:22 2017

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8156 CPU @ 3.60GHz
4 "physical id"s (chips)
32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 4
siblings : 8
physical 0: cores 1 5 9 13
physical 1: cores 1 2 5 11
physical 2: cores 1 2 5 11
physical 3: cores 1 5 9 13

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
Thread(s) per core: 2
Core(s) per socket: 4
Socket(s): 4
NUMA node(s): 8
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Platinum 8156 CPU @ 3.60GHz
Stepping: 4
CPU MHz: 3591.556
BogoMIPS: 7183.11
Virtualization: VT-x
L1d cache: 32K

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Platform Notes (Continued)

```

L1i cache: 32K
L2 cache: 1024K
L3 cache: 16896K
NUMA node0 CPU(s): 0,1,16,17
NUMA node1 CPU(s): 2,3,18,19
NUMA node2 CPU(s): 4,5,20,21
NUMA node3 CPU(s): 6,7,22,23
NUMA node4 CPU(s): 8,9,24,25
NUMA node5 CPU(s): 10,11,26,27
NUMA node6 CPU(s): 12,13,28,29
NUMA node7 CPU(s): 14,15,30,31
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpfperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pln pts dtherm intel_pt
tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2
erms invpcid rtm cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd
avx512bw avx512vl xsaveopt xgetbv1 cqm_llc cqm_occup_llc

```

```
/proc/cpuinfo cache data
cache size : 16896 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 8 nodes (0-7)
node 0 cpus: 0 1 16 17
node 0 size: 96350 MB
node 0 free: 96035 MB
node 1 cpus: 2 3 18 19
node 1 size: 96767 MB
node 1 free: 96505 MB
node 2 cpus: 4 5 20 21
node 2 size: 96767 MB
node 2 free: 96484 MB
node 3 cpus: 6 7 22 23
node 3 size: 96767 MB
node 3 free: 96293 MB
node 4 cpus: 8 9 24 25
node 4 size: 96767 MB
node 4 free: 96509 MB
node 5 cpus: 10 11 26 27
node 5 size: 96767 MB
node 5 free: 96510 MB
node 6 cpus: 12 13 28 29
node 6 size: 96767 MB

```

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Platform Notes (Continued)

```
node 6 free: 96529 MB
node 7 cpus: 14 15 30 31
node 7 size: 96766 MB
node 7 free: 96530 MB
node distances:
node   0   1   2   3   4   5   6   7
  0: 10 21 31 31 31 31 31 31
  1: 21 10 31 31 31 31 31 31
  2: 31 31 10 21 31 31 31 31
  3: 31 31 21 10 31 31 31 31
  4: 31 31 31 31 10 21 31 31
  5: 31 31 31 31 21 10 31 31
  6: 31 31 31 31 31 31 10 21
  7: 31 31 31 31 31 31 21 10
```

```
From /proc/meminfo
MemTotal:      792289328 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2
```

```
From /etc/*release* /etc/*version*
SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
    VERSION = 12
    PATCHLEVEL = 2
    # This file is deprecated and will be removed in a future service pack or release.
    # Please check /etc/os-release for details about this release.
os-release:
    NAME="SLES"
    VERSION="12-SP2"
    VERSION_ID="12.2"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp2"
```

```
uname -a:
Linux linux-smfo 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Oct 18 16:13
```

```
SPEC is set to: /home/cpu2017
Filesystem      Type  Size  Used Avail Use% Mounted on
```

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Platform Notes (Continued)

```
/dev/sda4      xfs   852G   18G   834G   3% /home
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U34 09/29/2017

Memory:

```
48x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666
```

(End of data from sysinfo program)

Compiler Version Notes

```
=====
CC 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base)
    557.xz_r(base)
=====
```

```
=====
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
=====
```

```
=====
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
    541.leela_r(base)
=====
```

```
=====
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
=====
```

```
=====
FC 548.exchange2_r(base)
=====
```

```
=====
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
=====
```

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

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Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-fopt-mem-layout-trans=3 -L/usr/local/jet5.0.1-64/lib -ljemalloc

C++ benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-fopt-mem-layout-trans=3 -L/usr/local/jet5.0.1-64/lib -ljemalloc

Fortran benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-fopt-mem-layout-trans=3 -fno-standard-realloc-lhs -falign array32byte
-L/usr/local/jet5.0.1-64/lib -ljemalloc

Base Other Flags

C benchmarks:

-m64 -std=c11

C++ benchmarks:

-m64

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Base Other Flags (Continued)

Fortran benchmarks:

-m64

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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