



# SPEC® CFP2006 Result

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## Lenovo Global Technology

ThinkSystem SR950  
(2.10 GHz, Intel Xeon Platinum 8176)

SPECfp®\_rate2006 = 3440

SPECfp\_rate\_base2006 = 3350

CPU2006 license: 9017

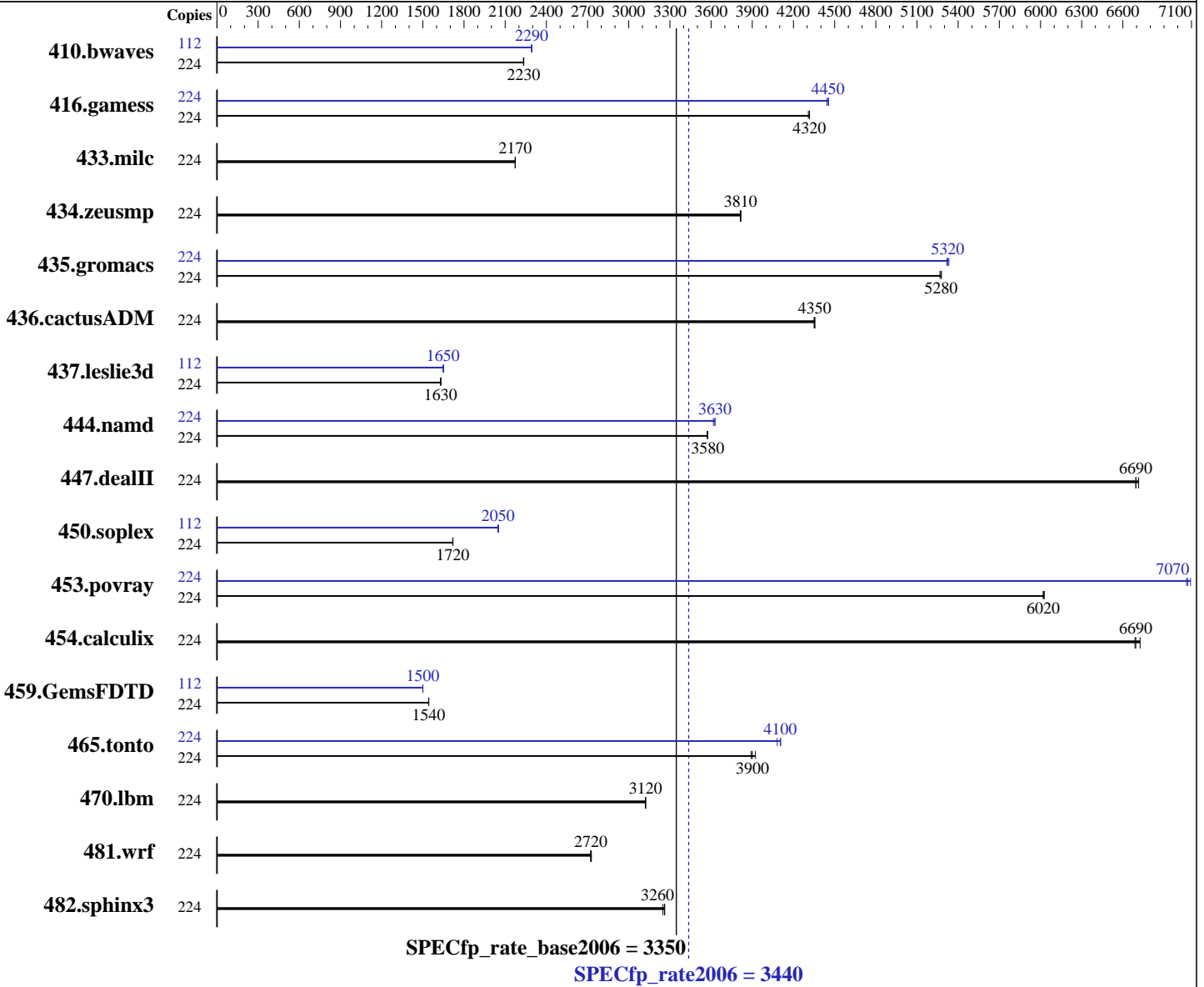
Test sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test date: Nov-2017

Hardware Availability: Sep-2017

Software Availability: Apr-2017



### Hardware

CPU Name: Intel Xeon Platinum 8176  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz  
 CPU MHz: 2100  
 FPU: Integrated  
 CPU(s) enabled: 112 cores, 4 chips, 28 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 12 SP2 (x86\_64)  
 Kernel 4.4.21-69-default  
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux  
 Auto Parallel: Yes  
 File System: tmpfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 38.5 MB I+D on chip per chip  
Other Cache: None  
Memory: 1536 GB (48 x 32 GB 2Rx4 PC4-2666V-R)  
Disk Subsystem: 800 GB tmpfs  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	224	<b><u>1362</u></b>	<b><u>2230</u></b>	1361	2240	1363	2230	112	664	2290	663	2290	<b><u>664</u></b>	<b><u>2290</u></b>
416.gamess	224	<b><u>1016</u></b>	<b><u>4320</u></b>	1018	4310	1016	4320	224	987	4440	<b><u>985</u></b>	<b><u>4450</u></b>	984	4460
433.milc	224	946	2170	947	2170	<b><u>946</u></b>	<b><u>2170</u></b>	224	946	2170	947	2170	<b><u>946</u></b>	<b><u>2170</u></b>
434.zeusmp	224	535	3810	<b><u>534</u></b>	<b><u>3810</u></b>	534	3820	224	535	3810	<b><u>534</u></b>	<b><u>3810</u></b>	534	3820
435.gromacs	224	<b><u>303</u></b>	<b><u>5280</u></b>	303	5280	304	5270	224	<b><u>300</u></b>	<b><u>5320</u></b>	300	5330	301	5320
436.cactusADM	224	615	4350	<b><u>615</u></b>	<b><u>4350</u></b>	614	4360	224	615	4350	<b><u>615</u></b>	<b><u>4350</u></b>	614	4360
437.leslie3d	224	<b><u>1292</u></b>	<b><u>1630</u></b>	1292	1630	1291	1630	112	639	1650	637	1650	<b><u>639</u></b>	<b><u>1650</u></b>
444.namd	224	<b><u>502</u></b>	<b><u>3580</u></b>	503	3570	502	3580	224	495	3630	<b><u>495</u></b>	<b><u>3630</u></b>	497	3620
447.dealII	224	<b><u>383</u></b>	<b><u>6690</u></b>	383	6690	382	6710	224	<b><u>383</u></b>	<b><u>6690</u></b>	383	6690	382	6710
450.soplex	224	1087	1720	<b><u>1087</u></b>	<b><u>1720</u></b>	1087	1720	112	<b><u>456</u></b>	<b><u>2050</u></b>	455	2050	456	2050
453.povray	224	198	6020	198	6030	<b><u>198</u></b>	<b><u>6020</u></b>	224	169	7060	168	7090	<b><u>168</u></b>	<b><u>7070</u></b>
454.calculix	224	275	6730	276	6690	<b><u>276</u></b>	<b><u>6690</u></b>	224	275	6730	276	6690	<b><u>276</u></b>	<b><u>6690</u></b>
459.GemsFDTD	224	<b><u>1540</u></b>	<b><u>1540</u></b>	1540	1540	1541	1540	112	<b><u>792</u></b>	<b><u>1500</u></b>	792	1500	792	1500
465.tonto	224	566	3890	562	3920	<b><u>565</u></b>	<b><u>3900</u></b>	224	<b><u>537</u></b>	<b><u>4100</u></b>	540	4080	537	4110
470.lbm	224	985	3120	<b><u>986</u></b>	<b><u>3120</u></b>	986	3120	224	985	3120	<b><u>986</u></b>	<b><u>3120</u></b>	986	3120
481.wrf	224	<b><u>918</u></b>	<b><u>2720</u></b>	917	2730	919	2720	224	<b><u>918</u></b>	<b><u>2720</u></b>	917	2730	919	2720
482.sphinx3	224	<b><u>1339</u></b>	<b><u>3260</u></b>	1338	3260	1344	3250	224	<b><u>1339</u></b>	<b><u>3260</u></b>	1338	3260	1344	3250

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Tmpfs filesystem can be set with:

```
mount -t tmpfs -o size=800g tmpfs /home
```

Process tuning setting:

```
echo 50000 > /proc/sys/kernel/sched_cfs_bandwidth_slice_us
echo 240000000 > /proc/sys/kernel/sched_latency_ns
echo 5000000 > /proc/sys/kernel/sched_migration_cost_ns
echo 100000000 > /proc/sys/kernel/sched_min_granularity_ns
```

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### Operating System Notes (Continued)

```
echo 150000000 > /proc/sys/kernel/sched_wakeup_granularity_ns
```

### Platform Notes

BIOS configuration:

Choose Operating Mode set to Maximum Performance

SNC set to Enable

DCU Streamer Prefetcher set to Disable

Stale AtoS set to Enable

LLC dead line alloc set to Disable

Sysinfo program /home/cpu2006-1.2-ic17.0u3/config/sysinfo.rev6993

Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)

running on Proton4S-SUSE12SP2 Sat Nov 11 18:54:57 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Platinum 8176 CPU @ 2.10GHZ

4 "physical id"s (chips)

224 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 28

siblings : 56

physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24  
25 26 27 28 29 30

physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24  
25 26 27 28 29 30

physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24  
25 26 27 28 29 30

physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24  
25 26 27 28 29 30

cache size : 39424 KB

From /proc/meminfo

MemTotal: 1584765780 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

SuSE-release:

SUSE Linux Enterprise Server 12 (x86\_64)

VERSION = 12

PATCHLEVEL = 2

# This file is deprecated and will be removed in a future service pack or release.

# Please check /etc/os-release for details about this release.

os-release:

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## Platform Notes (Continued)

```

NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

```

```

uname -a:
Linux Proton4S-SUSE12SP2 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC
2016 (9464f67) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Nov 11 07:23

```

SPEC is set to: /home/cpu2006-1.2-ic17.0u3
Filesystem      Type      Size      Used Avail Use% Mounted on
tmpfs           tmpfs     800G      5.0G   796G   1% /home

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```

BIOS Lenovo -[PSE105X-1.00]- 08/17/2017
Memory:
48x NO DIMM NO DIMM
48x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666 MHz

```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2006-1.2-ic17.0u3/lib/ia32:/home/cpu2006-1.2-ic17.0u3/lib/intel64:/home/cpu2006-1.2-ic17.0u3/sh10.2"

```

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

```

## Base Compiler Invocation

C benchmarks:  
icc -m64

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## Base Compiler Invocation (Continued)

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3



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## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.deallI: -DSPEC\_CPU\_LP64  
 450.soplex: -D\_FILE\_OFFSET\_BITS=64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

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## Peak Optimization Flags (Continued)

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -fno-alias -auto-ilp32  
-qopt-mem-layout-trans=3

447.dealII: basepeak = yes

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -qopt-malloc-options=3  
-qopt-mem-layout-trans=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -qopt-mem-layout-trans=3

### Fortran benchmarks:

410.bwaves: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: Same as 410.bwaves

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -auto -inline-calloc  
-qopt-malloc-options=3

### Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -qopt-prefetch -auto-ilp32  
-qopt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-E.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-E.xml>

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Tested with SPEC CPU2006 v1.2.  
Report generated on Fri Dec 15 12:20:35 2017 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 15 December 2017.