



# SPEC® CINT2006 Result

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Dell Inc.

SPECint®2006 = 66.9

PowerEdge M640 (Intel Xeon Gold 5120, 2.20 GHz)

SPECint\_base2006 = 64.0

CPU2006 license: 55

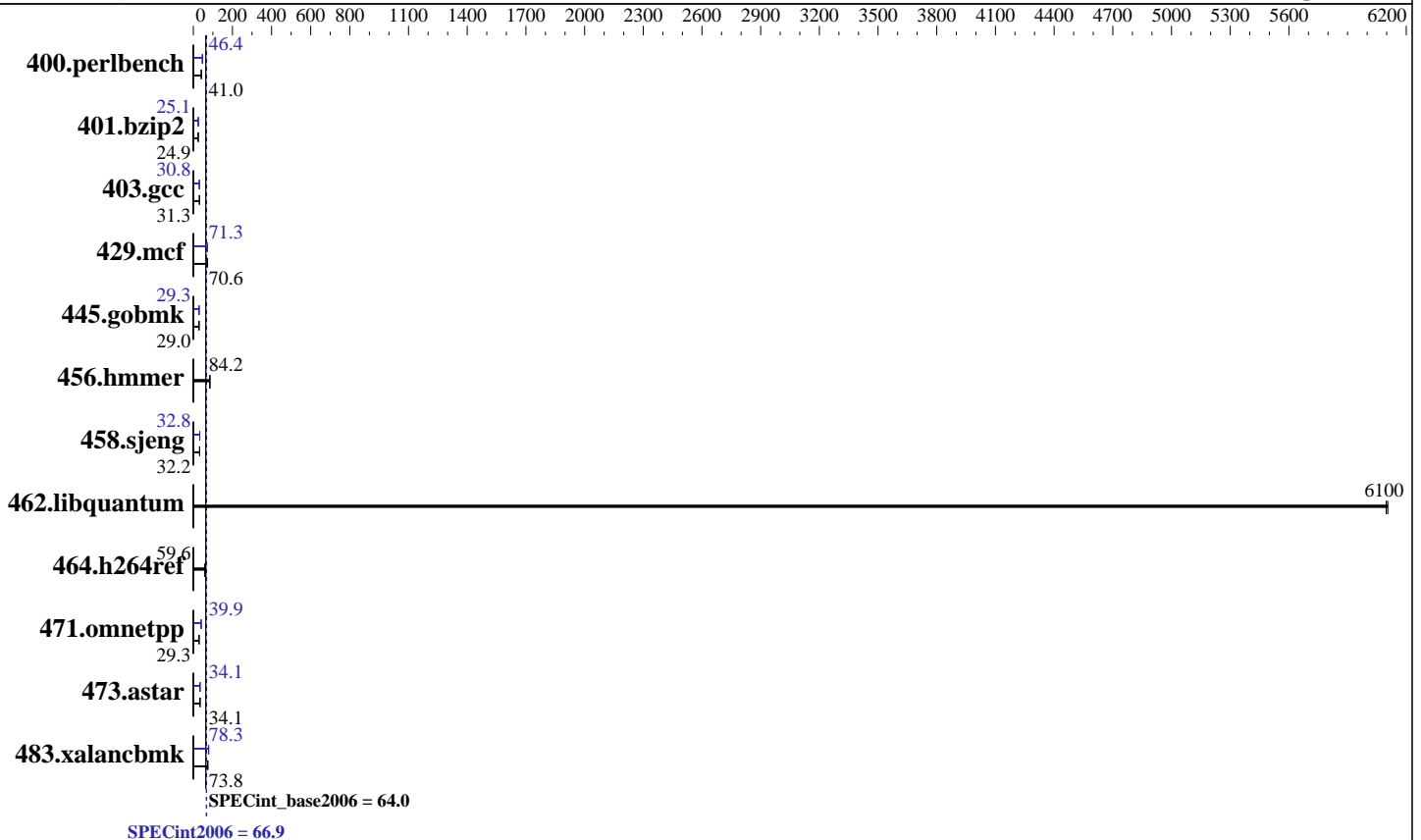
Test date: Jul-2017

Test sponsor: Dell Inc.

Hardware Availability: Sep-2017

Tested by: Dell Inc.

Software Availability: Apr-2017



## Hardware

CPU Name: Intel Xeon Gold 5120  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core  
 L3 Cache: 19.25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R, running at 2400 MT/s)  
 Disk Subsystem: 1 x 960 GB SATA SSD  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 12 SP2  
 4.4.16-56-default  
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++  
 Compiler for Linux  
 Auto Parallel: Yes  
 File System: btrfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	238	41.0	<u>238</u>	<u>41.0</u>	241	40.5	<u>211</u>	<u>46.4</u>	211	46.4	211	46.3
401.bzip2	<u>387</u>	<u>24.9</u>	388	24.9	387	24.9	384	25.2	385	25.1	<u>385</u>	<u>25.1</u>
403.gcc	<u>257</u>	<u>31.3</u>	257	31.3	258	31.2	258	31.2	264	30.5	<u>261</u>	<u>30.8</u>
429.mcf	<u>129</u>	<u>70.6</u>	125	73.0	130	70.1	128	71.3	127	71.6	<u>128</u>	<u>71.3</u>
445.gobmk	362	29.0	<u>362</u>	<u>29.0</u>	362	29.0	<u>358</u>	<u>29.3</u>	358	29.3	358	29.3
456.hmmer	111	84.1	<u>111</u>	<u>84.2</u>	110	84.8	111	84.1	<u>111</u>	<u>84.2</u>	110	84.8
458.sjeng	376	32.2	<u>376</u>	<u>32.2</u>	376	32.2	369	32.8	<u>369</u>	<u>32.8</u>	369	32.8
462.libquantum	3.40	6100	3.39	6110	<u>3.40</u>	<u>6100</u>	3.40	6100	3.39	6110	<u>3.40</u>	<u>6100</u>
464.h264ref	<u>371</u>	<u>59.6</u>	371	59.6	372	59.5	<u>371</u>	<u>59.6</u>	371	59.6	372	59.5
471.omnetpp	<u>213</u>	<u>29.3</u>	212	29.4	215	29.0	157	39.9	<u>157</u>	<u>39.9</u>	157	39.8
473.astar	205	34.2	206	34.0	<u>206</u>	<u>34.1</u>	206	34.1	206	34.1	<u>206</u>	<u>34.1</u>
483.xalancbmk	<u>93.5</u>	<u>73.8</u>	93.4	73.8	93.7	73.6	88.3	78.2	<u>88.1</u>	<u>78.3</u>	88.0	78.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS settings:  
 Sub NUMA Cluster disabled  
 Virtualization Technology disabled  
 System Profile set to Custom  
 CPU Performance set to Maximum Performance  
 C States set to Autonomous  
 C1E disabled  
 Energy Efficient Turbo disabled  
 Uncore Frequency set to Dynamic  
 Energy Efficiency Policy set to Performance  
 Memory Patrol Scrub disabled  
 Logical Processor enabled  
 CPU Interconnect Bus Link Power Management disabled  
 PCI ASPM L1 Link Power Management disabled  
 Sysinfo program /root/cpu2006-1.2\_ic17u3/config/sysinfo.rev6993  
 Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
 running on linux-u8yg Sat Jul 29 01:07:50 2017

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## Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5120 CPU @ 2.20GHz
 2 "physical id"s (chips)
 56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 14
  siblings  : 28
  physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
  physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 19712 KB

```

```

From /proc/meminfo
MemTotal:      196687636 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

```

```

uname -a:
Linux linux-u8yg 4.4.16-56-default #1 SMP Mon Aug 8 14:24:26 UTC 2016
(5b281a8) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Jul 27 07:20

```

SPEC is set to: /root/cpu2006-1.2_ic17u3
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal       btrfs 921G  230G  687G  26% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program  
Continued on next page



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## Platform Notes (Continued)

reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 0.4.3 07/21/2017

Memory:

12x 00AD00B300AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666 MHz, configured at 2400 MHz

4x Not Specified Not Specified

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/root/cpu2006-1.2\_icl7u3/lib/ia32:/root/cpu2006-1.2\_icl7u3/lib/intel64:/root/cpu2006-1.2\_icl7u3/sh10.2"

OMP\_NUM\_THREADS = "28"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled by default.

Filesystem page cache cleared with:

shell invocation of 'sync; echo 3 > /proc/sys/vm/drop\_caches' prior to run

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -DSPEC\_CPU\_LP64

456.hmmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

471.omnetpp: -DSPEC\_CPU\_LP64

473.astar: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch  
-auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh10.2 -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

445.gobmk: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

C++ benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -D\_FILE\_OFFSET\_BITS=64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

```

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

```

## Peak Optimization Flags

C benchmarks:

```

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
               -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -qopt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
            -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div -auto-ilp32 -qopt-prefetch

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
          -qopt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
          -qopt-prefetch -auto-p32

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
            -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2)

456.hmmer: basepeak = yes

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
            -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

```

C++ benchmarks:

```

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
              -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -qopt-ra-region-strategy=block
              -Wl,-z,muldefs -L/sh10.2 -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
            -auto-p32 -Wl,-z,muldefs -L/sh10.2 -lsmartheap64

```

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## Peak Optimization Flags (Continued)

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-Wl,-z,muldefs -L/sh10.2 -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revC.xml>

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