



# SPEC® CINT2006 Result

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Dell Inc.

SPECint®\_rate2006 = 2090

PowerEdge R640 (Intel Xeon Gold 6152, 2.10 GHz)

SPECint\_rate\_base2006 = 1990

CPU2006 license: 55

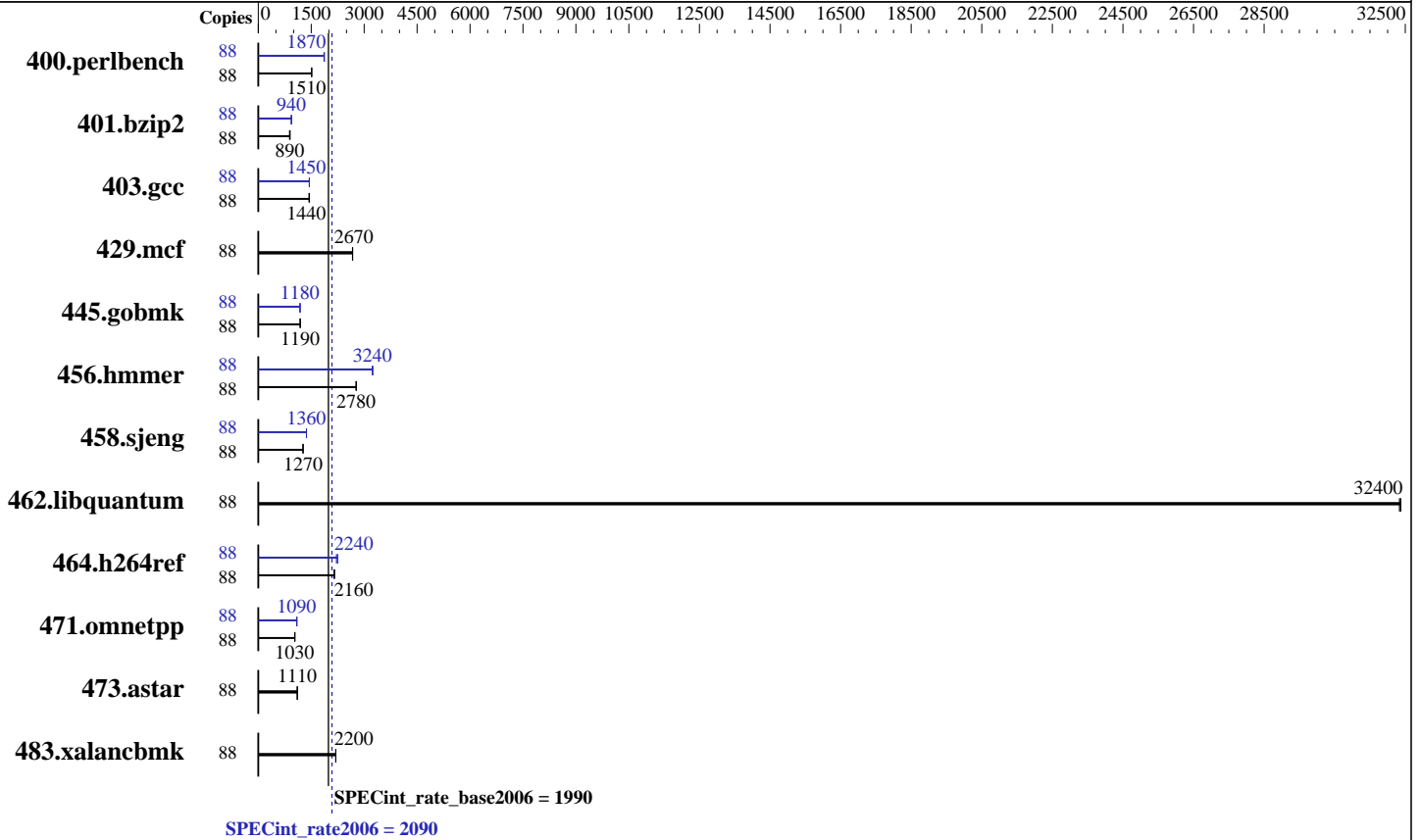
Test date: May-2017

Test sponsor: Dell Inc.

Hardware Availability: Jul-2017

Tested by: Dell Inc.

Software Availability: Nov-2016



## Hardware

CPU Name: Intel Xeon Gold 6152  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 2100  
 FPU: Integrated  
 CPU(s) enabled: 44 cores, 2 chips, 22 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core  
 L3 Cache: 30.25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R)  
 Disk Subsystem: 1 x 400 GB SATA SSD  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 12 SP2  
 4.4.21-69-default  
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++  
 Compiler for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	88	568	1510	<u>569</u>	<u>1510</u>	569	1510	88	462	1860	459	1870	<u>460</u>	<u>1870</u>
401.bzip2	88	941	903	<u>954</u>	<u>890</u>	958	887	88	<u>904</u>	<u>940</u>	908	935	903	940
403.gcc	88	492	1440	489	1450	<u>492</u>	<u>1440</u>	88	489	1450	<u>490</u>	<u>1450</u>	491	1440
429.mcf	88	<u>300</u>	<u>2670</u>	300	2670	301	2670	88	<u>300</u>	<u>2670</u>	300	2670	301	2670
445.gobmk	88	776	1190	777	1190	<u>777</u>	<u>1190</u>	88	781	1180	<u>780</u>	<u>1180</u>	780	1180
456.hammer	88	296	2770	295	2780	<u>296</u>	<u>2780</u>	88	254	3230	253	3240	<u>254</u>	<u>3240</u>
458.sjeng	88	839	1270	839	1270	<u>839</u>	<u>1270</u>	88	779	1370	<u>780</u>	<u>1360</u>	780	1360
462.libquantum	88	56.4	32300	56.3	32400	<u>56.3</u>	<u>32400</u>	88	56.4	32300	56.3	32400	<u>56.3</u>	<u>32400</u>
464.h264ref	88	903	2160	<u>904</u>	<u>2160</u>	905	2150	88	880	2210	867	2250	<u>867</u>	<u>2240</u>
471.omnetpp	88	<u>531</u>	<u>1030</u>	531	1030	531	1040	88	503	1090	504	1090	<u>504</u>	<u>1090</u>
473.astar	88	560	1100	<u>558</u>	<u>1110</u>	558	1110	88	560	1100	<u>558</u>	<u>1110</u>	558	1110
483.xalancbmk	88	276	2200	<u>276</u>	<u>2200</u>	277	2200	88	276	2200	<u>276</u>	<u>2200</u>	277	2200

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS settings:  
 Sub NUMA Cluster enabled  
 Virtualization Technology disabled  
 System Profile set to Custom  
 CPU Performance set to Maximum Performance  
 C States set to autonomous  
 C1E disabled  
 Uncore Frequency set to Dynamic  
 Energy Efficiency Policy set to Performance  
 Memory Patrol Scrub disabled  
 Logical Processor enabled  
 CPU Interconnect Bus Link Power Management disabled  
 PCI ASPM L1 Link Power Management disabled  
 Sysinfo program /root/cpu2006-1.2\_ic17u3/config/sysinfo.rev6993  
 Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
 running on linux-xoj4 Fri May 12 23:52:23 2017

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## Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name      : Intel(R) Xeon(R) Gold 6152 CPU @ 2.10GHz
 2 "physical id"s (chips)
 88 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores    : 22
  siblings     : 44
  physical 0:  cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
  physical 1:  cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
cache size     : 30976 KB

```

```

From /proc/meminfo
MemTotal:      394732016 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

```

```

uname -a:
Linux linux-xoj4 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 May 12 23:51

```

SPEC is set to: /root/cpu2006-1.2_ic17u3
Filesystem      Type  Size  Used Avail Use% Mounted on

```

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## Platform Notes (Continued)

/dev/sda2 xfs 369G 9.4G 360G 3% /  
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 0.5.5 04/27/2017

Memory:

20x 00AD00B300AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666 MHz  
4x 00AD063200AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/root/cpu2006-1.2\_ic17u3/lib/ia32:/root/cpu2006-1.2\_ic17u3/lib/intel64:/root/cpu2006-1.2\_ic17u3/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled by default

Filesystem page cache cleared with:

shell invocation of 'sync; echo 3 > /proc/sys/vm/drop\_caches' prior to run  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

## Base Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -D\_FILE\_OFFSET\_BITS=64  
403.gcc: -D\_FILE\_OFFSET\_BITS=64  
429.mcf: -D\_FILE\_OFFSET\_BITS=64  
445.gobmk: -D\_FILE\_OFFSET\_BITS=64  
456.hmmer: -D\_FILE\_OFFSET\_BITS=64  
458.sjeng: -D\_FILE\_OFFSET\_BITS=64  
462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

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## Base Portability Flags (Continued)

464.h264ref: -D\_FILE\_OFFSET\_BITS=64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -D\_FILE\_OFFSET\_BITS=64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-qopt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh10.2 -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

```

403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.aster: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

```

## Peak Optimization Flags

C benchmarks:

```

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
               -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -auto-ilp32 -qopt-mem-layout-trans=3

```

```

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
            -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -qopt-prefetch -auto-ilp32
            -qopt-mem-layout-trans=3

```

```

403.gcc: -xCORE-AVX512 -ipo -O3 -no-prec-div
         -qopt-mem-layout-trans=3

```

429.mcf: basepeak = yes

```

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
            -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -qopt-mem-layout-trans=3

```

```

456.hmmer: -xCORE-AVX512 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
           -qopt-mem-layout-trans=3

```

```

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
            -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -unroll4 -auto-ilp32
            -qopt-mem-layout-trans=3

```

462.libquantum: basepeak = yes

```

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
              -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -unroll2 -qopt-mem-layout-trans=3

```

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## Peak Optimization Flags (Continued)

C++ benchmarks:

```
471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
             -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2)
             -qopt-ra-region-strategy=block
             -qopt-mem-layout-trans=3 -Wl,-z,muldefs
             -L/sh10.2 -lsmartheap
```

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revC.xml>

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